September 3, 2010

Minutes of IEEE 1149.1 - Initialize Sub-Group Meeting

Attendees:

Carl Barnhart
CJ Clark
Ted Eaton
Roland Latvala
Ken Parker
Carol Pyron
Francisco Russi
Brian Turmelle

Minutes:

This meeting started with discussion of what work remained:

- The long example from Carol needs to be reworked to match the latest BNF. (Carol, Carl, CJ, Ken)
- The INIT normative and descriptive text needs to be incorporated in the Standard. (CJ)
- The latest BNF needs to be confirmed and incorporated in the BSDL Annex. (CJ)
- We need to formalize the side-file syntax and semantics (PDL). (CJ is working on incorporating the PDL in a new Annex.)

Ken questioned whether there is a need of a standard side file, since the mnemonics and register fields give enough information for an ATPG-support GUI to present the options to a user and the results could be passed in any format the ATPG tool desired. Two reasons were presented: documentation; and due to the disaggregation of the industry, the board designer (who would have to create the side file) and the board tester who would have to use it may be in different companies on opposite sides of the earth. Ken withdrew the question.

Ted asked if we should include the PDL support for clocks. The consensus was no.

CJ reported that the compiler work using the new BNF was still in progress, but no problems had been found yet.

CJ intends to have his dot-1 meeting on the 7th, but will skip the 14th due to the P1687 face-to-face meeting.

Roland needs some meeting time for his analog designer to ask some questions.

The meeting ended early.

Current Status:

Formalize Rules – In progress.

BSDL Constructs - - BNF coding in progress, semantic checks in progress.

Formalize PDL constructs - We need to start on this.

Actions:

CJ will provide an example of a dynamic register for discussion.

Work still to be done:

Formalize side-file language. Incorporate INIT into 1149.1 Std.

Next meeting date: Same time next Friday September 10th.