

Date – 05/19/2014

Attendees: CJ Clark, Adam Ley, Bill Tuthill, Bob Gottlieb, Brian Turmelle, Craig Stephan, Dharma Konda, Ismed Hartanto, Jon Colburn, Josh Ferry, Marc Hutner, Steve Sunter, Tapan J Chakraborty,

Absent with Excuse : Frans de Jong, Philippe Lebourg,
Not Present for $\frac{3}{4}$ of meeting:

Missing: Bill Huott, Carol Pyron, Jim Wilson, Kent Ng, Kevin Gorman, Saman Adham, Tom Waayers, Heiko Ehrenburg, Dave Armstrong, Roger Sowada, Dwayne Burek, Gobinathan Athimolom, Gurgun Harutyunyan, Mike Ricchetti, Teresa McLaurin, Zahi Abuhanmdeh

Agenda:

- 1) Patent Slide
- 2) Motion to accept Clause 6.6 of version 16 of the draft
(Scan packet definition and explanation)
 - a.) Discussion
- 3) New Business
- 4) Motion to Adjourn

Meeting Called to order at 11:03 am EDT

Minutes:

Review Patent Slide – Slide Presented to the Group.

Solicited input from anybody who is aware of patents that might read on our standard.

No Response

Discussion on Section 6.6

Review of comments on drafts that were sent out to reflector from Josh and Marc

CJ made corrections that Josh had found and posted those corrections

Discussion on number of channels needed

4080 scan channels per packet

1,040,400 scan channels per PEDDA

Tapan – typical numbers is smaller than this so the 4080 would be sufficient.

CJ – you might get to these numbers if you don't use compression.

Tapan – is scan channel defined in the terminology

CJ- may not be defined. Will add definition

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CJ – are we in a position to accept what we have in 6.6 in terms of accurately explaining how to form a scan packet?

Bob – Agrees with the way we are doing it but need to go back to the examples and double check.

CJ – can we accept what we have? Subject to further review?

Bob – is fine with it

Tapan – as long as you keep the room to modify it.

CJ – always open to modification if any issues pop up.

Jon – the only purpose of IR is for instructions that don't have a DR associated with them?

CJ – no you have some test data registers with modes associated with them.

CJ – instruction register exists to set a mode. So we need to load the instruction register, where applicable.

CJ – example – 2 test data registers INIT and Boundary Register. Boundary Register has multiple instructions that use the Boundary Register.

Bob – so when we specify EXTEST (or whatever) when we put that in, how are we specifying the rest of the fields.. DR Chain is what?

Bob – would always have some scan channel that is associated with the IR and use that.

CJ – it's a scan channel (the IR). Not forced to be zero but would be a number.

CJ – there are certain TDR that have more than one Instruction and why you need it.

Tapan – do we have a sense how big the PEDAA will be in terms of bit count?

CJ – expecting PEDDA to be a decompressor in terms of size. Maybe 800 flops.

Tapan – could be multiple PEDDAs per chip

CJ – yes. Not limited in the spec

CJ – can distribute the PEDDA architecture. Multiple PEDDAs could be put in different locations on the chip to ease routing.

Tapan – timing (routing) a concern?

CJ – will have same issues with Scan Chains now. Still need to do timing closure

Bob – wouldn't think distributing PEDDA logic would be feasible. Maybe distributing multiple PEDDAs would work.

CJ – No specific rules saying where the blocks need to be. So what would work for routing?

Tapan – would like another week to review.

CJ – can we make the first order of business of the next meeting to make a motion to accept the section?

Bob- yes. That would be good

Tapan – are there any pending changes to be put in?

CJ – there was one change that we made during the meeting so can put the on the web site.

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Call for New Business

No new business

Please use reflector to review what is in the Draft.

Please send comments to reflector.

Anything that needs to be fixed. Or comments on State Diagram.

Motion to Adjourn: Josh

Seconded: Bob

Meeting adjourned: 11:56 pm EDT

Next Meeting:

June 9th, 2014 11:00am

Motion Summary

0 motions made

Action Items

~~*Bill Tuthill – 10-21-2013 – Add minutes and Attendance spreadsheet to the website.*~~

~~*CJ – 11-11-2013 – Reach out to ATE industry and Probe Industry to get update on future of ATE equipment to see which data speeds and protocols they are heading towards.*~~

Philippe – Look into alternative method to create control information (pause, start, terminate, etc.) rather than using K characters in packet.

Bob – create a case study to show use of Attributes

Frans – will start some block diagrams of a simple use case to help illustrate the current architecture

~~*Dwayne – present to the group his ideas for a simplified scheme – Direct Interface.*~~

NOTES:

1149.10 working group website - <http://grouper.ieee.org/groups/1149/10/>

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