## 1450.4 meeting minutes – 01/16/13

Attendees: Ernie Wahl, Jim O'Reilly, Mitsuo Fujii

Not present: Julia DiChiaro, Oleg Erlich, Markus Seuring, Ajay Khoche, Paul Reuter

## **Summary:**

Line numbers are with regard to syntax document dated January 15, 2013

- Line 707: Add statement "Standard and user-defined TestType and FlowType names shall not be used as identifiers." (to address the concerns raised by STIL-CAST SEMI Working Group (SCSWG)).
- Line 521: move Revision History table to separate file
- Line 747 751: add description:
  - o Information related to a specific tester, such as signal to tester-channel mapping, is found in a Tester subblock of which there may be more than one. A tester is deemed to have one or more test-heads. Each testhead may be divided into one or more partitions. Each partition may run a different test-program (multi-DUT) and control a subset of the test-head channels. A partition may test one or more devices (multi-site).
- Line 776: remove plus sign indicating multiple signal maps to comport with revised description on line 972
- Line 868: sync up with spice: use L instead of I to represent inductors
- Line 972: revise description:
  - a signal map per Sites block may be defined. It shall map every signal listed in the associated Signals block to a tester-channel. The global unnamed Signals block is used by default. The Signals statement under the Device block may be used to select a named Signals block, required for multi-DUT testing.
- Line 1567: flow-node name qualifications
- Line 1658: accept exit-port qualifications:
  - The port label, required and arbitrary, shall be unique within ExitPorts context. To guarantee an explicit exit path, the last port's Boolean expressions shall be True.
- Line 1707: Table 6: change "Post Port Actions" to "Branching Actions" and solidify action syntax
- Line 1707: Recover unary\_ bin\_expr | multi\_bin\_expr syntax (from the most recent version before P1450.4\_D00.doc: P1450.4-D35-syntax-summary\_08\_04\_2010.doc)
- Line 2798, 2830, 3000 Table 15: add eval
- Line 3074: remove "All" as part of the bin specification statement
- ADD TO ISSUES:
  - o Reserved in STIL.0/2 also reserved in STIL.4?
  - o Example single letter reserved, other than Spec reserved?

Reference documents (If logged into your google account, can edit. If not, can only view.)

- Current Draft Syntax Document
- <u>Issues List</u>
- Namespace resolution examples document
- <u>Communications spread sheet</u> (scratchpad spreadsheet)
- <u>Communications word doc</u> (scratchpad word document)

**Next meeting:** 01/23/13

For reference STIL .4 information can be found at the IEEE STIL website: <a href="http://grouper.ieee.org/groups/1450/">http://grouper.ieee.org/groups/1450/</a> (select the <a href="http://grouper.ieee.org/groups/1450/dot4/index.html">http://grouper.ieee.org/groups/1450/dot4/index.html</a>