

## 1450.4 meeting minutes – 02/13/13

**Attendees:** Ernie Wahl, Jim O'Reilly, Mitsuo Fujii

**Not present:** Julia DiChiaro, Oleg Erlich, Markus Seuring, Ajay Khoche, Paul Reuter

### Summary:

Line numbers are from syntax document dated February 12, 2013

- Line 763 - 765:

```
(Design {  
    (SIG_NAME { (sig_attribute)* }) * // Why duplicate signals? Remove  
Design, augment Signals syntax.  
})
```

- Problem: may be incongruent with Signals block and offers no additional value
- Solution: remove Design block and add (sig\_attribute)\* to each signal in Signals block

- Line 800: (Power PWR\_RAIL\_NAME (,PWR\_RAIL\_NAME);)
  - Problem: override buffer library power voltage on a per signal (buffer instance) basis
  - Solution: power rail name becomes synonymous with power signal name

PWR\_RAIL\_NAME shall be a reference to a previously defined signal of type "Supply":

```
(Power PWR_SIG_NAME (,PWR_SIG_NAME);)
```

Signals of type "Supply" shall optionally specify a power override value via: Power VOLTS;

- Rationale: theoretically all pads with the the same signal name are at an equi-potential point. For power signals, that implies they are all connected to the same rail on the chip. Analog and Digital signals may then be associated with different power rails/signals on a per pad instance basis. Syntax already allows for multiple pad coordinates per signal as required by signals of type "Supply".

**Reference documents** (If logged into your google account, can edit. If not, can only view.)

- [Current Draft Syntax Document](#)
- [Issues List](#)
- [Namespace resolution examples document](#)
- [Communications spread sheet](#) (scratchpad spreadsheet)
- [Communications word doc](#) (scratchpad word document)

**Next meeting:** 02/20/13

For reference STIL .4 information can be found at the IEEE STIL website: <http://grouper.ieee.org/groups/1450/>  
(select the [P1450.4](#) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>