

1450.4 meeting minutes – 02/29/12

Attendees: Herbert Eichinger, Oleg Erlich, Jim O'Reilly, Ernie Wahl

Not present: Markus Seuring, Ajay Khoche, Paul Reuter, Julia DiChiaro

Summary:

Line numbers are with regard to syntax document dated Feb 21, 2012.

- CONSENSUS: Line 1262: add While statement to process N results emanating from TestExec
- CONSENSUS: Line 1058, 1126: add inherited Parameters, etc., to TestType & FlowType syntax description
- ACTION ITEM Oleg: multi-limits Teradyne code example (Limits/TestName/TestNumber)
- ACTION ITEM Jim: multi-limits Advantest code example (Limits/TestName/TestNumber)
- SignalMap:
 - SUGGESTION: signal map per tester/tpg (currently using STIL.1 name map)
 - SUGGESTION: per site, provide check when two site signals share 1 channel

Actions:

- Jim to rework Verigy example from STIL paper so that the STIL translation is consistent with its original Verigy code.

Reference documents (If logged into your google account, can edit. If not, can only view.)

- [Current Draft Syntax Document](#)
- [Issues List](#)
- [Namespace resolution examples document](#)
- [Communications spread sheet](#) (scratchpad spreadsheet)
- [Communications word doc](#) (scratchpad word document)

Next meeting: 03/07/12

For reference STIL .4 information can be found at the IEEE STIL website: <http://grouper.ieee.org/groups/1450/> (select the [P1450.4](#) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>