1450.4 meeting minutes - 03/25/10

Attendees: Bruce Parnas, Jim O'Reilly, Ernie Wahl

Not present: Markus Seuring, Ajay Khoche, Greg Maston

Agenda:

- IEEE Meeting Preamble (No discussion of proprietary information).
- Minutes from last two weeks are now on the web.
- Discussion items.
 - o Next steps.
 - Let's make a list of everything we need to do to get a draft ballot document prepared. We won't discuss any of these in detail, just list the items and decide which ones are musts
 - Tester resource to signal mapping.
 - Axis block. Need for searches and shmoos.
 - For flows, use trees with nested flows (a la Verigy SmarTest flow code). We can represent that, but want to make P1450.4 syntax intuitive for representing these structures.
 - Ernie to provide a list of issues he's been keeping, and forward to the WG a list of issues still open.
 - Definition of functional test (two types one type with single PatternExec parameter, and another with the components of PatternExec as parameters).
 - Do we need to define a required set of TestProgram global variables? If so, what should these variables be?
 - Scoping of variables, and potential conflicts of names need to specify this in more detail.
 - Dot3 extension legal characters for variable names, length of variable names, and reserved words for specific tester languages.
- Open issues are there other open issues that should be considered? A review of the open issues list can guide us here.
 - Issues list:
 http://spreadsheets.google.com/ccc?key=0AoKiPr1I9LY9dF95dkhSTVVqOU5GbWJyWFNhY0JPX0E&hl=en
 - Namespace resolution examples document: http://docs.google.com/Doc?docid=0AYKiPr1I9LY9ZGY4dmNjNTNfMGZkOGJ2bmZ y&hl=en
 - o If logged into your google account, can edit. If not, can only view.
- Next Meeting 04/01/10.