

1450.4 meeting minutes – 04/01/14

Attendees: Ernie Wahl, Jim O'Reilly, Paul Reuter, Markus Seuring, Ric Dokken, Mitsuo Fujii,

Not present: Oleg Erlich, Julia DiChiaro, Ajay Khoche

Agenda:

- Review section 6.11.2.8 PatternExec and section 6.10.6 requirements for passing it to StdPatternExec
- Consider section 6.5.3 ChannelMap syntax to include 2+ pin, plane, or pad names (relate to Package pin/plane and Signals pad-number information). This restores previous syntax capability but adds pad numbers and top-level context. Also, new syntax introduces previously non-existing possibility of conflicts:
 - ChannelMap may conflict with Signals block regarding pad numbers.
 - Per tester-configuration Signal-to-Package-pin/plane mappings may disagree.
- Review proposed ChannelMap/Device block changes and integrate descriptive text. Review location in skeleton and potential dependencies.
- Define a “STIL.4 compliant” tool as supporting all or a subset of STIL.4 syntax, provided the supported syntax complies with STIL.4 specifications.
- Section 6.11.3.2, ref STIL.1/STIL.4 shared memory, special attention to initialization sequence.
- Annex A Name Spaces
- Annex C Event Sequence
- Annex F Block Sequence
- Section 6.9 Binning: review syntax after rescinding hard bin counters. See near line 3591.
- Seeking help with coding examples: writing coding examples is an excellent way to spot documentation shortcomings. More participation equals more scrutiny. A complete production test program would be useful.

Summary:

Line numbers if any are with regard to STIL.4 syntax document dated March 31, 2014.

- Long discussion about ChannelMap in DeviceBlock vs. SignalMap in TestProgram block.
- RESOLVED: Move ChannelMap back under Device and add Top-level SignalMap: include alphanumeric SITE_NAME
 - Rationale for re-introducing the ChannelMap under the Device block is as follows:
 - The short explanation for why a single block definition doesn't work is design philosophy: the ChannelMap definition in conjunction with the context in which it is used, creates no internal conflicts. SignalMap contents as I currently understand them, repeat mapping information introducing the possibility of contradictions and as a result, the need to check for them
 - Jim to write up and distribute proposed SignalMap syntax for review. (DONE as of 4/6/2014)
 - Also note that the SignalMap block as specified in the TestProgram block, and the ChannelMap block as specified in the Device block, are mutually exclusive. One CANNOT use both at the same time.
- RESOLVED: Either TestProgram refers to SignalMap or Device block refers to TestProgram
- RESOLVED: Define a “STIL.4 compliant” tool as parsing all legal STIL.4 syntax, supported syntax in accordance with specified semantics. Specific block structures may be ignored if a tool does not require them, but they MUST be parsed with no errors generated. Requires word-smithing and where to put in the document.
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Action Items:

- Jim to write up description of syntax/semantics of SignalMap block and distribute.
- Jim to research what it means to be “standard-compliant” – can a tool be compliant if it only reads without error specific syntax constructs, but does not act on them?

Reference documents (If logged into your google account, can edit. If not, can only view.)

- <http://spreadsheets.google.com/ccc?key=0AoKiPr1I9LY9dF95dkhSTVVqOU5GbWJyWFNhY0JPX0E&hl=en>
- Namespace resolution examples document:
<http://docs.google.com/Doc?docid=0AYKiPr1I9LY9ZGY4dmNjNTNfMGZkOGJ2bmZy&hl=en>
- Scratchpad spreadsheet: <https://spreadsheets0.google.com/ccc?key=tQ93VDnAZ-CI9RFKpPrPDzw&authkey=COzyro8K&hl=en&authkey=COzyro8K#gid=0>

- Scratchpad "Word" doc: https://docs1.google.com/document/d/1zVu2M8nTJsrn0nFbBhiuM8-YRt4ErYqdy_uSa3x3_T4/edit?authkey=CLrgwrsG#

Next meeting: 04/08/14.

For reference STIL .4 information can be found at the IEEE STIL website: <http://grouper.ieee.org/groups/1450/> (select the [P1450.4](#) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>