

## 1450.4 meeting minutes – 04/10/13 and 04/12/13

**Attendees:** Ernie Wahl, Jim O'Reilly, Mitsuo Fujii, Markus Seuring

**Not present:** Julia DiChiaro, Oleg Erlich, Ajay Khoche, Paul Reuter

### Summary:

Attendees: Jim O'Reilly, Markus Seuring, Ernie Wahl

Line numbers are from syntax document dated April 9, 2013

- Line 3973: ACCEPT add Trim burn voltage and current.
- Line 3991: ACCEPT Environment with variable parameter list of literal values w units.
- Line 4178: ACCEPT pattern syntax to be adapted for multi-device environment where SignalGroups references are allowed (same as levels and timing).
- Line 3997: need better understanding of FreeClock to provide tester-independent syntax. STARC requirements provided by Mitsuo-san follow:
  - Is it Synchronous or Asynchronous?
    - STARC requirement: Asynchronous
  - Can it execute not just a clock but an arbitrary waveform?
    - STARC requirement: clock
  - Starts/stops before, during, with, or after pattern? Test-program (test)?
    - STARC requirement: start before or during pattern execution.
  - Define as:
    - a tester timing resource (augment STIL.0 Waveforms syntax)?
    - load-board clock circuit?
    - a signal requirement? only part-time?
    - a specific test type? (e.g., RunTo keep-alive until IDDQ is performed?)
    - all of the above?
  - STARC requirements:
    - tester timing resource
    - load-board clock circuit
    - part-time signal requirement
    - test type
- Try to coordinate with Greg Maston regarding STIL.0

**Next meeting:** 04/17/13

For reference STIL .4 information can be found at the IEEE STIL website: <http://grouper.ieee.org/groups/1450/> (select the [P1450.4](#) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>