

1450.4 meeting minutes – 04/25/12

Attendees: Ernie Wahl, Jim O'Reilly

Not present: Julia DiChiaro, Oleg Erlich, Markus Seuring, Ajay Khoche, Paul Reuter

Summary:

Line numbers are with regard to syntax document dated April 24, 2012.

- Entire Document: Change CurrentExec to ThisTest
- Line 747, RECOMMEND: name is sufficient for now, have SiteLayout keyword and array syntax in back pocket if needed.
- Line 784, RECOMMEND: with reference to pad center.
- Line 793, RECOMMEND: Provide shorthand for pin names, e.g., A[1..2] to yield PGA pin names A1 and A2; [A..C][1..3] to yield PGA pin names A1, A2, A3, B1, B2, B3, C1, C2, and C3.
- Line 796, RECOMMEND: Associate shorthand used for PinList.
- Line 827, RECOMMEND: No logical pins (can use position in Signals block as logical pin)
- Line 1852, RECOMMEND: Scope operators limited to one step, i.e., Parent.x = legal, Parent.Parent.x is illegal.

Actions:

- Jim to rework Verigy example from STIL paper so that the STIL translation is consistent with its original Verigy code.

Reference documents (If logged into your google account, can edit. If not, can only view.)

- [Current Draft Syntax Document](#)
- [Issues List](#)
- [Namespace resolution examples document](#)
- [Communications spread sheet](#) (scratchpad spreadsheet)
- [Communications word doc](#) (scratchpad word document)

Next meeting: 05/2/12

For reference STIL .4 information can be found at the IEEE STIL website: <http://grouper.ieee.org/groups/1450/> (select the [P1450.4](#) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>