1450.4 meeting minutes – 05/06/10

Attendees: Bruce Parnas, Ernie Wahl, Jim O'Reilly, Oleg Erlich, Ajay Khoche
Not present: Markus Seuring , Greg Maston

Agenda:

- IEEE Meeting Preamble (No discussion of proprietary information).
- Minutes from last week are now on the web.
- Discussion items
    - Review of binning items (from Ernie's list) discussed by Jim/Ajay (listed below).
        - When a Bin is disabled, does Axis.Size remain the same?.
            - Resolution: YES
            - Semantics:
                - If a bin is disabled, SetBin is ignored. The default state for a bin is Clear. Note that this bin state is reset to Clear at each On Start event, and is separate from the bin counters, which retain their counts until they're separately reset by the appropriate event (Load, Reset, Start, WaferStart, LotStart, <UserKeyword>) for each counter.
                - Only softbins can be disabled.
                - Since a disabled softbin can't be set during the flow, it won't have any impact on the bin mapping (you can include a disabled softbin in the binmap, but since it cannot be set, it will not factor into the mapping).
                - For the SetBinStop statement, using a disabled bin as an argument results in the same behavior as using None (no bin) as an argument – that is, no bin is set, and flow does not stop.
            - Question: do we expect to be able to disable a bin during execution of the test flow, or only set those attributes statically, when defining the binmap?
                - Bins can be enabled or disabled only while flow is NOT executing. This can be done either by
                    - editing dot4 source before compilation or interpretation
                    - after the *On Load* event is done and while an *On Start* event is not active (i.e., the flow is not executing), e.g., via a system GUI
        - Restrict retest max count and counter to hard bins only?
            - Resolution: YES. This is already in the syntax document at *hard_bin_attributes* (line 393 of D32, dated 2-10-2010).
        - Allow or require a NoBin (this should be None, not NoBin, per previous agreement - example below shows use of None) mapping to cover the case where no soft bin has been set (for instance: Map None -> 5).
            - Resolution: YES, allow this. Many languages (i.e. SmarTest) already have similar constructs.
        - BinSpec: change to FailBinSpec & PassBinSpec ? or add property (see Variables section item "BinSpec variable name needs resolution rules so it isn't confused with Bin names").
            - Resolution: Still under discussion

- Open issues - are there other open issues that should be considered? A review of the open issues list can guide us here.
    - Issues list: http://spreadsheets.google.com/ccc?key=0AoKiPr1I9LY9dF95dkhSTVVqOU5GbWJyWFNhY0JPX0E&hl=en
    - Namespace resolution examples document: http://docs.google.com/Doc?docid=0AYKiPr1I9LY9ZGY4dmNjNTNfMGZkOGJ2bmZy&hl=en
    - If logged into your google account, can edit. If not, can only view.
- Next Meeting 04/01/10.

For reference STIL .4 information can be found at the IEEE STIL website: http://grouper.ieee.org/groups/1450/ (select the P1450.4 link from the table) or use the direct link http://grouper.ieee.org/groups/1450/dot4/index.html