1450.4 meeting minutes – 06/26/13

Attendees: Ernie Wahl, Jim O'Reilly, Mitsuo Fujii, Markus Seuring
Not present: Julia DiChiaro, Oleg Erlich, Ajay Khoche, Paul Reuter

Summary:
Line numbers are from syntax document dated June 25, 2013.

- Line 840: observation "(PkgNr PKG_NR (, PKG_NR)*;)" is insufficient for multi-package test. Consider replacing with "Composition SITE_NR (, SITE_NR)*".
    - Pros: keyword Composition can describe a package that contains multiple instances of the same chip or a mix of chips via the SITE_NR reference.
    - Cons: not as easy to see how many packages are being tested in parallel (req's combination of Composition and ChannelMap or comment to figure out).
    - NOTE: CHAN_ID in ChannelMap is specified in the same order as Chip sites, i.e., consecutive by default or specified via Chip/Site statement.
- Line 3981: discussed Mitsuo-san's request for re-instituting Pad optional parameter PAD_NAME. We could not determine what is to be gained by it. We took it out because keyword Pad may apply to one or many pads, consider e.g., signal VDD (should we call it PadInfo instead?). We agreed to put PAD_NAME back once we understand what function it performs that isn't already covered.
    - Jim supplied diagram from STARC to aid in discussion. We note different terms used by STIL.4 and STARC, respectively:

      Buffer INSTANCE_NAME -> cell name
      Node NODE_NAME -> pin name of cell

    - NOTE: STIL.4 NODE_NAME is the name of a node on a buffer type, not a name unique to an instance. The buffer INSTANCE_NAME in conjunction with NODE_NAME is what makes the node unique among several instances of a buffer type. The diagram did not make it clear what "pin name of cell" is with regard to buffer type or instance.

- Line 4008, 4032: defer Trim until phase II, STIL.5
- Line 4094, 4097: bring example in line with definition:

  Power 5V, 3V -> Power VDD5, VDD33 // Power args are signal names not values

  The VOH specification is 2.5V when the power supply is at 5V, the VOH specification is 1.5V when the power supply is at 3V. For a dual rail programmable buffer, the VOH specification is 2.5V when power supply rail VDD5 is active, the VOH specification is 1.5V when power supply rail VDD33 is active.

Reference documents (If logged into your google account, can edit. If not, can only view.)

- http://spreadsheets.google.com/ccc?key=0AoKiPr1I9LY9dF95dkhSTVVqOU5GbWJyWFNhY0JPX0E&hl=en
- Namespace resolution examples document: http://docs.google.com/Doc?docid=0AYKiPr1I9LY9ZGY4dmNjNTNfMGZkOGJ2bmZy&hl=en
- Scratchpad spreadsheet: https://spreadsheets0.google.com/ccc?key=tQ93VDnAZ-Cl9RFKpPrPDzw&authkey=COzyro8K&hl=en&authkey=COzyro8K#gid=0
- Scratchpad "Word" doc: https://docs1.google.com/document/d/1zVu2M8nTJsrm0nFbBhiuM8-YRt4ErYqdy_uSa3x3_T4/edit?authkey=CLrgwrsG#

Next meeting: 07/10/13 (Meeting on July 3rd cancelled – holiday)

For reference STIL .4 information can be found at the IEEE STIL website: http://grouper.ieee.org/groups/1450/ (select the P1450.4 link from the table) or use the direct link http://grouper.ieee.org/groups/1450/dot4/index.html