

## 1450.4 meeting minutes – 09/12/12

**Attendees:** Ernie Wahl, Jim O'Reilly

**Not present:** Julia DiChiaro, Oleg Erlich, Markus Seuring, Ajay Khoche, Paul Reuter

### Summary:

Line numbers are with regard to syntax document dated September 11, 2012

- Line 765: expanded syntax to cover instance where SIG\_NAME represents a signal-group:  

```
(SIG_NAME CHAN_ID (,CHAN_ID)*; |  
  SIG_NAME CHAN_ID (,CHAN_ID)* { (chan_config)* } |  
  SIG_NAME { CHAN_ID (,CHAN_ID)*; } (, { CHAN_ID (,CHAN_ID)*; })*; |  
  SIG_NAME { CHAN_ID (,CHAN_ID)*; } (, { CHAN_ID (,CHAN_ID)*; })* { (chan_config)* })*
```

Example:

```
data { 0,1; }, { 2,3; }; // 2 sites, data is a 2 signal signal-group  
data[0] 0,8;           // 2 sites, data[0] is a single signal  
data[1] 1,9;           // 2 sites, data[1] is a single signal
```
- Line 732: remove "Multiple chip name(s), for scaled devices, e.g., FPGAs ?": scaled devices may be covered via user-defined keywords
- Line 748: delete Wafer reference, Wafer references Device instead
- Line 891: EJW: rewrite with line 765 in mind
- Line 963: EJW: add example with line 765 in mind
- Line 1049: REVISIT: even though multi-project-wafer testing requires mapping each chip site on a wafer to a Device test-program, it is not clear that STIL.4 has to provide a standard syntax for this and related purposes.
  - Comments from SCSWG/SSTAG (Mitsuo Fujii): We need only wafer size, chip size and wafer orientation mark (you call it the origin?) in order to set up a prober with a probe card for the wafer testing actually.
- REVISIT: it appears we need at least 2 separate STIL.4 identifier naming rules:
  - one for ids used in expressions, i.e., lms should not be confused with an identifier.
  - one for labels that may likely start with a number. So far we've identified:
    - PKG\_PIN\_NAME, CHAN\_ID, DEVICE\_NAME, CHIP\_NAME, TESTER\_NAME, BIN\_DEF\_NAME
  - SCSWG/SSTAG (Mitsuo Fujii) suggestions:
    - (1) User-defined names consist of underline (underscore), and lower/upper case letters and digit (alphanumeric). Accept.

**NOTE:** Discussions about multi-site issues are ongoing with SCSWG

### Actions:

- Jim to rework Verigy example from STIL paper so that the STIL translation is consistent with its original Verigy code.

**Reference documents** (If logged into your google account, can edit. If not, can only view.)

- [Current Draft Syntax Document](#)
- [Issues List](#)
- [Namespace resolution examples document](#)
- [Communications spread sheet](#) (scratchpad spreadsheet)
- [Communications word doc](#) (scratchpad word document)

**Next meeting:** 09/17/12

For reference STIL .4 information can be found at the IEEE STIL website: <http://grouper.ieee.org/groups/1450/> (select the [P1450.4](#) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>