1450.4 meeting minutes – 09/25/13

Attendees: Ernie Wahl, Markus Seuring, Jim O'Reilly, Mitsuo Fujii
Not present: Julia DiChiaro, Oleg Erlich, Ajay Khoche, Paul Reuter

Summary:
Line numbers are from syntax document dated Sept. 24, 2013.

- Line 4269 - 4272: MOVE to SignalGroups section. ACCEPT rewrite:
  - As per STIL.0, section 15, a signal group may be empty. If an empty signal group is referenced in any STIL resource blocks (timing, levels, patterns, etc.), that empty signal group will have no impact on the results of a test which uses such resource blocks. This mechanism allows reuse of timing, levels, and patterns for scaled devices. For example, when the same chip is packaged with multiple pinouts, some signals may not be bonded out in some packages.
- Line 4274 - 4277: MOVE to SignalGroups section. ACCEPT rewrite:
  - As per STIL.0, section 18.1, optional statement SignalGroups GROUPS_DOMAIN is used to indicate a named set of groups to be used to resolve group references in the WaveformTables defined in the Timing block. STIL.0 does not fully describe the resolution mechanism but a subsequent Clarifications document provides more detail (URL http://grouper.ieee.org/groups/1450/index.html).
- Line 4279 - 4289: MOVE to SignalGroups section. ACCEPT rewrite:
  - In light of the fact that STIL.4 allows for multiple (named) Signals blocks some explanation is in order. There are two namespaces for signal and signal groups: one pools the signal and signal groups defined in the unnamed global blocks, the other pools signal groups defined in the named global blocks. When in a SignalGroups block, a SignalGroup is defined in terms of another, a reference to a signal or signal-group name on the right-hand side is resolved in the following order:
    1. The current SignalGroups block is searched for the name. A match resolves to the references contained in that name.
    2. A reference is matched against the names defined in the global SignalGroups block. A match resolves to the references contained in that name.
    3. A reference is matched against the names defined in the Signals block.
- Line 4291 - 4291: MOVE to SignalGroups section. ACCEPT rewrite:
  - "When named Signals or SignalGroups blocks are specified in the Device/Package block directly, or Device/Chip block indirectly, those named blocks shall augment the global unnamed blocks with regard to the pooling and name resolution mechanism described above. Similar to unnamed Signals and SignalGroups blocks, signal and group identifiers inside Signals and SignalGroups blocks of the same name shall share the same namespace. For consistent behavior between named and unnamed blocks, STIL.4 shall not permit signal names to be overridden by signal group names under any circumstance (unlike STIL.0)."
- Line 4296 - 4297: DELETE

Reference documents (If logged into your google account, can edit. If not, can only view.)

- http://spreadsheets.google.com/ccc?key=0AoKiPr1I9LY9dF95dkhSTVVqOU5GbWJyWFNhY0JPX0E&hl=en
- Namespace resolution examples document: http://docs.google.com/Doc?docid=0AYKiPr1I9LY9ZGY4dmNjNTNfMGZkOGJ2bmZy&hl=en
- Scratchpad spreadsheet: https://spreadsheets0.google.com/ccc?key=tQ93VDnAZ-Cl9RFKpPrPDzw&authkey=COzyro8K&hl=en&authkey=COzyro8K#gid=0
- Scratchpad "Word" doc: https://docs1.google.com/document/d/1zVu2M8nTJsrm0nFbBhiuM8-YRt4ErYqdy_uSa3x3_T4/edit?authkey=CLrgwrsG#

Next meeting: 10/02/13

For reference STIL .4 information can be found at the IEEE STIL website: http://grouper.ieee.org/groups/1450/ (select the P1450.4 link from the table) or use the direct link http://grouper.ieee.org/groups/1450/dot4/index.html