

1450.4 meeting minutes – 10/24/12

Attendees: Ernie Wahl, Jim O'Reilly, Mitsuo Fujii

Not present: Julia DiChiaro, Oleg Erlich, Markus Seuring, Ajay Khoche, Paul Reuter

Summary:

Line numbers are with regard to syntax document dated October 23, 2012

- Line 1545: ACCEPT form "(Port PORTLABEL boolean_expr { (exit_port_stmt)* } next_target;)*"
 - consider: substitute post_action_stmt for exit_port_stmt
 - define pre_action_stmt, post_action_stmt, and next_target(_stmt)
 - reallocate post_action_stmt and next_target_stmt components
- Line 1733: DROP VAR_NAME from form "Next (FLOW_NODE_NAME | VAR_NAME)" for phase I
- Line 1176: ACCEPT section 7.4.8 Fail Memory Analysis
- Line 2519: ACCEPT form "(from_vecloc, to_vecloc, sigref_expr)"

Reference documents (If logged into your google account, can edit. If not, can only view.)

- [Current Draft Syntax Document](#)
- [Issues List](#)
- [Namespace resolution examples document](#)
- [Communications spread sheet](#) (scratchpad spreadsheet)
- [Communications word doc](#) (scratchpad word document)

Next meeting: 10/31/12

For reference STIL .4 information can be found at the IEEE STIL website: <http://grouper.ieee.org/groups/1450/> (select the [P1450.4](#) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>