1450.4 meeting minutes - 08/25/04

Attendees: Jim O'Reilly, Tony Taylor, Ernie Wahl, Yuhai Ma, Dave Dowding, Jose Santiago, Oscar Rodriguez

Not present: Eric Nguyen, Doug Sprague, Dan Fan, Don Organ, Jim Mosley, Tom Micek

Agenda:
- Syntax subgroup activities status
- Conceptual model subgroup activities status
- STC activities update

Syntax subgroup status:
- Still need another week to iron out the syntax before making presentation to full working group. Issues still needing work:
  - Binning (i.e., complex binning with logical combinations of softbins to hardbins).
  - On* entry points. Need to explicitly define the behavior of each of these.
  - Multi-site issues.
    - Do we need unique binmaps per site?
    - Do we need keywords to specify which operations can be done in parallel and which must be done serially?
    - Do we need the ability to run different test programs test programs on different sites? Do we need a "SiteManager" block in STIL .4? From Jose, STC has capability to run different test programs on different sites. There are a number of different scenarios:
      - Same test program running on all sites
      - Different test programs running on different sites (multi-project wafers, etc.).
      - Parallel testing of multiple circuit blocks within one die (seems to be more of a 1450.6 or 1500 issue to me).
      - Other scenarios?
  - Table of contents for ballot document – need someone to start working on this. Let's discuss this at next week's meeting.

Conceptual model subgroup status:
No updates

STC status:
No updates.