

P1450.4 meeting minutes - 08/31/05

Attendees: Dave Dowding, Chris Nelson, SB (Intel), Jose Santiago, Jim O'Reilly, Ernie Wahl, Bob Roberts

Not present: Jim Mosley, Tom Micek, Yuhai Ma, Eric Nguyen, Oscar Rodriguez, Steve Lill, Tony Taylor, Daniel Fan, Doug Sprague

Summary:

- Have started on draft document (courtesy of Jose Santiago). Currently it just contains a table of contents. Walk through document structure.
- SB (Intel) – Intel expert/architect for internal test program generation tools.
- Questions/comments from SB:
 - Will the syntax include GUI attributes? For instance, X/Y positioning of block; input/output ports of blocks.
 - Multi-site synchronization:
 - How does the standard deal with synchronization among multiple sites.
 - Synchronization among multiple duts of different types running on the same site?
 - Jim to coordinate discussions with SB regarding this issue.
- Chris/Dave to provide references containing information which STC used to guide design decisions regarding multi-site.
- Face-to-face meetings at ITC. Possible ITC attendees from the WG - Dave, Chris, Jose, Jim(?).
- For reference STIL .4 information can be found at the IEEE STIL website:
<http://grouper.ieee.org/groups/1450/> (select the [P1450.4](http://grouper.ieee.org/groups/1450.4) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>