

P1450.4 meeting minutes - 09/07/05

Attendees: Dave Dowding, Chris Nelson, Jose Santiago, Jim O'Reilly, Bob Roberts, Ernie Wahl, SB

Not present: Jim Mosley, Tom Micek, Yuhai Ma, Eric Nguyen, Oscar Rodriguez, Steve Lill, Tony Taylor, Daniel Fan, Doug Sprague

Summary:

- Describe for Chris the structure and interactions of the full WG, the syntax subgroup and the conceptual model group.
- Chris's comments/areas of interest.
 - Flow hierarchy (multiple test methods within a flow node – how is this handled?)
 - Arbiter/pass/fail actions. Would like to justify various aspects of syntax with applications need.
- Action items – how do we deal with some of the issues which have arisen?
 - Multi-site
 - Jim to study multi-site requirement documents from Intel. Will talk with SB.
 - Variable scoping
 - Review Tony's document; compare/contrast OTPL vs. STIL.
 - Graphical notation (SB) – how is this dealt with (IS it dealt with?)
 - X/Y coordinates of specific point (upper left corner of flow-node?)
 - X/Y coordinates of exit ports on flow nodes
 - Important when dealing with hierarchical flow (flow nodes within flow nodes).
 - Important when dealing with multi-site synchronization (talk to SB for more info).
 - Attribute subblocks for various blocks. Credence (ASAP), Advantest, Inovys have solutions; Teradyne, Agilent do not. Not sure about Credence XTOS or languages other than ASAP.
 - Jim/SB to summarize existing scope of existing solutions to present to WG next Wednesday.
- For reference STIL .4 information can be found at the IEEE STIL website:
<http://grouper.ieee.org/groups/1450/> (select the [P1450.4](#) link from the table) or use the direct link <http://grouper.ieee.org/groups/1450/dot4/index.html>