Spreading the News: P1500 & Lucent

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The Motivation

- To keep the Lucent designers well informed about the recent developments to the standard
- Avoid re-inventing the wheel
- To ensure preparedness for quick adaptation of the standard
- To collect feedback from the designers who are not regular attendees of the WG meetings
Forums

• ASIC Process Management Team (PMT) meetings
• Lucent Tech Design Users Conference
• Lucent Test Conference
Presentation

- IEEE P1500 goals and activities
- Latest updates on CTAG and CTL
- Updates on other task force activities
- Schedules
- Discussion, Q&A
Feedback

• When?
• Why can't we use 1149.1 with user defined instruction for parallel TAM access?
• Who is responsible for the wrapper (IP provider, integrator)?
• If we have CTL and appropriate EDA tools, do we need a standard wrapper?
• Soft Cores
• For BIST-ed cores why use wrappers?