

PHY Section Annex D

hp_phy_annex_d_1.pdf
SONET/SDH function RS

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Agenda

- As per request from PHY comment resolution ad hoc for P802.17/D1.0
 - Comment #593
 - Address Existing Annex D's Comments
- Update
 - 1) PHY_DATA.indicate(INPUT_FRAME, STATUS, LENGTH)
 - SPI-x section

Summary

New structure of Annex D

- Overall summary
- SRS physical frame structure
 - GRS frame structure
- LINK_STATUS.indicates
 - SF
 - SD
- SPI interfaces
 - SPI-3 8 bit up to 622Mbps
 - SPI-3 32 bits up to OC-48 rate
 - SPI-4.1 64 bit up to 10Gbps interface
 - SPI-4.2 16 bit up to 10Gbps interface

SPI sections

- Transmission order
 - Bit ordering
 - Byte ordering
- Signal descriptions
 - Mapping to PSAP primitive
 - PHY_DATA.request
 - OUTPUT_FRAME
 - LENGTH
 - PHY_DATA.indicate
 - INPUT_FRAME
 - LENGTH
- Electrical definition
 - Referred to OIF

Address D1.1 Comments

- 306
- 307
- 309
- 310 (1/2)
- 311
- 312
- 308 (CRC)

What's Next

- Associated file
 - anxD_SONET_RS_hp4.pdf
- Motion
 - To replace existing Annex D



Thank You