

APPROPRIATE SYNTAX:

BOOLEAN

BEHAVIOUR DEFINED AS:

True if the TimeSync capability is supported in the receive path and false otherwise.

If a Clause 45 MDIO Interface to PMA/PMD, WIS, PCS, PHY XS, DTE XS and/or TC is present, then the value stored in this attribute is equal to the logical OR operation over the values stored in the following instantiated MDIO registers (for each MMD, in case of multiple instances) 1.1800.0, 2.1800.0, 3.1800.0, 4.1800.0, 5.1800.0, and 6.1800.0 (see 45.2.1.146, 45.2.2.20, 45.2.3.66, 45.2.4.28, 45.2.5.28, 45.2.6.14, respectively).

### 30.13.1.3 aTimeSyncDelayTXmax

ATTRIBUTE

APPROPRIATE SYNTAX:

INTEGER

BEHAVIOUR DEFINED AS:

The maximum data delay as specified in 90.7, expressed in units of ns, [with sub-ns fraction](#).

If a Clause 45 MDIO Interface to PMA/PMD, WIS, PCS, PHY XS, DTE XS and/or TC is present, then the value stored in this attribute represents the maximum transmit path data delay values, consisting of the sum of the values of the registers in the instantiated sublayers (for each MMD, in case of multiple instances):

- for PMA/PMD: 1.~~1801~~[1801](#), [1.1802](#), and 1.~~1802~~[1809](#), see 45.2.1.147
- for WIS: 2.~~1801~~[1801](#), [2.1802](#), and 2.~~1802~~[1809](#), see 45.2.2.21
- for PCS: 3.~~1801~~[1801](#), [3.1802](#), and 3.~~1802~~[1809](#), see 45.2.3.67
- for PHY XS: 4.~~1801~~[1801](#), [4.1802](#), and 4.~~1802~~[1809](#), see 45.2.4.29
- for DTE XS: 5.~~1801~~[1801](#), [5.1802](#), and 5.~~1802~~[1809](#), see 45.2.5.29
- for TC: 6.~~1801~~[1801](#), [6.1802](#), and 6.~~1802~~[1809](#), see 45.2.6.15

### 30.13.1.4 aTimeSyncDelayTXmin

ATTRIBUTE

APPROPRIATE SYNTAX:

INTEGER

BEHAVIOUR DEFINED AS:

The minimum data delay as specified in 90.7, expressed in units of ns, [with sub-ns fraction](#).

If a Clause 45 MDIO Interface to PMA/PMD, WIS, PCS, PHY XS, DTE XS and/or TC is present, then the value stored in this attribute represents the minimum transmit path data delay values, consisting of the sum of the values of the registers in the instantiated sublayers (for each MMD, in case of multiple instances):

- for PMA/PMD: 1.~~1803~~[1803](#), [1.1804](#), and 1.~~1804~~[1810](#), see 45.2.1.147
- for WIS: 2.~~1803~~[1803](#), [2.1804](#), and 2.~~1804~~[1810](#), see 45.2.2.21
- for PCS: 3.~~1803~~[1803](#), [3.1804](#), and 3.~~1804~~[1810](#), see 45.2.3.67
- for PHY XS: 4.~~1803~~[1803](#), [4.1804](#), and 4.~~1804~~[1810](#), see 45.2.4.29
- for DTE XS: 5.~~1803~~[1803](#), [5.1804](#), and 5.~~1804~~[1810](#), see 45.2.5.29
- for TC: 6.~~1803~~[1803](#), [6.1804](#), and 6.~~1804~~[1810](#), see 45.2.6.15

### 30.13.1.5 aTimeSyncDelayRXmax

ATTRIBUTE

APPROPRIATE SYNTAX:

INTEGER

BEHAVIOUR DEFINED AS:

The maximum data delay as specified in 90.7, expressed in units of ns, [with sub-ns fraction](#).

If a Clause 45 MDIO Interface to PMA/PMD, WIS, PCS, PHY XS, DTE XS and/or TC is present, then the value stored in this attribute represents the maximum receive path data delay values, consisting of the sum of the values of the registers in the instantiated sublayers (for each MMD, in case of multiple instances):

- for PMA/PMD: 1.~~1805-1805~~, [1.1806](#), and 1.~~1806~~[1811](#), see 45.2.1.148
- for WIS: 2.~~1805-1805~~, [2.1806](#), and 2.~~1806~~[1811](#), see 45.2.2.22
- for PCS: 3.~~1805-1805~~, [3.1806](#), and 3.~~1806~~[1811](#), see 45.2.3.68
- for PHY XS: 4.~~1805-1805~~, [4.1806](#), and 4.~~1806~~[1811](#), see 45.2.4.30
- for DTE XS: 5.~~1805-1805~~, [5.1806](#), and 5.~~1806~~[1811](#), see 45.2.5.30
- for TC: 6.~~1805-1805~~, [6.1806](#), and 6.~~1806~~[1811](#), see 45.2.6.16

### 30.13.1.6 aTimeSyncDelayRXmin

ATTRIBUTE

APPROPRIATE SYNTAX:

INTEGER

BEHAVIOUR DEFINED AS:

The minimum data delay as specified in 90.7, expressed in units of ns, [with sub-ns fraction](#).

If a Clause 45 MDIO Interface to PMA/PMD, WIS, PCS, PHY XS, DTE XS and/or TC is present, then the value stored in this attribute represents the minimum receive path data delay values, consisting of the sum of the values of the registers in the instantiated sublayers (for each MMD, in case of multiple instances):

- for PMA/PMD: 1.~~1807-1807~~, [1.1808](#), and 1.~~1808~~[1812](#), see 45.2.1.148
- for WIS: 2.~~1807-1807~~, [2.1808](#), and 2.~~1808~~[1812](#), see 45.2.2.22
- for PCS: 3.~~1807-1807~~, [3.1808](#), and 3.~~1808~~[1812](#), see 45.2.3.68
- for PHY XS: 4.~~1807-1807~~, [4.1808](#), and 4.~~1808~~[1812](#), see 45.2.4.30
- for DTE XS: 5.~~1807-1807~~, [5.1808](#), and 5.~~1808~~[1812](#), see 45.2.5.30
- for TC: 6.~~1807-1807~~, [6.1808](#), and 6.~~1808~~[1812](#), see 45.2.6.16