

Table 90–1—Summary of TimeSync features in Clause 45

Register	Name	Reference
1.1800	TimeSync PMA/PMD capability register	45.2.1.146
1.1801 through 1.1804_ 1.1809 through 1.1810	TimeSync PMA/PMD transmit path data delay	45.2.1.147
1.1805 through 1.1808_ 1.1811 through 1.1812	TimeSync PMA/PMD receive path data delay	45.2.1.148
2.1800	TimeSync WIS capability register	45.2.2.20
2.1801 through 2.1804_ 2.1809 through 2.1810	TimeSync WIS transmit path data delay	45.2.2.21
2.1805 through 2.1808_ 2.1811 through 2.1812	TimeSync WIS receive path data delay	45.2.2.22
3.1800	TimeSync PCS capability register	45.2.3.66
3.1801 through 3.1804_ 3.1809 through 3.1810	TimeSync PCS transmit path data delay	45.2.3.67
3.1805 through 3.1808_ 3.1811 through 3.1812	TimeSync PCS receive path data delay	45.2.3.68
4.1800	TimeSync PHY XS capability register	45.2.4.28
4.1801 through 4.1804_ 4.1809 through 4.1810	TimeSync PHY XS transmit path data delay	45.2.4.29
4.1805 through 4.1808_ 4.1811 through 4.1812	TimeSync PHY XS receive path data delay	45.2.4.30
5.1800	TimeSync DTE XS capability register	45.2.5.28
5.1801 through 5.1804_ 5.1809 through 5.1810	TimeSync DTE XS transmit path data delay	45.2.5.29
5.1805 through 5.1808_ 5.1811 through 5.1812	TimeSync DTE XS receive path data delay	45.2.5.30
6.1800	TimeSync TC capability register	45.2.6.14
6.1801 through 6.1804_ 6.1809 through 6.1810	TimeSync TC transmit path data delay	45.2.6.15
6.1805 through 6.1808_ 6.1811 through 6.1812	TimeSync TC receive path data delay	45.2.6.16