# Annex 98B Selector Base Page Definition

## 98B.3 Technology Ability Field bit assignments

**Table 98B–1—Technology Ability Field bit assignments**

| bit | Selector description |
|---|---|
| A0 | 100BASE-T1 ability |
| A1 | 10BASE-T1S full duplex ability |
| … | … |
| A6 | 25GBASE-T1 ability |
| A7 | Technology Category Selection bit 0 |
| A8 | Technology Category Selection bit 1 |
| A10 – A21 | Category-specific technology ability bits |
| A22 | 10BASE-T1S half duplex capability |
| … | … |
| A25 | 10BASE-T1L EEE ability |
| A26 | Technology Category Selection bit 2 |

### 98B.3.1 Technology Category Selection

The Technology Category Selection bits are mapped as shown in Table98B-1.

**Table 98B–1—Technology Category Selection**

| A7, A8, A26 | Category |
|---|---|
| 000 | NULL |
| 001 | Reserved |
| 010 | BASE-T1L |
| otherwise | Reserved |

### 98B.3.2 Technology Ability Field bit assignments for NULL Category

Any meaningful combination of bits A0 – A6 and A22 – A25 may be set. Bits A10 – A21 are assigned as shown in Table 98B–2.

**Table 98B–2—Category-specific technology bit assignments for NULL category**

| bit | Selector description |
|---|---|
| A10 | 100BASE-T1L standard transmit/receive level ability |
| A11 – A20 | Reserved |
| A21 | 100BASE-T1L increased transmit/receive level ability |

***Editor's Note***

*Bit A21 is mapped in the same way as in draft 2.1. Currently 802.3dm is proposing to use bits A11 – A16.*

### 98B.3.4 Technology Ability Field bit assignments for BASE-T1L Category

Any meaningful combination of bits A9 and A23 – A25 may be set. Bits A10 – A21 are assigned as shown in Table 98B–4.

**Table 98B–4—Category-specific technology bit assignments for BASE-T1L category**

| bit | Selector description |
|---|---|
| A10 | 100BASE-T1L standard transmit/receive level ability |
| A11 | 100BASE-T1L increased transmit/receive level ability |
| A12-A21 | Reserved |