P802.3ak Draft 4.1 Comments

| Cl 00 | SC 46.1.2 | $P$ | LO |
| :--- | :---: | :---: | :---: |
| Bradshaw, Peter | BitBlitz Communicatio | \#5 |  |
| Comment Type T | Comment Status A |  |  |

802.3ae says in 46.1 .2 about XGMII ""This interface is used to provide media
independence so that an identical media access controller may be used with all 10GBASE
PHY types using either serial or wavelength division multiplexed optics."" There is no
technical reason that XGMII cannot be used with CX4 (in fact I expect most
implementations will have XGMII explicitly or implicitly embedded within them).

## SuggestedRemedy

Direct the editor to consider adding this to the 'modify' sections, with proposed new wordin such as replacing 'serial or wavelength division multiplexed optics' with 'serial or multiple lane optical or electrical channels', which would aslo cover the 10GBASE-T proposal.
Proposed Response
Response Status $\mathbf{C}$
ACCEPT IN PRINCIPLE.
Elivated from "E" to "T"
In 46.1 .2 change "This interface is used to provide media independence so that an identical media access controller may be used with all 10GBASE PHY types using either serial or wavelength division multiplexed optics." to "This interface is used to provide media independence so that an identical media access controller may be used with all 10GBASE PHY types."

| CI 01 | SC 3 | P3 | $L 6$ |
| :--- | :---: | :---: | :---: |
| Frazier, Howard | SWI | \# 23 |  |
| Comment |  |  |  |

Comment Type TR Comment Status A TR23

Incomplete reference to IEC 61076-3-113. I have been told that all references must be complete prior to the start of the WG ballot

## SuggestedRemedy

Provide the complete reference to IEC 61076-3-113, including the date and title.

## Proposed Response Response Status C

ACCEPT IN PRINCIPLE.
Added in "Connectors for electronic equipment - Part 3-113: Screened, serial multi-
conductor cable to board connectors suitable for $10 \mathrm{Gbit} / \mathrm{sec}$ data rates." This is a similar title to IEC 61076-3-103.

| Cl 01 | SC 3 | P3 | L6 |
| :--- | :---: | :---: | :---: |
| Dawe, Piers | Agilent |  | \# 50 |
| Comment Type | E | Comment Status A |  |
| TR23 |  |  |  |

Comment Type E Comment Status A TR23
The connector reference IEC 61076-3-113 has now showed up on the IEC website. Here's its title: Connectors for electronic equipment - Part 3-113: Screened, serial multi-
conductor cable to board connectors suitable for $10 \mathrm{Gbit} / \mathrm{sec}$ data rates Target dates: CDV 2004-02 FDIS 2004-12
SuggestedRemedy
Add title to reference. Note the target dates: are they timely enough or do you want to refer additionally to the SFF document in the interim, maybe as an informative reference?
Proposed Response Response Status C

## ACCEPT.



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It appears that the CX4 Task Force has created a new form of electrical cable, one that is capable of carrying light.
SuggestedRemedy
Return paragraph to its original form. Insert new subheading ""44.3.1 Fiber delay constraints"" following 44.3. Add new subheading after equation 44-1, ""44.3.2 Copper delay constraints"". Add new text point to a reference for the CX4 delay parameters, or a reference to contact the electrical cable manufacturer.

## Proposed Response Response Status C

ACCEPT IN PRINCIPLE.
Change "... ratio of the speed of light in the fiber or electrical cable to ..." to "... ratio of the speed of electromagnetic propagation in the fiber or electrical cable to ..."

| $C l 45$ | $S C 2$ | $P 194$ |
| :--- | :---: | :---: |
| Bradshaw, Peter | BitBlitz Communicatio | \# 66 |

Comment Type $\quad \mathbf{T}$ Comment Status $\mathbf{X}$

T66
Section 44A7 (including Figure 44A-7) implies a possible loopback ability at all of the PMA, WIS and PCS sublayers (explicitly omitting WIS if not present). Other parts of 802.3ae specify:- PMA loopback (45.2.1.1.4) is mandatory or optional, depending on PMA type, the ability being advertised in bit 1.8.0, loopback is mandatory for a WIS device (if present), and for a 10G-BASE-R PCS, but is forbidden for all other PCS types (45.2.3.1.2)*, and loopback is optional for a PHY XS device (45.2.4.1.2) (advertised in bit 4.24.10), and mandatory for a DTE XS device (45.2.5.1.2), where the 5.24 .10 bit must be $0^{*}$. These awkward inconsistencies (a PHY XGXS and a DTE XGXS are otherwise identical) are enhanced by the addition of the CX4 PMA/PMD, since the functional differences between a CX4 PMA/PMD/PCS device and a DTE XGXS device are mainly some changes to the output and input levels and the SIGNAL_DETECT function, the required register Device Address value changes, and the loopbāk function and advertising scrambling.
*Comments on the comment: a small side bet says that any plausible compliant devices will actually have this loopback/bit, and will have had to hide it somewhere in a vendorspecific register or in some other way.

## SuggestedRemedy

Add a section after page 11 with following:- 1. In 45.2.3.1.2: Remove the prohibition against loopback in a 10GBASE-X PCS device, making it optional. If present, the 3.24.10 bit could be used to advertise its presence, since this register is required in a 10GBASE-X PCS. Current compliant devices are still in compliance, since they do not have the loopback* and the advertising bit would say so. 2. In 45.2.5.1.2: Allow the 5.24.10 bit to optionaly be a 1 , so that a device that can implement both PHY XGXS and DTE XGXS need not change this status bit when changing device address*. Present comforming devices would be allowed to keep this bit a 0 , but it would be recommended that it be a 1.

## Proposed Response Response Status Z

Withdrawn, commentor will consider submitting a maintenance request.

Cl 45 Booth, Brad

Comment Type E Intel

L17

Comment Status A
Remove capitalization.

## SuggestedRemedy

Change all references to ""10G PMA/PMD extended ability register"". Also, remove change ""(Register 1.11)"" to ""(1.11)"" in heading.
Proposed Response Response Status C
ACCEPT IN PRINCIPLE.
Capitalization removed, "(Register 1.11)" kept to remain consistant with the rest of Clause 45.

| Cl 45 | $S C 2.1 .6 .1$ | $P 10$ | $L 5$ |
| :--- | :---: | :---: | :---: |
| Bradshaw, Peter | BitBlitz Communicatio | \# |  |

Comment Type
TR
Comment Status D
TR67

I believe the register 1.7 struture proposed in D4.0 is prefereable to that proposed in D4.1.
The original 802.3ae pattern had some reasonable logical consistency (hobgoblins being ignored), where all 10GBASE-X devices had '00' as the last two bits, and the optical devices all came in the order 'E-L-S' in numerical ascending order. The related ability bits 1.8.0--7 would all encode into the control bits. The D4.0 proposal reatined and expanded this, leaving '0000' for a possible EX4 addition, and '1000' for SX4. Furthermore, the extended 'ability' bits, now ordered 1.8.0...7 and 1.11.0...14, would continue to encode from the control bits. Efficiency in the use of register 1.7 values hardly seems necessary (there are still over 65,000 codes available, and no previous 802.3 speed family has used more than 6 variants: even if we give up to 32 variants to each data rate, and go up by a data rate factor of
SuggestedRemedy
3.2 each time, a.c.w the prior factor of 10, and devote a full bit of 1.7 to each step (rather than a decoded value), the wavelength of the data pattern gets below 100 nm , well into the UV, before 1.7 runs out!) Register 1.8 is to be extended to 1.11, and (leaving bit 1.11.15 for further extension bits to 1.12 with $1.12 .15 \& 1.13$ ), we have a total of 46 more bits, of which one is needed now for CX4, possibly one for SX4, and probably one or two for 10GBASE-T, maybe one for an XFI-type electrical interface. These can fill up 1.11.0-4, leaving 5-14 for expansion. Two nominally independant chages, but preferably done together. 1. Retain the changes proposed for 45.2.1.6.1 in D4.0 2. Modify Table 45-11 on Page 12 of D4.2 to use 1.11.1 for CX4 ability, reserving the other bits.

## Proposed Response <br> Response Status Z

Withdrawn by commentor.
Note: This is a non-binding comment because the commentor was not a voting member at the time of the comment.

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| Cl $54 \quad$ SC 0 | $P$ | $L$ | $\# 47$ |
| :--- | :---: | :---: | :---: |
| Baumer, Howard | Broadcom Corp. |  |  |
| Comment Type T | Comment Status A |  |  |

It is the commentor's opinion that the current link budget as specified in Clauses: 54.7.3.8 Transmitter jitter, 54.8.2 Cable assembly insertion loss, 54.8.3 Cable assembly return loss, 54.8.4.1 Differentialk near end crosstalk, 54.8.4.2 Multiple disturber near end crosstalk, 54.8.5.1 Equal Level Far-End Crosstalk (ELFEXT) loss, 54.8.5.2 Multiple Disturber Equal Level Far-End Crosstalk (MDELFEXT) loss do not produce an error free system with a BER of $10^{\wedge}-12$ or better. This is based on simulations run with these limits.

## SuggestedRemedy

Further simulations with adjusted limits should need to be run and the appropriate limits that create a error free system (to a BER of better than $10^{\wedge}-12$ ) found. This is in support of comment \#388 against draft 4.0.


Management is always optional, as well as the MDIO being optional. See e.g. 28.5.3.

## SuggestedRemedy

Change to ""... shall be integrated with the appropriate physical sublayers (see Table 54-1) and may be integrated with the management ...
Proposed Response Response Status C

## REJECT.

Not all management functions are optional (e.g. transmit disable). The access of the management functions through the Clause 45 MDIO interface is optional as stated.

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| Cl 54 | SC 7.3.6 | P28 | L6 |
| :--- | ---: | :---: | :---: |
| Dawe, Piers | Agilent |  | \# 57 |
| Comment Type | TR | Comment Status A |  |
| Com57 |  |  |  |

This continues my comment 418 against D4.0. Rationale is this: we want the ""shall""s and the PICS to certify what the compliant product does, all the time, not that $100 \%$ testing is required. We leave implementers room to use margin, ""right by design"" and test reduction strategies to build cost-effective product. We have struggled with similar wordsmithing issues in EFM. I think the remedy below gives you what need.

## SuggestedRemedy

Instead of saying: ""These measurements are to be made for each pair while observing the differential signal output at TP2 using the transmitter test fixture shown in Figure 54-3 and with all other transmitters disabled."" please change to: ""The template \{is met|shall be met\} for each differential signal output at TP2 using the transmitter test fixture shown in Figure 54-3, when the three other transmitters are disabled.""
Proposed Response Response Status C
ACCEPT IN PRINCIPLE.
Will remove the sentence: "These measurements are to be made for each pair while observing the differential signal output at TP2 using the transmitter test fixture shown in
Figure 54-3 and with all other transmitters disabled." Will modify "... test pattern specified in 48A.2." to "... test pattern specified in 48A.2, with all other transmitters disabled."


Peak-peak jitter should be expressed as magnitude (positive value).

## SuggestedRemedy

Remove "+/-" in all references to peak-peak jitter values. Also apply changes to
corresponding PICS items in section 54.12.4.3 (DS15, 16, and 17).
Proposed Response
Response Status C
ACCEPT.


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Heading should use lowercase.

## SuggestedRemedy

Change to read ""Connector pin assignments"". Apply similar change to 54.9.3.2.
Proposed Response Response Status C

ACCEPT.

| $C / 54$ | $S C 8$ | $P 31$ | $L 18$ |
| :--- | ---: | ---: | ---: |
| Dawe, Piers | Agilent |  | \# |
|  |  |  |  |

Comment Type T Comment Status A TR31
Line 10 says TP1 and TP4 while this table says TP2/TP3.
SuggestedRemedy
Reconcile.
Proposed Response Response Status C ACCEPT.

Table item is being removed from the response to comment \#484 against D4.0.

| Cl 54 | SC 8 | P31 |
| :--- | :---: | :---: |
| Dove, Daniel | HP ProCurve Networki | \#31 |

Comment Type TR Comment Status R TR31
Erroneous reference (TP2 and TP3)
SuggestedRemedy
Change ""(TP2 and TP3)"" to ""(TP1 and TP4)""
Proposed Response Response Status C
REJECT.
Table item is being removed from the response to comment \#484 against D4.0.
""The impedance for the cable assembly, shall be recorded at half the length of the cable but not to exceed 1ns away from the MDI."" Problem 1: you can't put a ""shall"" under an informative table. Problem 2: ""shall be recorded"": like, keep records of every cable? For how many years? Problem 3: a cable more than a very few feet long will have its midpoint more than 1 ns from either end. Which do you mean, mid-point or 1 ns from an end?
SuggestedRemedy

1. Move the impedance requirement to the normative section 54.8.1. 2. Something like ""cable impedance is defined at <position>"" or better, see below 3. 1 ns from each MDI or at the mid-point if cable is shorter than 2 ns ? or better I think, don't take a TDR approach: just define the impedance looking into TP1 with TP4 terminated by the test fixture (and looking into TP4 with TP1 terminated ...).
Proposed Response Response Status C

## REJECT.

Table item is being removed from the response to comment \#484 against D4.0.


Last sentence appears to have been cut-n-pasted into the wrong locations.

## SuggestedRemedy

Remove sentence ""This includes..."" Other locations; P34-L6, P34-L25, P36-L8, P36-L27
Proposed Response Response Status C
ACCEPT.

TYPE: TR/technical required T/technical E/editorial COMMENT STATUS: D/dispatched A/accepted R/rejected SORT ORDER: Clause, Page, Line, Subclause RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

Page 9 of 12
Cl 54 SC 8.3


TYPE: TR/technical required T/technical E/editorial COMMENT STATUS: D/dispatched A/accepted R/rejected SORT ORDER: Clause, Page, Line, Subclause RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn


TYPE: TR/technical required T/technical E/editorial COMMENT STATUS: D/dispatched A/accepted R/rejected SORT ORDER: Clause, Page, Line, Subclause RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

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Cl $54 \quad S C$ 9.4.6

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SuggestedRemedy
Make them bigger, removing the second and third decimal places.
Proposed Response Response Status C
ACCEPT IN PRINCIPLE.
See comment \#28

| Cl 54 | $S C$ Figures 54-5 to 54-10 | $P$ | $L$ | \# |
| :--- | :---: | :---: | :---: | :---: |
| Bradshaw, Peter | BitBlitz Communicatio |  |  |  |

Comment Type E Comment Status R E64
Many Figures still have Howard's nice 'grey box' surrounding them. In particular, this is true of Figures 54-5, 54-6, 54-7, 54-8, 54-9 \& 54-10 This is inconsistent with similar figures in other 802.3 documents e.g. Figures 47-4, 47-5, 47-6, 52-4, 52-8 \& 52-15. Although consistency may be the hobgoblin of small minds, that may be all that are available...
SuggestedRemedy
Remove the grey boxes (perhaps easier than expanding our minds!).
Proposed Response Response Status C REJECT.

RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn

TYPE: TR/technical required T/technical E/editorial COMMENT STATUS: D/dispatched A/accepted R/rejected SORT ORDER: Clause, Page, Line, Subclause

## SORT ORDER: Clause, Page, Line, Subclause

