Figure 1, for Tom Lindsay comment to D1.0

Sine jitter source

Modulation port

Same settings as comprehensive stressed sensitivity test

Frequency Synthesizer

Clock Source

Pattern Generator

Bessel-Thomson filter

E/O Converter

Optical Attenuator

TP3

System Under Test

PCS (RX)

PMA (RX)

PMD (RX)

Mode Conditioning Patch Cord

[Editor's note: Signal characterization/calibration to be included]