

Minutes

Attendees:

Ali Ghiasi, Broadcom
Pete Anslow, Ciena
Mike Dudek QLogic Corporation
Rick Rabinovich Alcatel Lucent
Richard Mellitz Intel
Charles Moore Avago Technologies
Megha Shanbhag TE Connectivity, Ltd.
Vinu Arumugham Cisco
John Petrilla Avago
David Chalupsky Intel
Wheling Cheng, Juniper

Minutes:

Call for patents , Approval of 073013 minutes

Reviewed latchman_01_080613_caui

- Transmitter equalizer proposal
 - 3 pre and post settings proposed which also fit with COM implementation
 - Tx equalizer characterization from CL93 may require modification for CAUI application
 - VMA methodology also an option
 - Ali will look into contribution on if change of CL93 is necessary and what values should be used
 - Target closure of spec methodology and Tx parameters during Aug 13th call
- Interference tolerance parameters
 - presented simulation of receiver tolerance using 15dB channel with potential minimum broadband noise and RJ
 - TBD remain on channel insertion loss and COM of ISI channel. This will depend on COM implementation and agreed channel loss target
- COM Discussion
 - CTLE implementation in COM different from proposal for chip-to-chip (COM version with CAUI-4 EQ proposal in development)
 - Discussed the use of COM model without Rx package assumption
 - allows designers to trade off package characteristics with Rx design
 - most similar to interference tolerance test where we use a reference receiver with CTLE to characterize the signal
 - update to COM which enables change of Rx package assumption is in process