

Agenda

- Patent Policy:
 - The meeting is an official IEEE ad hoc. Please review the patent policy at the following site prior to the meeting.
<http://www.ieee802.org/3/patent.html>
- Chip-Chip Discussion
 - Current Status
- Chip-Module Draft Baseline
 - Specification BER discussion

Current Status of Chip-Chip

- Last CAUI call highlighted interest in Tx emphasis and Rx CTLE reach potential
 - 15dB estimate by a number of individuals
 - Follow up measurements encouraging
 - Email exchange on limiting tap weights in DFE to limit probability of burst error
- Application Targets: ~15dB – 17dB at 12.89GHz
 - Annex 83A – 25cm (or ~10inches) over PCB
 - 1.59dB loss/inch at 12.89GHz we get 15.9dB + connector (~1dB) = ~17dB
 - Meg6_HighSR-Narrow (kochuparambil_01_0112)
 - ImpFR4_HighSR-Wide: 1.52dB/inch at 12.89GHz
 - OIF SR: 15.4dB
 - OIF MR Target: ~20dB
- Simulation & Measured results to solidify specification
 - Leverage IEEE and OIF work

Straw Poll: Eye Opening Target

- 1E-12
 - A) Use OIF VSR numbers at 1E-12
 - [0.54UIpp@1E-12](#)
 - 100mV @1E-12
 - B) Use extrapolation of OIF numbers to 1E-12
- 1E-15
 - C) Use OIF VSR numbers
 - [0.54UIpp @1E-15](#)
 - 100mV @1E-15

Straw poll results:

A: 4

B: 3

C:11+1 (late voter)

Attendees & Minutes

Attendees

Rick Rabinovich, Charles Moore, Pete Anslow , Mike Dudek, Ali Ghiasi, David Brown, Dan Dove, Adam Healey, Piers Dawe, Richard Mellitz, Tom Palkert, Wheling Cheng, Mike Li, Adee Ran

Minutes

- Discussed material loss reference to kochuparambil_01_0112
- Noted that OIF SR parameters assumed DFE based Rx
- Reviewed ghiasi_01_1212caui4
 - Concern over amplitude of 900mVppd for next gen processes
 - Tightening jitter by 0.1UIpp may be challenging for ASICs/FPGAs
 - May have flexibility around sensitivity (input amplitude required by slicer) and CTLE gain
 - Action to simulate higher loss channels (15dB)
- Discussed if DFE will be prevented by the spec
 - CTLE vs DFE in the Rx becomes an implementation choice
 - Spec should be flexible on implementation, but prevent MTTFPA issue
 - Note on error statistics could ensure appropriate MTTFPA
 - Limit on DFE taps could ensure appropriate MTTFPA
- Next call: 10am – 11am PT on January 9th