

CAUI-4 Ad hoc

Ryan Latchman, Mindspeed

Agenda

- Patent Policy: The meeting is an official IEEE ad hoc. Please review the patent policy at the following site prior to the meeting.
<http://www.ieee802.org/3/patent.html>
- Approval of Sept 16th 2013 CAUI-4 ad hoc minutes
- CAUI-4 chip-to-chip
 - Presentations:
 - SLi_01_093013_cau
 - mellitz_01_093013
 - Current chip-chip summary
 - Transmitter with eye mask and programmable Tx Equalization (evaluated leveraging CL72)
 - 15dB channel evaluated using COM with ideal Rx package, reference EQ
 - Interference tolerance using reference receiver eye opening
- Chip-4 chip-to-module

Minutes

- Attendees
 - [Kapil Shrikhande DELL](#)
 - [John Petrilla Avagotech](#)
 - [Pete Anslow Ciena](#)
 - Elizabeth Kochuparambil Cisco
 - Tom Palkert Visi
 - Charles Moore Avago Technologies
 - Adee Ran Intel
 - Richard Mellitz Intel
 - Vinu Arumugham Cisco
 - Shaohua Li Brocade
 - Ryan Latchman Mindspeed
 - Ali Ghiasi Broadcom
 - Gary Nicholl Cisco
- Patent Policy
- Approval of Sept 16th 2013 CAUI-4 ad hoc minutes
- SLi_01_093013_caui
 - Loss per inch higher than expected due to via's
 - Conclusion: ~20dB loss target needed for these channels
 - S4P files available on CAUI-4 ad hoc website
- mellitz_01_093013
 - DFE tap weight restrictions didn't impact results very much since optimal value was usually not very large for these channels
 - More stringent COM requirement may help ensure channels do not necessitate a solution which is prone to MTTFPA
 - Ensuring receiver doesn't have an MTTFPA issue is a topic for future discussion
- Actions:
 - Additional host channels requested
 - Ryan, Adam, Adee, Richard to discuss distribution of updated COM code
- Chip-module
 - Discussed allocating MDIO registers for host to communicate reference receiver setting for host evaluation
 - Discussed modifying module compliance test
 - Add a low loss test
- Next meeting: Oct 14th at 9am