

**Combined PHY Link ad hoc and PHY sub-Task Force conference call – Minutes
Wednesday, October 23, 2013**

Meeting opened: 11:06 AM pacific

Meeting closed: 11:47 AM pacific

IEEE-SA Call for potentially essential patents. Refer to:

- <https://development.standards.ieee.org/myproject/Public/mytools/mob/slideset.pdf>
- Response to call: no response to call

Presentation Items

- Update: Mark, Avi:
 - [Update to Downstream PHY High Level Block Diagram – R03](#)
 - Took comments during call, will update for next conference call
 - Question of scrambler before/after FEC – placed after so as to make sure parity bits of LDPC codeword are properly randomized. Scrambler will be a synchronized scrambler.

Discussion

- **TDD**
 - Use of downstream markers discussed
 - Concept of using upstream burst mode FDD for downstream TDD
 - PMD for RF on/off issues needs to be specified

- **Work Items list from York interim**
 - http://www.ieee802.org/3/bn/public/workitems_3bn_01a_0913.pdf

Work Plan / Slides Review / Update

- No updates were made to the slides.

Attendance

Person	Employer / Affiliation
Mark Laubach	Broadcom
Syed Rahman	Huawei
Michel Allard	Cogeco
Hesham ElBakoury	Huawei
Marek Hajduczenia	Brighthouse Networks
Leo Montreuil	Broadcom
Steve Shellhammer	Qualcomm
Jin Zhang	Marvell