

# Approved minutes P802.3bs 400 Gb/s Ethernet Electrical Ad Hoc Teleconference 19<sup>th</sup> December 2016

Minutes taken by Andre Szczepanek, Inphi

The meeting started at 8:04 am Pacific chaired by Andre Szczepanek, the attendee list was taken from the Webex attendee list, plus any e-mail notifications of attendance.

Documentation for the call can be found at the Ad Hoc web page:

<http://www.ieee802.org/3/bs/public/adhoc/elect/index.shtml>

Andre reminded everyone of the updated IEEE patent policy (<http://www.ieee802.org/3/patent.html>) and asked if anyone was unfamiliar with it. No one responded.

Andre asked if anyone had any objection or additions to the draft agenda. No one responded, so this agenda was approved by the Ad Hoc.

Andre asked if anyone had any objection or additions to the draft minutes from the 24<sup>th</sup> October 2016 call. No one responded, so the minutes were approved by the Ad Hoc.

## Presentation #1

Title: Electrical Interface Ad Hoc Opening/Agenda

Presenter: Andre Szczepanek, Inphi

[http://www.ieee802.org/3/bs/public/adhoc/elect/19Dec\\_16/szczepanek\\_01\\_121916\\_elect.pdf](http://www.ieee802.org/3/bs/public/adhoc/elect/19Dec_16/szczepanek_01_121916_elect.pdf)

## Presentation #2

Title: Four lane interleaving

Presenter: Peter Anslow, Ciena

[http://www.ieee802.org/3/bs/public/adhoc/elect/19Dec\\_16/anslow\\_01\\_121916\\_elect.pdf](http://www.ieee802.org/3/bs/public/adhoc/elect/19Dec_16/anslow_01_121916_elect.pdf)

The presentation outlined an issue with 4-way interleaving of 200G or 400G FECLs that causes a reduction of clock content for the scrambled Idles pattern, and proposes a solution.

## Presentation #3

Title: Alignment of Tx Jitter specifications, COM, and Rx interference/jitter tolerance tests

Presenter: Adeo Ran, Intel

[http://www.ieee802.org/3/bs/public/adhoc/elect/19Dec\\_16/ran\\_01\\_121916\\_elect.pdf](http://www.ieee802.org/3/bs/public/adhoc/elect/19Dec_16/ran_01_121916_elect.pdf)

This presentation related to a number of comments on Draft 2.2 submitted by Adeo, relating to inter-relation of various parameters with the COM model.

As there was a short period of time left after Adeo's presentation, Andre presented comment 12 on page 7 from the D2.2 Comments (by clause) file and asked Adeo to

explain his concerns and Yasuo and Piers to respond.

[http://www.ieee802.org/3/bs/comments/P802d3bs\\_D2p2\\_comments\\_rec\\_a\\_CI.pdf](http://www.ieee802.org/3/bs/comments/P802d3bs_D2p2_comments_rec_a_CI.pdf)

Before closing he asked Adee, Piers & Yasuo to discuss the issue off-line.

In the closing minutes Ali Ghiasi indicated he had some new information on the QSFP issue that was rejected on D2.0. Andre indicated that discussion of the issue at the January interim would only be dependent on completion of all other business.

The meeting adjourned at 9:55am Pacific.

#### Attendee list (taken from Webex attendee list)

Andre Szczepanek	inphi
Georgios Takos	APM
Peter Anslow	Ciena
Mark Gustlin	Xilinx
Adee Ran	Intel
Ali Ghiasi	Independent
Megha Shanbhag	TE
Yasuo Hidaka	Fujitsu
Matt Traverso	Cisco
David Ofelt	Juniper
Piers Dawe	Mellanox
Gary Nicholl	Cisco
Matt Brown	APM
Bill Kirkland	Semtech
Nathan Tracy	TE
John D'Ambrosia	FutureWei
Oded Wertheim	Mellanox
Rob Stone	Broadcom
Gianpiero Maria Bognanni	Cisco
Mark Nowell	Cisco
David Malicoat	Kazan Networks
Thomas Sellinger	APM
Zvi Rechtman	Mellanox
Bart Zeydel	APM
Kumaran Krishnasamy	Broadcom