

Unapproved minutes P802.3bs 400 Gb/s Ethernet Electrical Ad Hoc Teleconference 28th August 2017

Minutes taken by Andre Szczepanek, HSZ Consulting Ltd

The meeting started at 8:04 am PFT chaired by Andre Szczepanek, the attendee list was taken from the Webex attendee list, plus any e-mail notifications of attendance.

Documentation for the call can be found at the Ad Hoc web page:

<http://www.ieee802.org/3/bs/public/adhoc/elect/index.shtml>

Andre reminded everyone of the updated IEEE patent policy

(<http://www.ieee802.org/3/patent.html>) and asked if anyone was unfamiliar with it. No one responded.

Andre presented the updated (V5) IEEE participation Foil.

Andre asked if anyone had any objection or additions to the draft agenda. No one responded, so this agenda was approved by the Ad Hoc.

Andre asked if anyone had any corrections to the draft minutes from the 24th July 2017 call. No one responded, so these minutes are approved by the Ad Hoc.

Presentation #1

Title: Electrical Interface Ad Hoc Opening/Agenda

Presenter: Andre Szczepanek, HSZ Consulting

http://www.ieee802.org/3/bs/public/adhoc/elect/28Aug_17/szczepanek_01_082817_elect.pdf

Presentation #2

Title: Package impedance and termination effect on COM (update based on D3.3)

Presenter: Mike Dudek, Cavium

http://www.ieee802.org/3/bs/public/adhoc/elect/28Aug_17/dudek_01_082817_elect.pdf

Mike made the case (again) for a higher value of COM for the channel than that used for the calibration of the interference tolerance test, based on additional simulations. Andre asked whether it would holding a straw poll on the reflector to see if there had been any increase in support for such a change. Mike didn't want to do this. Andre suggested Mike gain supporters for his presentation prior to the interim meeting.

Presentation #3

Title: The high-frequency pole of the reference CTLE in Annex 120E (comment #r03-12)

Presenter: Yasuo Hidaka, Fujitsu

http://www.ieee802.org/3/bs/public/adhoc/elect/28Aug_17/hidaka_01_082817_elect.pdf

There was some agreement that options #1 or #3 would be an improvement.

Andre then presented two of the new comments received on D3.3, for discussion by the ad hoc in the remaining time.

- 1) R03-46: (Optimum CTLE peaking must be at least 8dB). Mike Dudek asked if Yasuo could re-run his simulations (presentation #2 above) to see the difference a fixed 8dB CTLE would make.
- 2) R02-45: (Reduce the 14.2dB hardware channel loss).

Andre announced that he would ask the TF Chair if he could delay the (already announced) 4th September ad hoc, to the 8th September, to avoid the US National holiday. (This was subsequently agreed).

The meeting adjourned at 9:59 am Pacific.

Attendee list (taken from Webex attendee list)

Andre Szczepanek	HSZ Consulting
Yasuo Hidaka	Fujitsu
Amy Kao	Luxshare-ICT
Yang Zhiwei	ZTE
Phong Pham	USconec
Mike Dudek	Cavium
Tom Palkert	Independent
Howard Heck	Intel
Jaclyn Dang	Cisco
Rohan Gandhi	Macom
Gianpiero Bognanni	Source Photonics
Tao Hu	Cavium
Ali Ghiasi	Independent
Adam Healey	Broadcom
Vital Balasubramanian	Innovium
T Sakai	Socionext
Stephen Didde	Keysight
Piers Dawe	Mellanox
Anand Anandakumar	XML
Ed Fraln	Semtech
Open Kareti	Cisco
Ken Jackson	SEI-device
Mega Shanbhag	TE
Michael Ressler	Hiatchi-cable
Martin White	Cavium

John Ewen
Nathan Tracy
David Malicoat
Jane Lim
Karen
Takeshi Nish
Rock Rabinovich
Galen Fromm
Richard Mellitz

Global Foundries
TE
Senko
Cisco
Kaiaam
Yeu
Ixia
Cray
Samtec