

IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference December 11th, 2015

Minutes taken by Mark Gustlin, Xilinx

The meeting started at 8:02 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page:
<http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml>

Mark showed the patent link and asked if anyone had any questions, no one responded.

Presentation #1

FEC Modes – Phil Sun

Presentation #2

400GbE AMs and PAM4 test pattern characteristics – Pete Anslow

Presentation #3

Marker Lock/Unlock Schemes for 400GE – Zhongfeng Wang

Attendees (taken from webex, please let me know if you have a correction or addition):

Mark Gustlin, Xilinx

Ben Jones, Xilinx

Eric Fortin, ALU

Sam Sambasivan, AT&T

Jeff Slavick, Avago Technologies

Eric Baden, Broadcom

Andy Moorwood, Ericsson

Tongtong Wang, Huawei

Pete Anslow, Ciena

Xinyuan Wang, Huawei

Tom McDermott, Fujitsu

David Malicoat, HPE

Scott Irwin, MoSys Inc

Q. Zhang, Cisco

Kumaran Krishnasamy,
Broadcom

Brian Teipen, Adva

Phil Sun, Credo Semiconductor

Bill Wilkie, Xilinx

Paul Mooney, Spirent

Martin White, Cavium

Guylain Barlow, Viavi

Dave Ofelt, Juniper
Mike Dudek, Qlogic
Ali Ghiasi, Independent
Slobodan Milijevic, Microsemi
Will Bliss, Broadcom
Dominic Lapierre, Exfo
Mark Nowell, Cisco
John Dillard, Microsemi
Gary Nicholl, Cisco
Steve Trowbridge, Alcatel-
Lucent
Kenneth Jackson, Sumitomo
Geoffrey Chacon, HPE
John Ewen, Global Foundries
Wheling Cheng, Ericsson
Mark Gravel, HP
JC Calderon, Inphi
Rob Stone, Broadcom
Piers Dawe, Mellanox
Rich Mellitz, Intel
Rajiv Pancholy, Broadcom
David Ofelt, Juniper
Zhongfeng Wang, Broadcom
Dino Pozzebon, PMC-Sierra
Vasu Parthasarathy, Broadcom
Jeffery Maki, Juniper
Shaohua Li, Broadcom