

Approved minutes

IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference February 17th, 2015

Minutes taken by Mark Gustlin, Xilinx

The meeting started at 8:02 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page:
<http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml>

Mark showed the patent link and asked if anyone had any questions, no one responded.

Presentation #1

Title: Logic Big Ticket Items – Mark Gustlin

See: [gustlin_02_0215_logic.pdf](#)

Some discussion around why 100G KP4 required the termination bits. Also discussion around if we need them in some PMD if not for the electrical interfaces. For KP4 it was a very receiver centric choice based on the channel having very poor SNR, not likely to face that situation in this project.

It was asked that since most PMDs are random errors, then why focus on a RS FEC instead of something else? Some discussion around the fact that we want to cover an end to end link, including the electrical interfaces that do have burst errors. And that all PMDs having only random errors is still to be shown.

Presentation #2

Title: Proposal for 400GbE FEC Architecture - Xinyuan Wang

See: [wangx_01_0215_logic.pdf](#)

It was asked how nx25G, 50G, 200G could impact his conclusion, it would need to be investigated.

Attendees (taken from webex, please let me know if you have a correction or addition):

Andre Szczepanek, Inphi

Mark Gustlin, Xilinx

Ghani Abbas, Ericsson

Derek Cassidy, BT

Tom McDermott, Fujitsu

Eric Baden, Broadcom

Tongtong Wang, Huawei

Derek Cassidy, BT

Zvi Rechtman, Melanox

Andy Moorwood, Ericsson

Xinyuan Wang, Huawei

Yasuo Hidaka, Fujitsu

Steve Trowbridge, Alcatel-Lucent
Paul Mooney, Spirent
Karl Bois, HP
Piers Dawe, Mellanox
John Ewen, IBM
Michael Ressler, Hitachi Cable
Pete Anslow, Ciena
Haoli Qian, Credo Semiconductor
Rita Horner, Synopsys
Pi Boson, ?
Adrian Butter, IBM
Dave Brown, Semtech
Rick Rabinovich, Alcatel-Lucent
Tom Palkert, Xilinx, Luxtera, Molex
Rich Mellitz, Intel
Brian Teipen, Adva
Mike Dudek, Qlogic
Ali Ghiasi, Independent
Jeff Slavick, Avago Technologies
Jishnu De, Synopsys
Martin Bouda, Fujitsu
Gary Nicholl, Cisco
Chad Erven, Semtech
omerzi?, Mellanox
Adee Ran, Intel
Laxmi Narayan, Synposys
David Law, HP
Susan Bueti, IBM
Peter Stassar, Huawei
Paul Kolesar, Commscope
Bert Klaps, Altera
Wheling Cheng, Ericsson
Cedrik Begin, Cisco
Matt Brown, Applied Micro
Benoit Mercier, ST
Microelectronics
Juan-Carlos Calderon, Cortina
David Ofelt, Juniper
Mark Gravel, HP
Susan Bueti, IBM
Brian Welch, Luxtera

Zhongfeng Wang, Broadcom
Oded Wertheim, Mellanox
Andre Szczepanek, Inphi
Mark Gustlin, Xilinx
Ghani Abbas, Ericsson
Derek Cassidy, BT
Tom McDermott, Fujitsu
Eric Baden, Broadcom
Tongtong Wang, Huawei
Derek Cassidy, BT
Zvi Rechtman, Mellanox
Andy Moorwood, Ericsson
Xinyuan Wang, Huawei