

IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference February 9th, 2016

Minutes taken by Mark Gustlin, Xilinx

The meeting started at 8:02 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page:
<http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml>

Mark showed the patent link and asked if anyone had any questions, no one responded.

Presentation #1

Proposed AM Format – Mark Gustlin

Attendees (taken from webex, please let me know if you have a correction or addition):

Thananya Baldwin, Ixia
Jerry Pepper, Ixia
Andre Szczepanek, Inphi
Will Bliss, Broadcom
Mark Gustlin, Xilinx
Geoffrey Chacon, HPE
Daniel Koehler, MorethanIP
Rick Rabinovich, Ixia
Eric Fortin, ALU
Upen Reddy, Cisco
Mike Dudek, Qlogic
Steve Trowbridge, Alcatel-Lucent
Adrian Butter, Global Foundries
Ghani Abbas, Ericsson
Ben Jones, Xilinx
Derek Cassidy, BT
Bill Wilkie, Xilinx
Pete Anslow, Ciena
Phil Sun, Credo Semiconductor
David Ofelt, Juniper
Gary Lim, Charter
Dominic Lapierre, Exfo
Gary Nicholl, Cisco
Eric Baden, Broadcom
Kenneth Jackson, Sumitomo
David Malicoat, HPE
Wheling Cheng, Ericsson

Pavel Zivny, Tektronix
John Stahl, Acacia Communications
Ali Ghiasi, Independent
Tom McDermott, Fujitsu
Piers Dawe, Mellanox
Rita Horner, Synopsys
JC Calderon, Inphi
Martin White, Cavium
Vasu Parthasarathy, Broadcom
Martin Langhammer, Altera
Steve Gorshe, PMC-Sierra
John Dillard, Microsemi
Mark Gravel, HP
Matt Brown, Applied Micro
Zvi Rechtman, Mellanox
John Dambrosia, FutureWei
technologies
Zhenyu Liu, Credo Semiconductor