

IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference January 8th, 2016

Minutes taken by Mark Gustlin, Xilinx

The meeting started at 8:02 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page:
<http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml>

Mark showed the patent link and asked if anyone had any questions, no one responded.

Presentation #1

400GbE AMs revised proposal – Pete Anslow

Some discussion around if the AMs need to end of a 10b boundary or not, this will be looked into.

Some discussion around the 6b pad, does it need to be a PRBS9 or not, other options can be looked into.

Presentation #2

Pre-FEC BER Monitoring and Signaling – Dave Ofelt

Some discussion on if we can identify which lane a particular error comes from due to the muxing that occurs.

Some discussions about what the requirements are for the mechanism from a timing point of view, how responsive must it be, msec, usec etc.

Attendees (taken from webex, please let me know if you have a correction or addition):

Mark Gustlin, Xilinx

Eric Fortin, ALU

Pete Anslow, Ciena

Eric Baden, Broadcom

Daniel Koehler, MorethanIP

Andre Szczepanek, Inphi

Ben Jones, Xilinx

Tongtong Wang, Huawei

Steve Trowbridge, Alcatel-
Lucent

Sam Sambasivan, AT&T

Paul Mooney, Spirent

Adrian Butter, Global
Foundries

Mike Dudek, Qlogic

David Ofelt, Juniper

Bill Wilkie, Xilinx

Mark Kimber, Semtech

Brian Teipen, Adva

John Dillard, Microsemi
Gary Nicholl, Cisco
Gary Lim, Charter
Derek Cassidy, BT
Michael Ressler, Hitachi Cable
Ali Ghiasi, Independent
Martin White, Cavium
Q. Zhang, Cisco
Vivek Telang, Broadcom
David Malicoat, HPE
Geoffrey Chacon, HPE
Phil Sun, Marvell
CJ Chen, Broadcom
Vasu Parthasarathy, Broadcom
Piers Dawe, Mellanox
Rita Horner, Synopsys
Mark Gravel, HP
David Law, HPE
Karen Liu, Kaiam
Kenneth Jackson, Sumitomo
Ed Ulrichs, Source Photonics
Raymond Nering, Cisco