

IEEE P802.3bs 200 Gb/s and 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference March 2nd, 2017

Minutes taken by Mark Gustlin, Xilinx

The meeting started at 7:32 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page:

<http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml>

Mark showed the patent link and asked if anyone had any questions, no one responded.

Mark also showed the participation requirement slide.

Presentation #1

Possible 7b scrambler text changes: Mark Gustlin - Xilinx

Presentation #2

Generating Variable Transition Density Patterns: Ali Ghiasi, Phil Sun - Credo

Attendees (taken from webex, please let me know if you have a correction or addition):

Tom McDermott, Fujitsu

Andre Szczepanek, Inphi

Mark Gustlin, Xilinx

Piers Dawe, Mellanox

Steve Trowbridge, Nokia

Will Bliss, Broadcom

Xinyuan Wang, Huawei

Matt Weber, Acacia Communications

Ted Sprague, Infinera

Ben Jones, Xilinx

Sam Sambasivan, AT&T

Yasuo Hidaka, Fujitsu

Phil Sun, Credo Semiconductor

Tao Hu, QLogic

John Dillard, Microsemi

Ali Ghiasi, Huawei

Stephen Didde, Keysight

Pete Anslow, Ciena

Dino Pozzebon, Microsemi

Mike Dudek, Cavium

Karen Liu, Kaia

Peter Stassar, Huawei

David Law, HP

John D'Ambrosia, FutureWei technologies

David Ofelt, Juniper
Marco Mazzini, Cisco
Rob Stone, Broadcom
John Ewen, Global Foundries
Don Cober, Comira
Anand Anandakumar, Maxlinear
Mark Kimber, Semtech
Zvi Rechtman, Melanox
David Malicoat, Malicoat Networking
Solutions
Rick Rabinovich, Ixia
Kapil Shrikhande, Innovium
Guylain Barlow, Viavi
Michael Ressler, Hitachi Cable
Gary Nicholl, Cisco
Martin White, Cavium