

# Table 33-1 reorg (comment #4)

G. Zimmerman

CME Consulting / Linear Tech &  
Commscope

January 2015

# Table 33-1 in Draft 0.2

*Editor's Note: Type 4 System Parameters are TBD. They will be added in the future.*

**Table 33-1—~~Type 1 and Type 2~~ System parameters**

Parameter	Symbol	Units	Type 1 value	Type 2 or <u>Type 3</u> value	Additional information
Nominal highest DC current per pair	$I_{\text{Cable}}$	A	0.350	0.600 <sup>1</sup>	See section <u>TBD that covers inter- pair unbalance</u>
<u>Channel pair-set</u> maximum DC pair loop resistance	$R_{\text{Ch}}$	$\Omega$	20.0	12.5	
Minimum cable type			twisted-pair cabling per 14.4 and 14.5 <sup>2</sup>	Class D	See , 33.1.4.2

<sup>1</sup>In Type 3, 60W Operation, the current per 2-pair might be impacted by pair to pair system resistance unbalance. See details in Section TBD.

<sup>2</sup>Class D recommended.

# Table 33-1 rearranged

**Table 33-1 System Parameters vs. System Type**

<b>System Type (Lowest type of PSE &amp; PD)</b>	<b>Nominal highest current per pair (<math>I_{\text{cable}}</math>, A)</b>	<b>Channel Pair-set maximum DC loop resistance (<math>R_{\text{chan}}</math>, <math>\Omega</math>)</b>	<b>Minimum Cabling Type<sup>3</sup></b>
<b>Type 1</b>	0.350	20.0	Twisted-pair Cabling per 14.4 and 14.5 (Class D recommended)
<b>Type 2</b>	0.600	12.5	Class D (ISO/IEC 11801:1995)
<b>Type 3</b>	0.600 <sup>1</sup>	12.5	Class D (ISO/IEC 11801:1995)
<b>Type 4</b>	TBD	TBD	TBD

<sup>1</sup> In Type 3, 60W Operation, the current per pair-set might be impacted by pair-to-pair system resistance unbalance. See details in Section TBD.

<sup>2</sup> See Section 33.1.4.2

<sup>3</sup> See informative Annex TBD for inter-pair unbalance

# Other issues to consider fixing

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- Reorganization did not fix the problems, only made the information clearer
  - We describe DC loop resistance per pair, but most industry uses DC loop resistance per conductor – so we have to explain it – why not just fix it here.
  - This entire table appears informative, why not mark it so. (or else make clear what is normative here)