

List of changes to IEEE P802.3bt D3.5 for D3.6

Comment No.	Change
1	Delete a paragraph from the front matter that should have already been deleted (P11, L41)
2	Bringing draft in line with base standard; deleting editorial instructions that have incorporated into the base standard (P38, L22)
3	Missed renumbering, error that needed fixed. (P174, L42) Table 145-16 from the second 18 'onward'; changed from 18-24 to 19-25
4	145.3.8.9: multiple notes in section, number the notes
8	Changing the 'base_year' to match current base standard as IEEE Std 802.3-2018 was approved by the IEEE-SA Standards Board on 14 June 2018.
12	Making it clear that 'PSE power pairs ext' is an active status field instead of a static capability field (P94, L24)
13	'Dual-signature power Class ext Mode A' (P96, L32): Adding instructions for the PD to set the field to zero. Also, fixing the sentence on L34, making it clear that this is instruction for the PSE when connected to a DS PD. Updated PICS as required.
14	Adding the missing max value to Table 145-14 item 9 (P171, L7, TClass_ACS max = T_LCE). Also, P169, L47, change "after TClass_ACS" to "within TClass_ACS" to match the max. Update PICS
15	Clarifying electrical connection for detection signatures (145.3.4, P205, L5): "one pair **of the other Mode** connected to the positive potential"
16	These are DS fields in the SS section. Clarifying that this is set to 0 for SS PDs and deleting redundant DS description (P248, L48)
17	Deleting PICS where requirements were deleted last cycle. (PD33, PD34, P275, L46)
18	Add section references to PSE MPS (145.2.12) and Irev (145.2.10.4), and PD reflected voltage (145.3.8.8) to end of sentence on P223, L34.
19	Remove a paragraph break on P176, L22