

## Proposed changes to 802.3bw 96.6 Management interface

1. Add subclause 96.6.3.

### 96.6.3 PMA and PCS MDIO function mapping

The MDIO capability described in Clause 45 defines several variables that provide control and status information for and about the PMA and PCS. Mapping of MDIO register bits to PMA and PCS control/status variables is shown in Table 96-nn.

2. Add the following table.

Table 96-nn MDIO register bit mapping for reset and loopback control and link status variables

Register name	Register/bit number	control/status variable
PMA/PMD control 1	1.0.15	pma_reset
PMA/PMD control 1	1.0.0	PMA loopback
PCS control 1	3.0.15	pcs_reset
PCS control 1	3.0.14	PCS loopback
PMA/PMD status 1	1.1.2	link_status