Current text:

The SP2 synchronization pattern zone represents the start-of-burst delimiter (SBD). It has the length of one block and the value of 0x1-BF-40-18-E5-C5-49-BB-59-6B-F8-D8-12-D8-58-E4-AB-40-BF-E7-1A-3A-B6 -44-A6-94-07-27-ED-27-A7-1B-54.

Option #1:

The SP2 synchronization pattern zone represents the start-of-burst delimiter (SBD). It has the length of one block (257 bits) and the value of 0x1-BF-40-18-E5-C5-49-BB-59-6B-F8-D8-12-D8-58-E4-AB-40-BF-E7-1A-3A-B6-44-A6-94-07-27-ED-27-A7-1B-54. The transmission bit sequence is:

(first bit) `1 1111 1101 0000 0010 0001 1000 1010 0111`
`1010 0011 1001 0010 1101 1101 1001 1010`
`1101 0110 0001 1111 0001 1011 0100 1000`
`0001 1011 0001 1010 0010 0111 1101 0101`

`0000 0010 1111 1101 1110 0111 0101 1000`
`0101 1100 0110 1101 0010 0010 0110 0101`
`0010 1001 1110 0000 1110 0100 1011 0111`
`1110 0100 1110 0101 1101 1000 0010 1010` (last bit)

Option #2:

The SP2 synchronization pattern zone represents the start-of-burst delimiter (SBD). It has the length of one block (257 bits) and is constructed from the values of *SP* and *BURST_DELIMITER* (see 76.3.2.5.2), as follows:

$$SP2 = 1 + SP<65:2> + BURST\_DELIMITER<65:2> + \overline{SP<65:2>} + \overline{BURST\_DELIMITER<65:2>},$$

where $\overline{SP<65:2>} + \overline{BURST\_DELIMITER<65:2>}$ represent the inverted sequences of SP and BURST_DELIMITER