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Proposed changes to 802.3ca - D2.1 PMA figures 142-19 & 142-20

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Proposed changes to D2.1 PMA Figures 142-19 & 142-20

- Propose to add notes to Figures 142-19 and 142-20 to address concerns expressed by a D2.0 voter
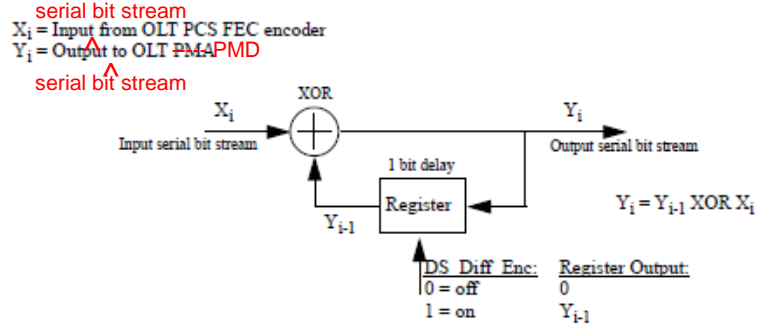

serial bit stream
X_i = Input from OLT PCS FEC encoder
Y_i = Output to OLT PMA PMD
serial bit stream

X_i
Input serial bit stream

XOR

Y_i
Output serial bit stream

1 bit delay
Register
Y_{i-1}

$Y_i = Y_{i-1}$ XOR X_i

DS_Diff_Enc: Register Output:
0 = off   0
1 = on Y_{i-1}

Figure 142–19—Differential encoding

From differential encoding detect function

serial bit stream
X_i = Input from ONU PMA PMD
Y_i = Output to ONU PCS Synchronizer
serial bit stream

DS_Diff_Enc: Register Output:
0 = off   0
1 = on X_{i-1}

X_i
Input serial bit stream
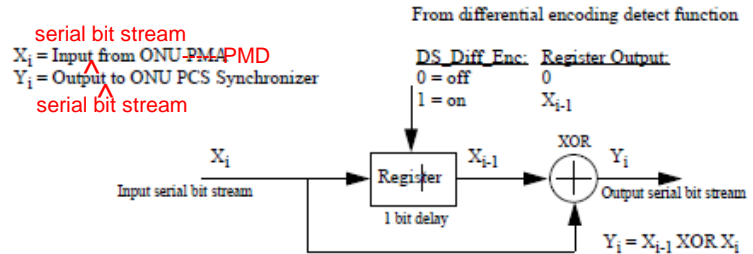
Register
1 bit delay

X_{i-1}

XOR

Y_i
Output serial bit stream

$Y_i = X_{i-1}$ XOR X_i

Figure 142–20—Differential decoding

Current D2.1 PMA figures (black) & proposed changes (Red)

2

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