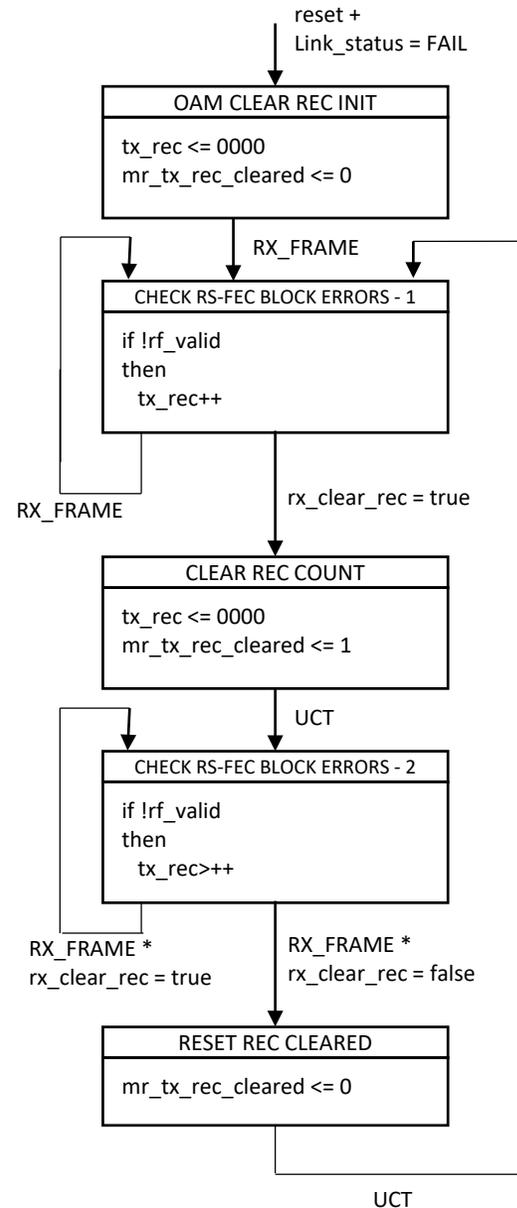
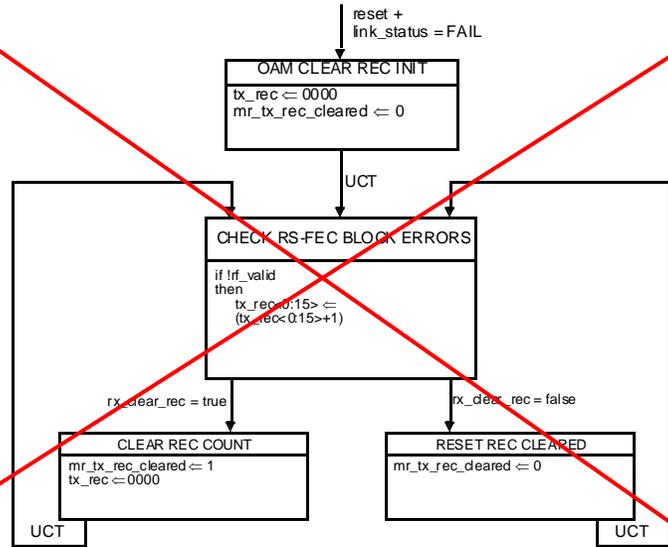


Clear REC state diagram Comment #19

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IEEE802.3ch Interim



This updated Figure corrects identified issues.

Transitions are now based on the reception of a Frame.

The REC counter will be cleared once per request and will start counting again. This will not be reset continually while waiting for the Link Partner to acknowledge the counter was cleared.

Once the clearing of the REC Counter is acknowledge, the counter continues to count errors and waits for a new request to clear the counter.

Email from William Lo: I have a question on the rx_clear_rec variable.

On page 128 it simply states “Request from the link partner to clear the REC counter”

Once this variable goes true, how does it go back to false?

We need to define this otherwise there is an infinite loop in Figure 149-24 that always clears the tx_rec.

A secondary issue is that the loops around figure 149-24 are running at infinite speed and is not paced.

I think the intention is to check the loop once per RS Frame. If we don't do this then tx_rec will keep incrementing once rf_valid is false.