## 149.5.1 Test Modes

### Test mode 2: Transmit MDI Jitter in Master Mode

Test mode 2 is for transmitter jitter testing on MDI when transmitter is in MASTER timing mode. When test mode 2 is enabled, the PHY shall transmit a continuous pattern of JP03A (as specified in Clause **94.2.9.1**) or JP03B (as specified in Clause **94.2.9.2**) with the transmitted symbols timed from its local clock source.

## 149.5.2.3 Transmit Timing Jitter

## 149.5.2.3.2 Transmit MDI Jitter in Master Mode

Jitter measurements in this subclause are performed with transmitter is enabled in Master timing mode with a local clock. To Measure Random Jitter in RMS (RJrms) and Deterministic Jitter (DJ) followsteps as specified in Clause **94.3.12.6.1**, with following modifications:

$fn$ =1MHz x **s**, $T$ = 68ns/**s**     (where **s**=1 @10G, **s**=0.5 @5G, **s**=0.25 @2.5G)

And for Even-odd jitter (EOJ) measurement follow steps as specified in Clause **94.3.12.6.2.**

The calculated transmit timing jitters for RJrms, DJ, and EOJ from above measurements should be:

RJrms = 1ps/**s**,  DJ = 9ps/**s**,    EOJ = 4ps/ **s**    (where **s**=1 @10G, **s**=0.5 @5G, **s**=0.25 @2.5G)