

# Common Mode (CM) Noise: Next Steps

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- ❑ Insight from comparing CM crosstalk responses to crosstalk responses
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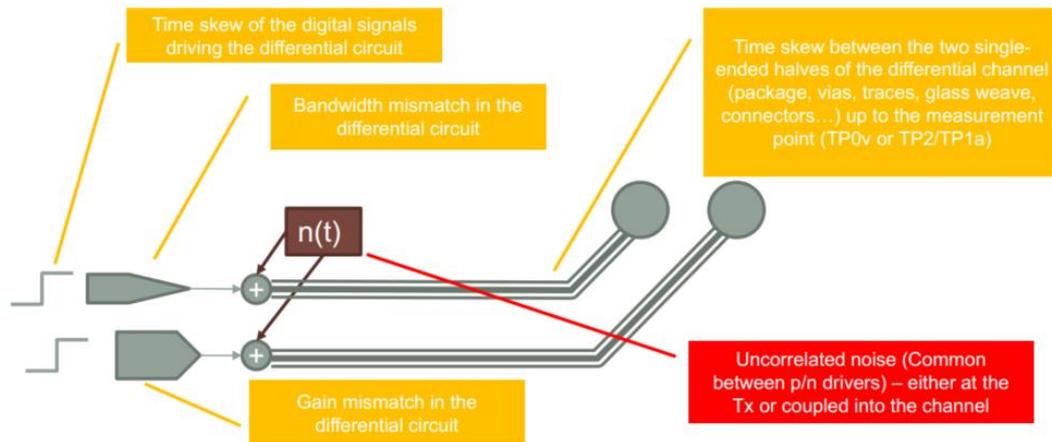
# ran\_3ck\_04\_1020 suggests sources and impact of common mode (CM) noise

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IEEE 802.3ck 3

## Common mode AC output – sources

- Tx for electrical interfaces is typically a fully differential circuit whose output should have constant common mode voltage.
- Typical causes of common mode AC signal in the TX are:



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## Common mode AC output – impact

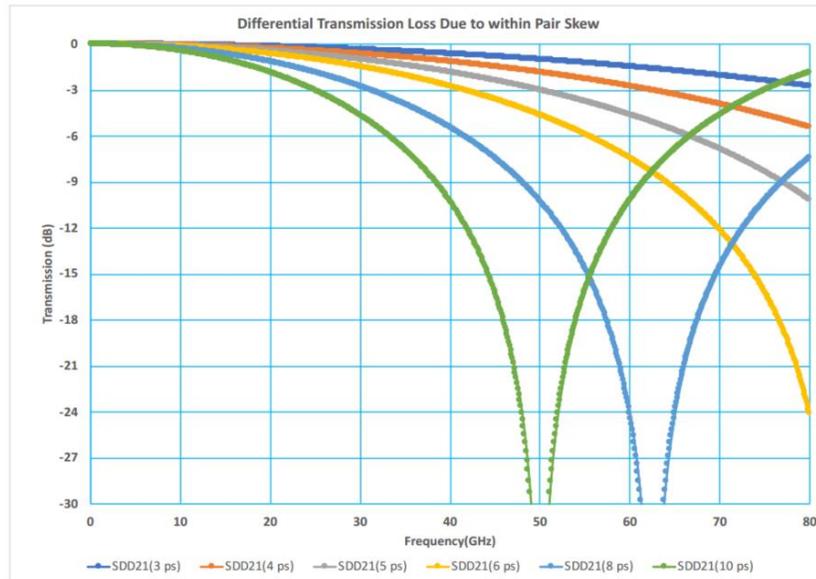
- Conversion of differential signal to **CM signal**
  - Frequency dependent
  - Causes distortion of the differential signal
  - Typically strong at high frequencies
  - Note that the channel (in CR/KR/C2C) can also cause D-C conversion
- Conversion of **CM noise** to **differential noise**
  - Note that the channel (in CR/KR/C2C) can also cause C-D conversion
- Common mode signal/noise impact on the receiver
  - Rx is also differential, but has finite CMRR
- EMI
  - Not performance related; other standards deal with this

What should we really care about?

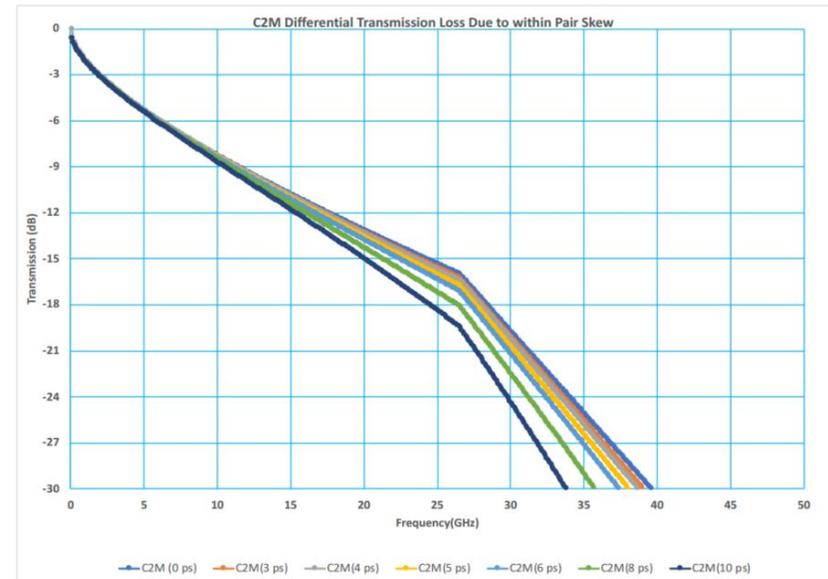
# ghiasi\_3ck\_03a\_0720 also suggest sources of CM Noise

## Sources of Common Mode

- **Driver P/N asymmetry and interconnect P/N mismatch are the two sources of common mode generation**
  - Graph show the theoretical impact of 3-10 ps of skew on C2M IL where the penalty increases with the Baudrate increase , D. Nozadze, IEEE EPEPS, 2017
  - The CK channels already include effects of P/N mismatch but currently COM reference model and package don't excite the common modes and obviously the impact is overlooked at the receiver.



A. Ghiasi

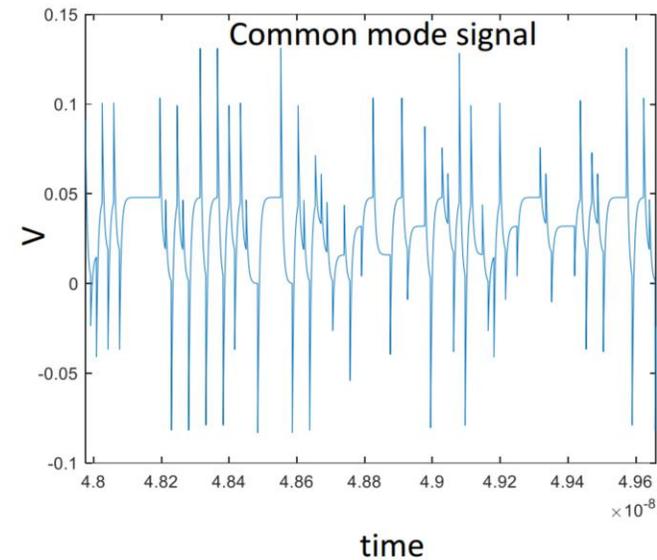
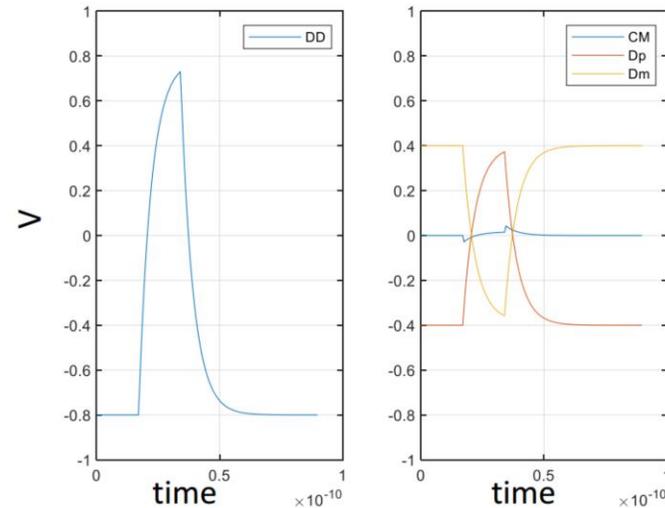


IEEE 802.3ck Task Force

# mellitz\_3ck\_adhoc\_01\_061720 suggest similar CM sources

What might a common signal look like

- Intrapair Voltage Imbalance
- Intrapair Skew
- CM crosstalk

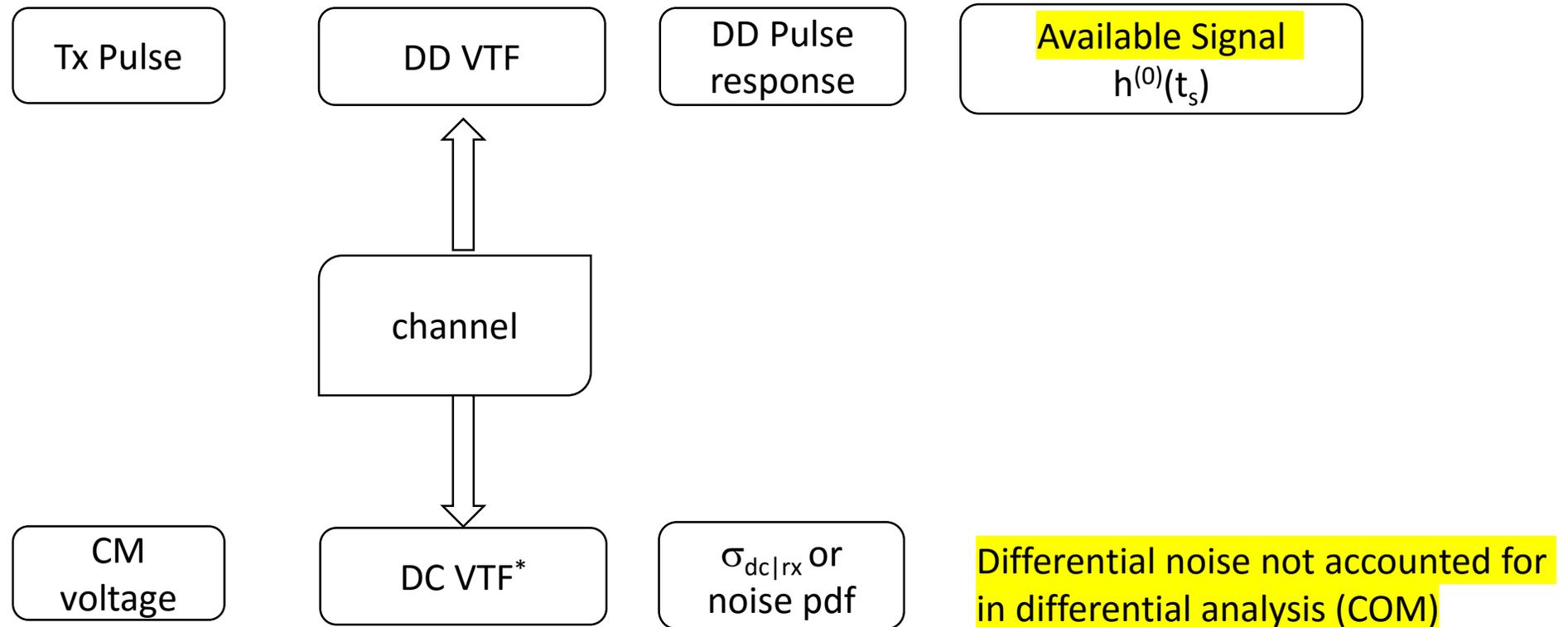


Should spec be an RMS and crest factor?

# Ways to look a CM noise impacting the differential signal at the receiver.

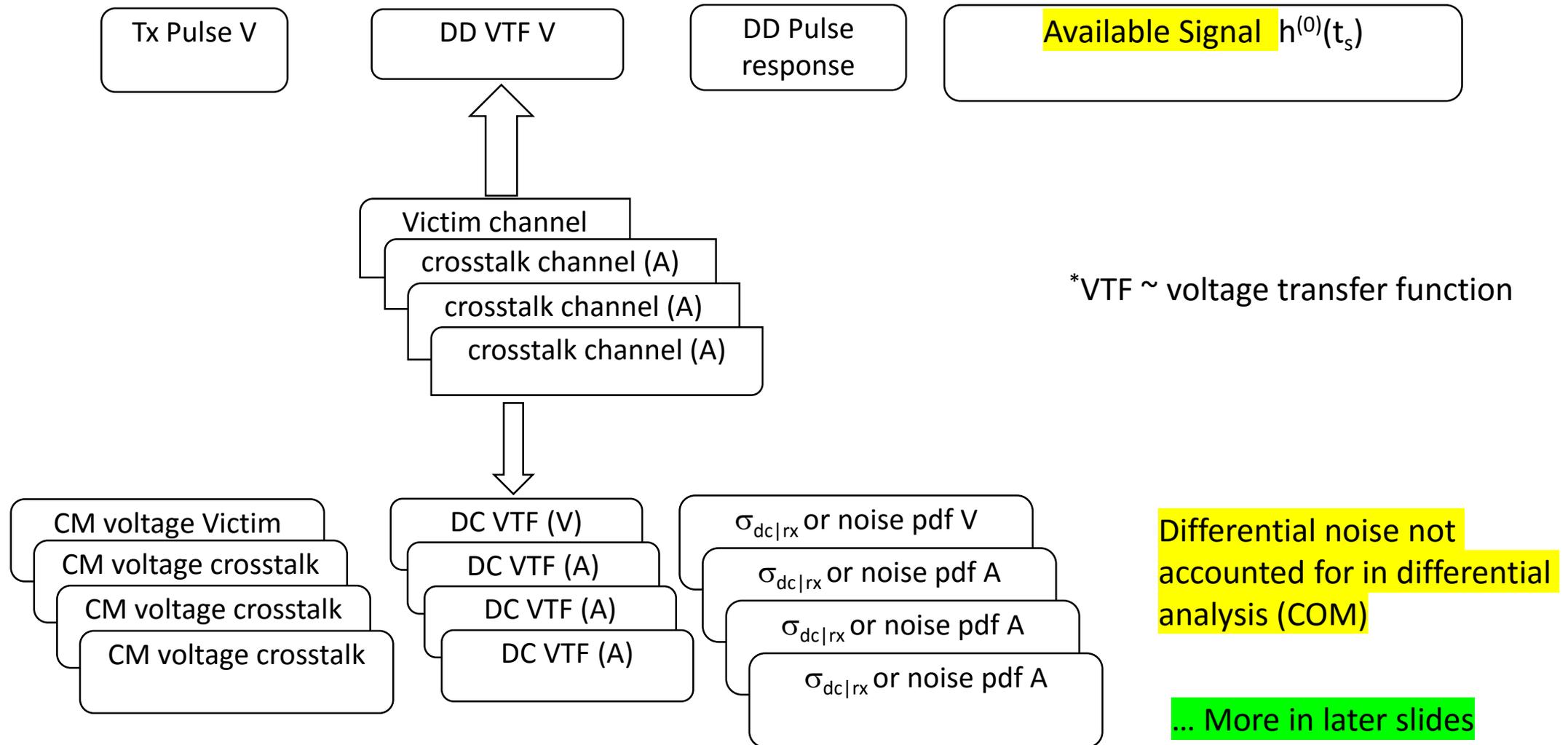
- ❑ Eventually CM noise looks like crosstalk
  - One big question is how much get to into the receiver
- ❑ wu\_3ck\_adhoc\_01\_090920.pdf – good way to get a feel for CM
  - SDC21 peaks
  - SDD21 (dB) – SDC21 (dB)
  - Integrated CM noise due to SDC21
- ❑ mellitz\_3ck\_adhoc\_01\_061720 – method to compute impact
  - Use the common mode to differential mode voltage transfer function
  - Trial impact version is COM
    - However the big question here is what to use as a CM source

# Simple model



\*VTF ~ voltage transfer function

# A little more detail: Should Crosstalk CM be considered



# Getting a feel for understanding if crosstalk CM matters?

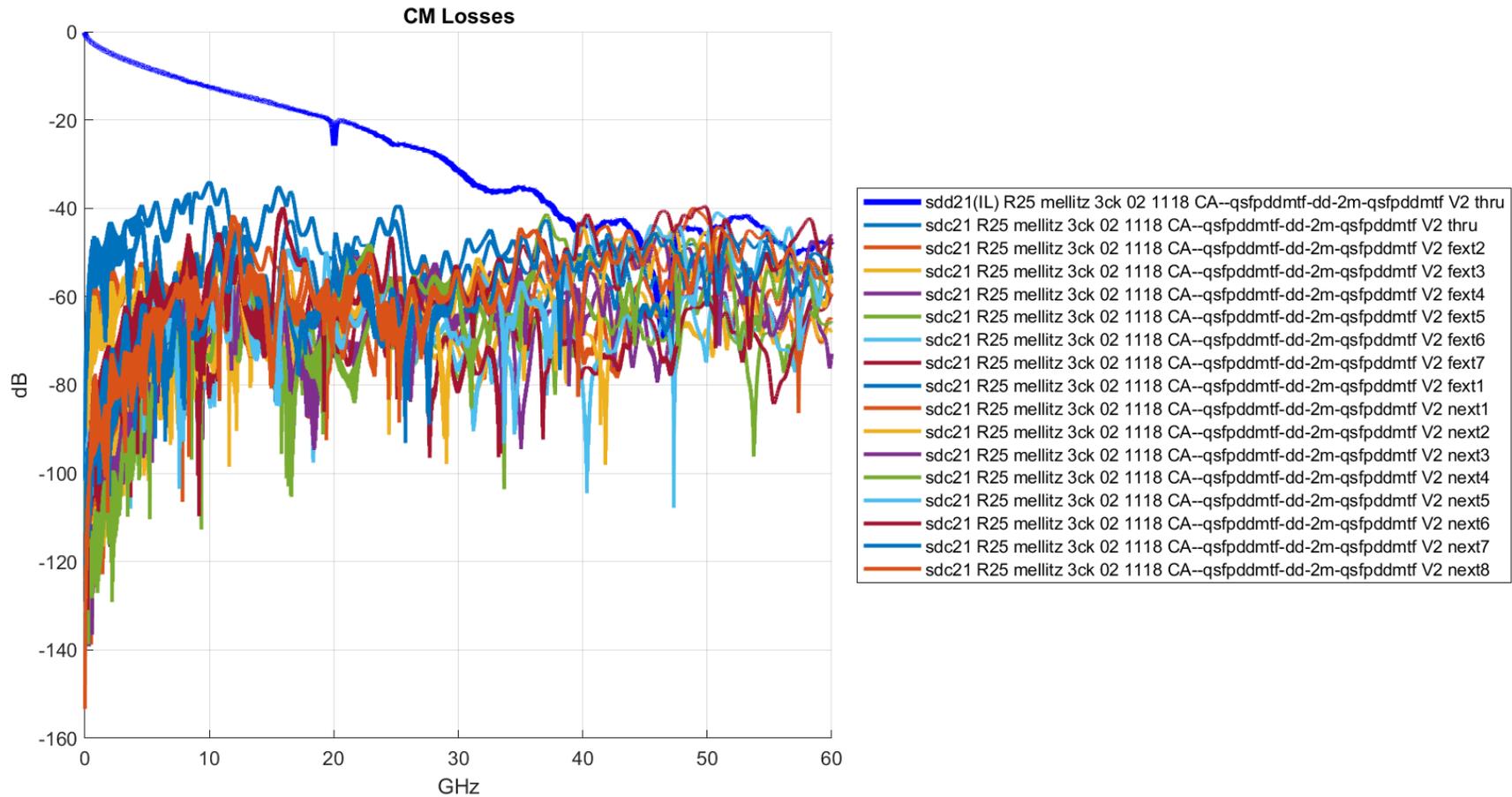
- ❑ crosstalk CM peaks are between 35 and 45 dB
  - For 1.5 m and 2 m cables,
- ❑ CM conversation is are not significant if the CM voltage is low
- ❑ Following 15 slides are snapshots sampling of the CM data
  - This should lend a feel for what channel CM data could be expected

# CM Data

- ❑ Slides of IL and CM loss for collection of cable posted channels
- ❑ Slides of comparison between CM and crosstalk pulse responses
  - Crosstalk uses  $A_{fe}$  and  $A_{ne}$  as pulse voltage
  - CM use 1 V as pulse voltage ... will be adjusted later
- ❑ One slide for on channel comparing crosstalk and CM responses

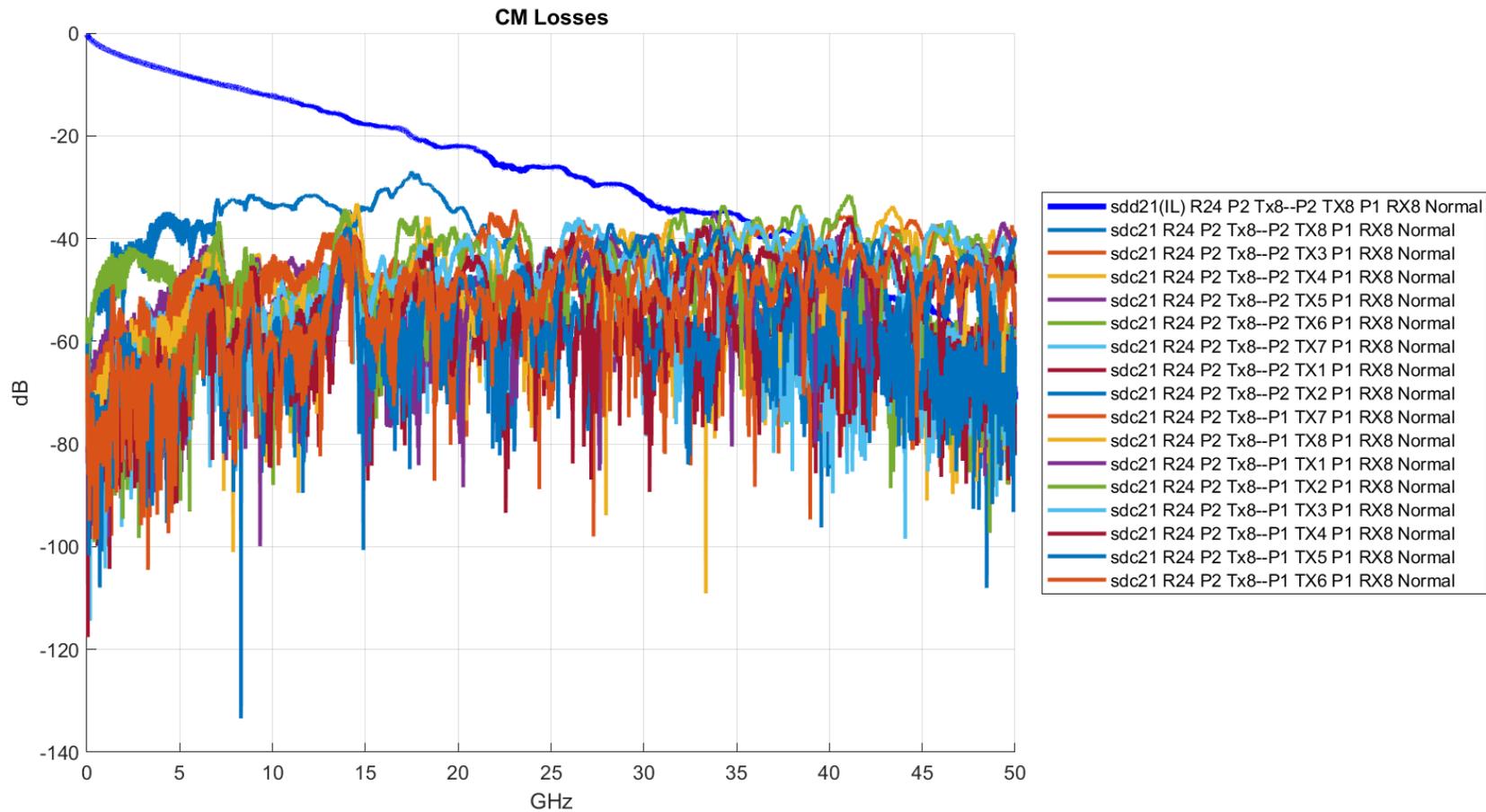
# mellitz\_3ck\_02\_1118\_CA--qsfpddmtf-dd-2m-qsfpddmtf\_V2\_thru

1

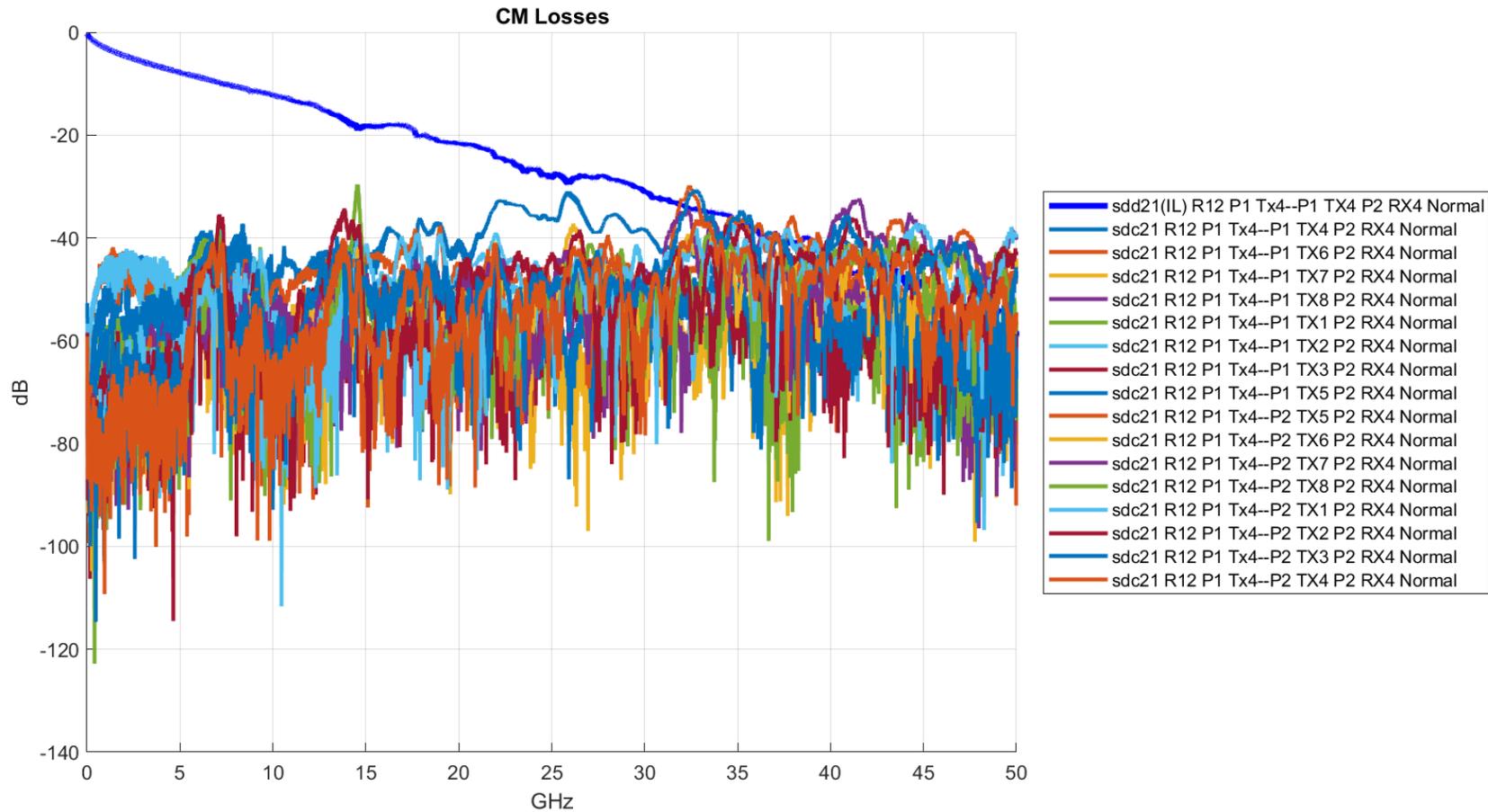


# P2\_TX8\_P1\_RX8\_Normal

2

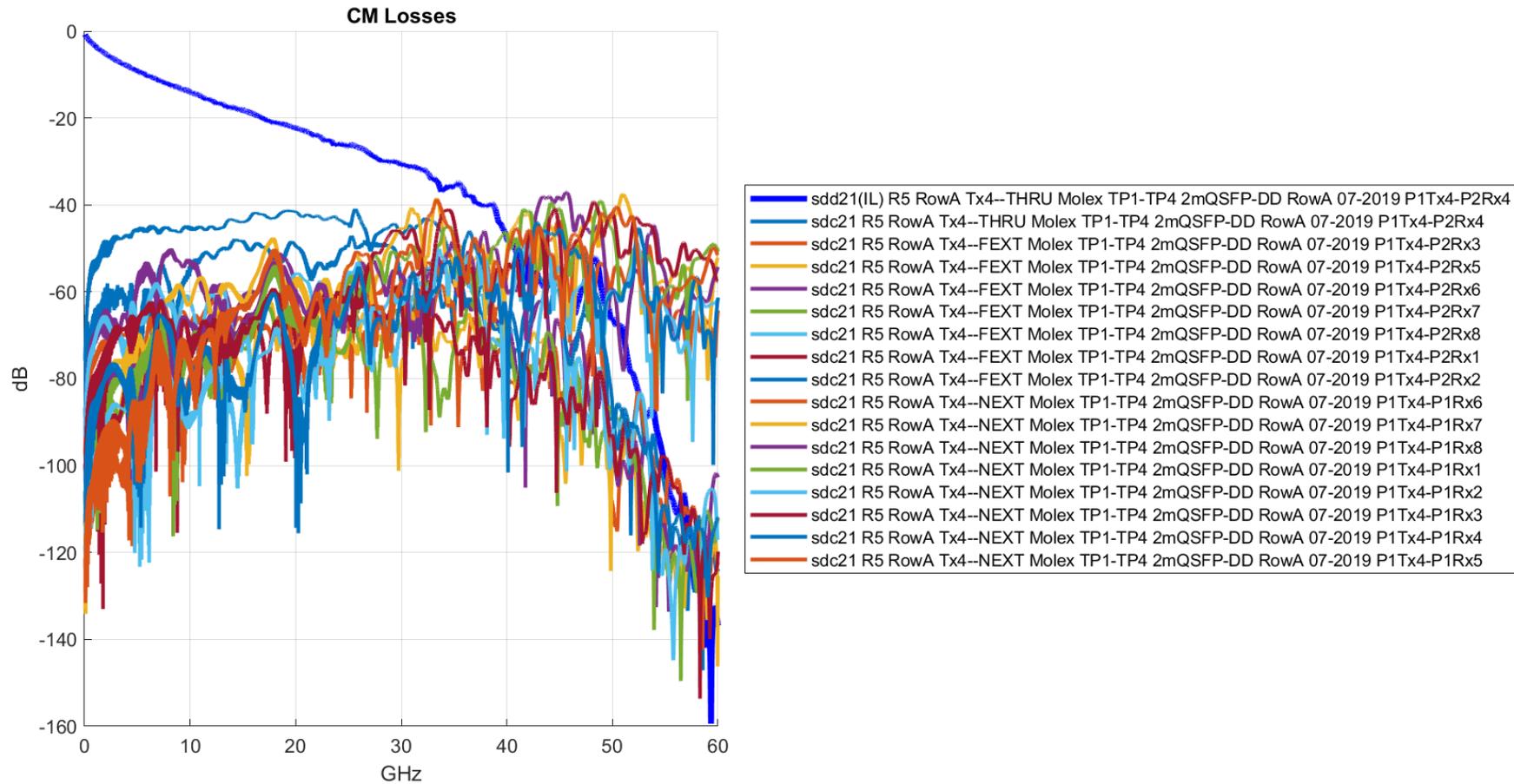


# P1\_TX4\_P2\_RX4\_Normal



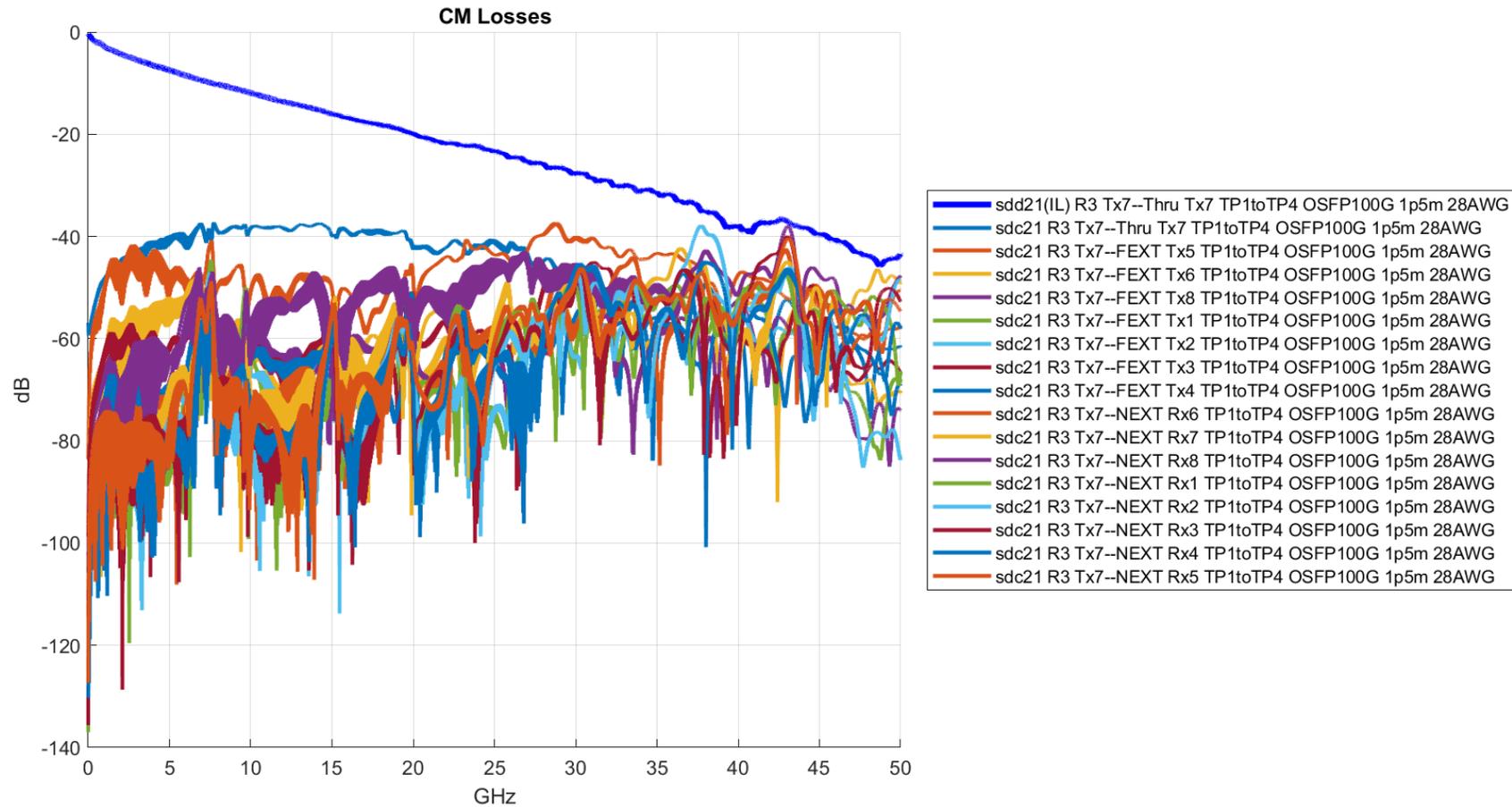
# THRU\_Molex\_TP1-TP4\_2mQSFP-DD\_RowA\_07-2019\_P1Tx4-P2Rx4

4

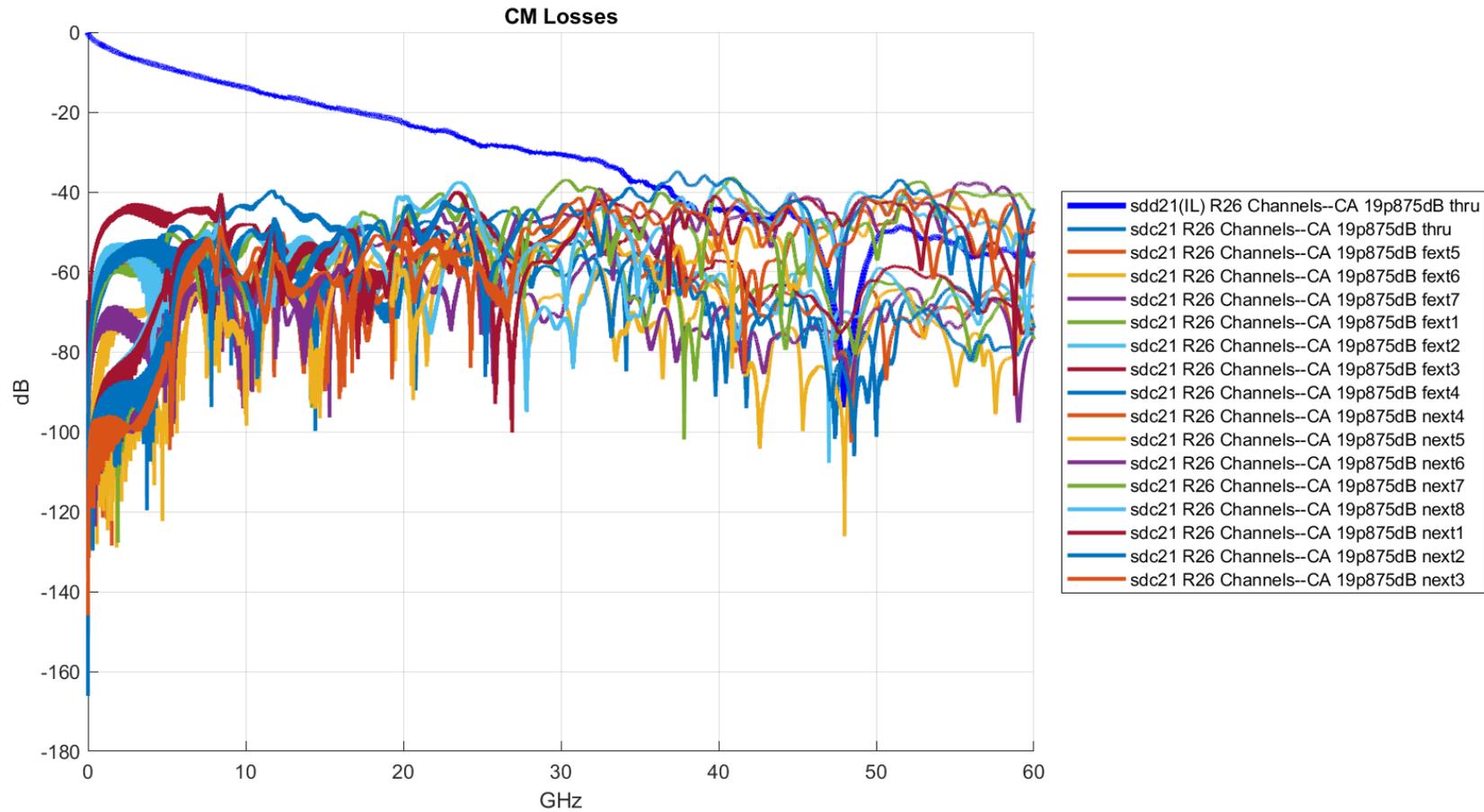


# Thru\_Tx7\_TP1toTP4\_OSFP100G\_1p5m\_28AWG

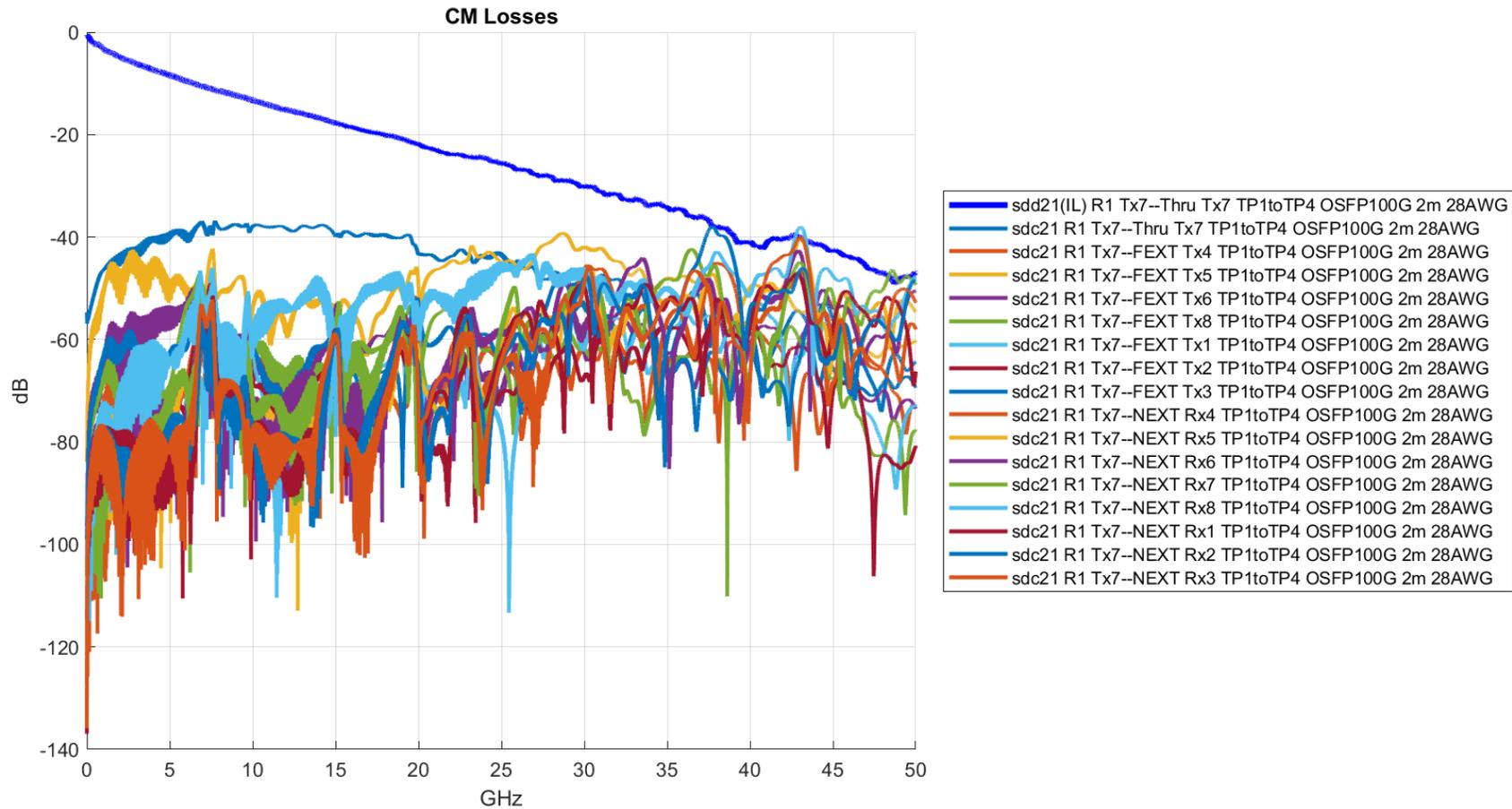
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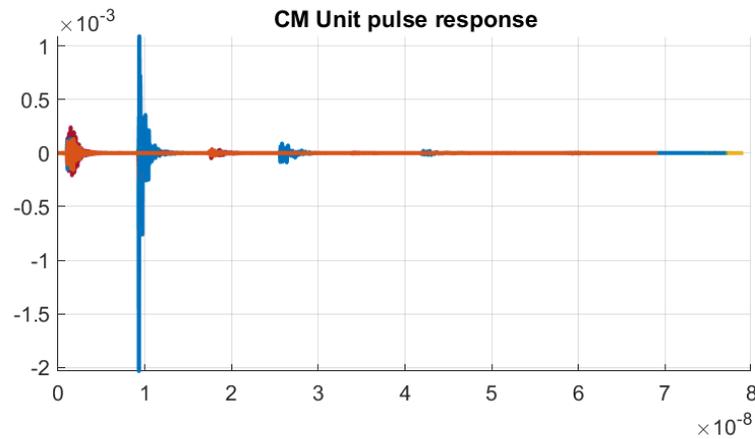
# CA\_19p875dB\_thru



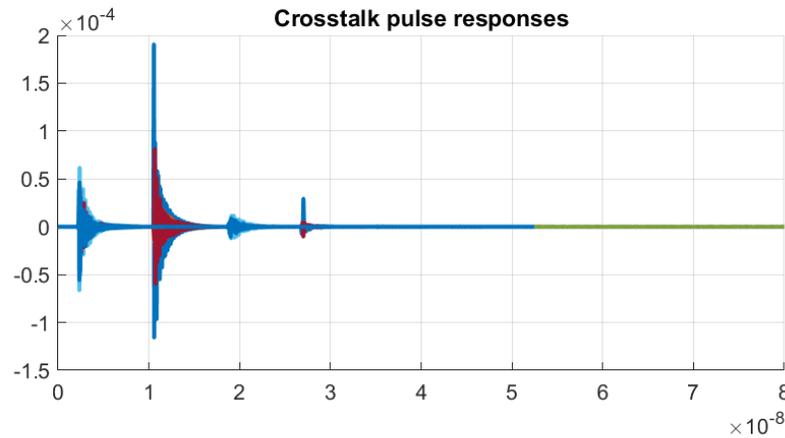
# Thru\_Tx7\_TP1toTP4\_OSFP100G\_2m\_28AWG



# mellitz\_3ck\_02\_1118\_CA--qsfpddmtf-dd-2m-qsfpddmtf\_V2\_thru

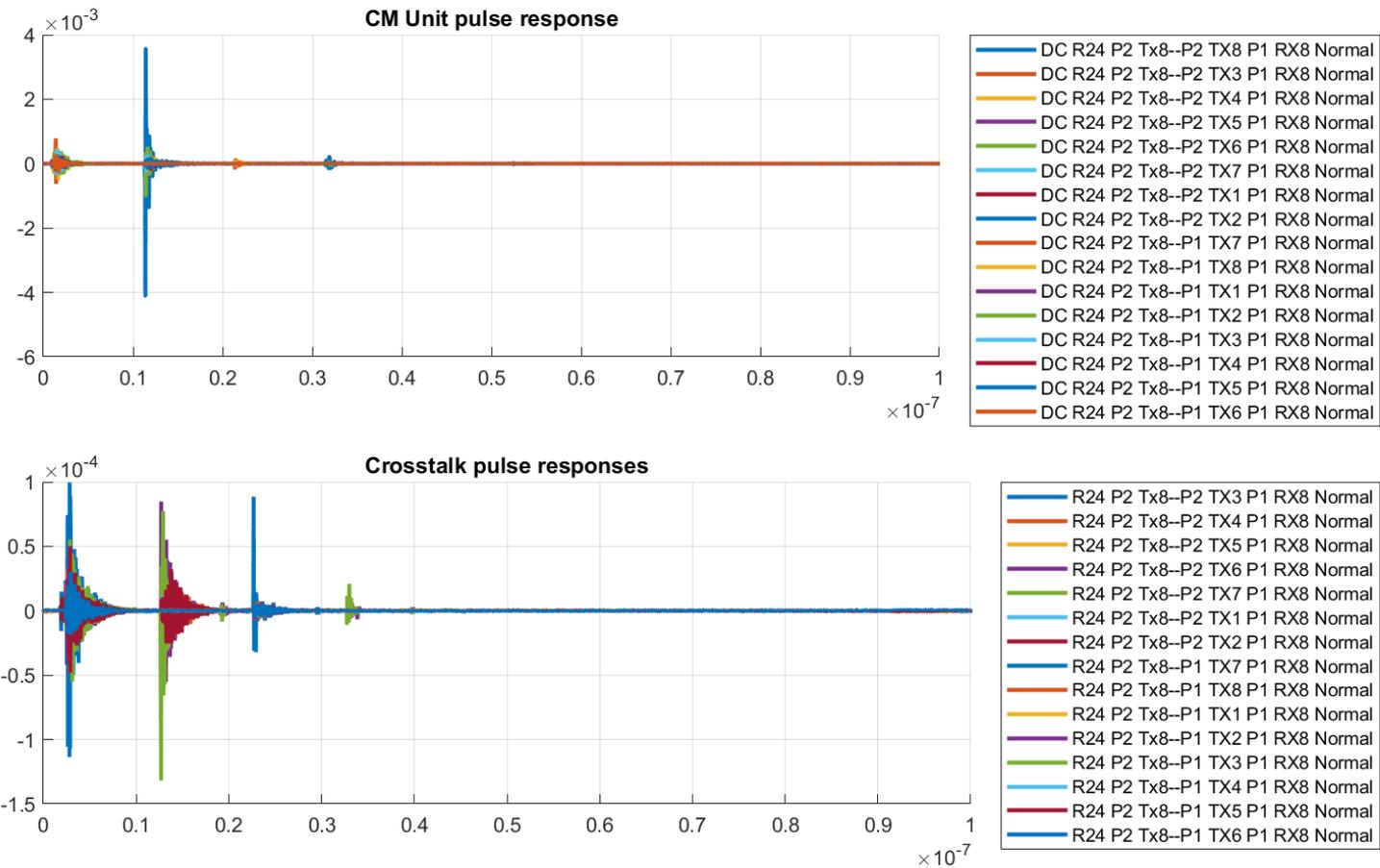


- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 thru
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext2
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext3
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext4
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext5
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext6
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext7
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext1
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next1
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next2
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next3
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next4
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next5
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next6
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next7
- DC R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next8

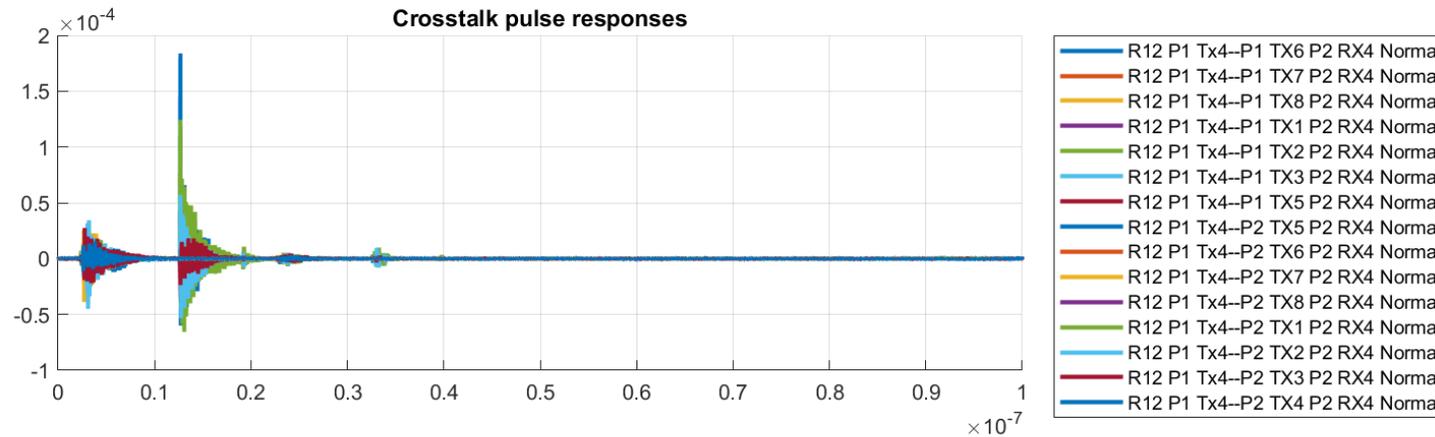
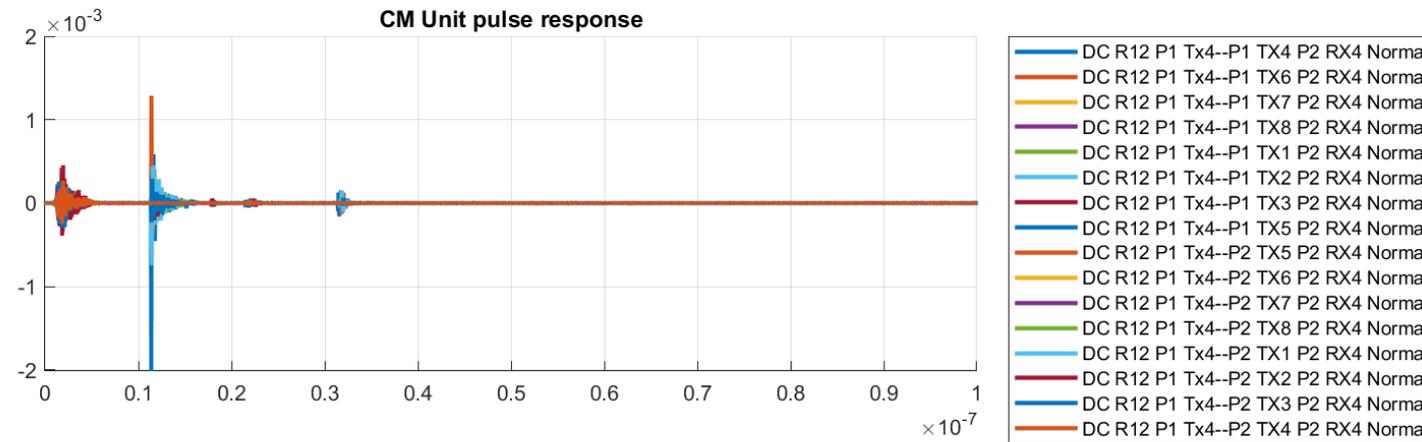


- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext2
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext3
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext4
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext5
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext6
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext7
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 fext1
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next1
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next2
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next3
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next4
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next5
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next6
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next7
- R25 mellitz 3ck 02 1118 CA--qsfpddmtf-dd-2m-qsfpddmtf V2 next8

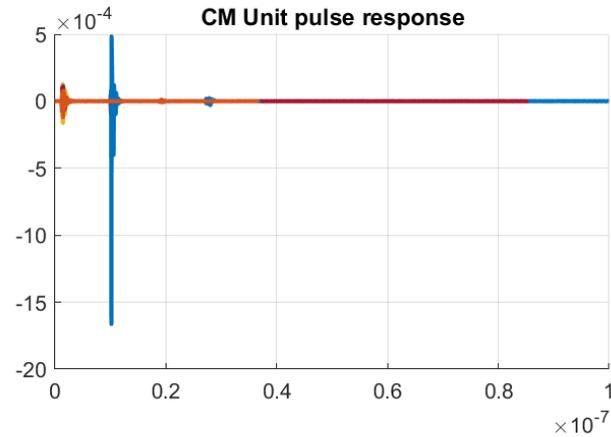
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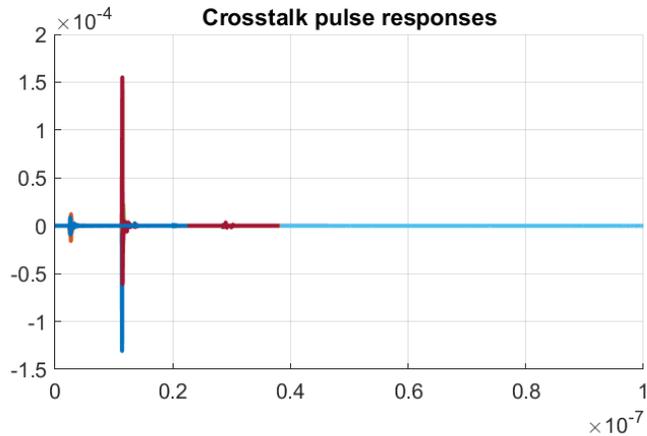
# P1\_TX4\_P2\_RX4\_Normal



# THRU\_Molex\_TP1-TP4\_2mQSFP-DD\_RowA\_07-2019\_P1Tx4-P2Rx4

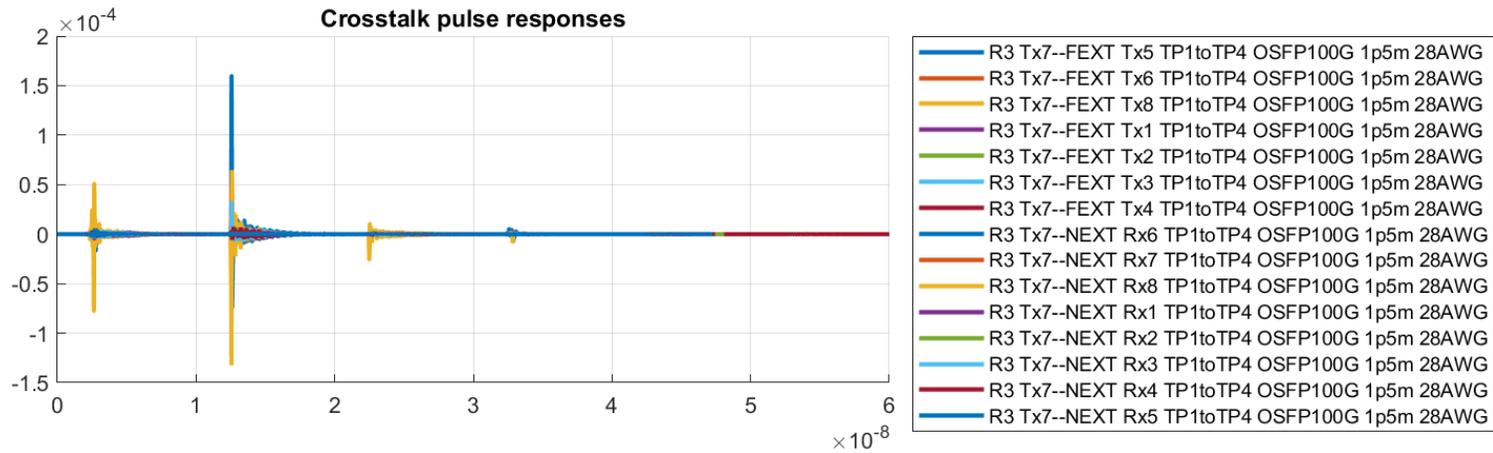
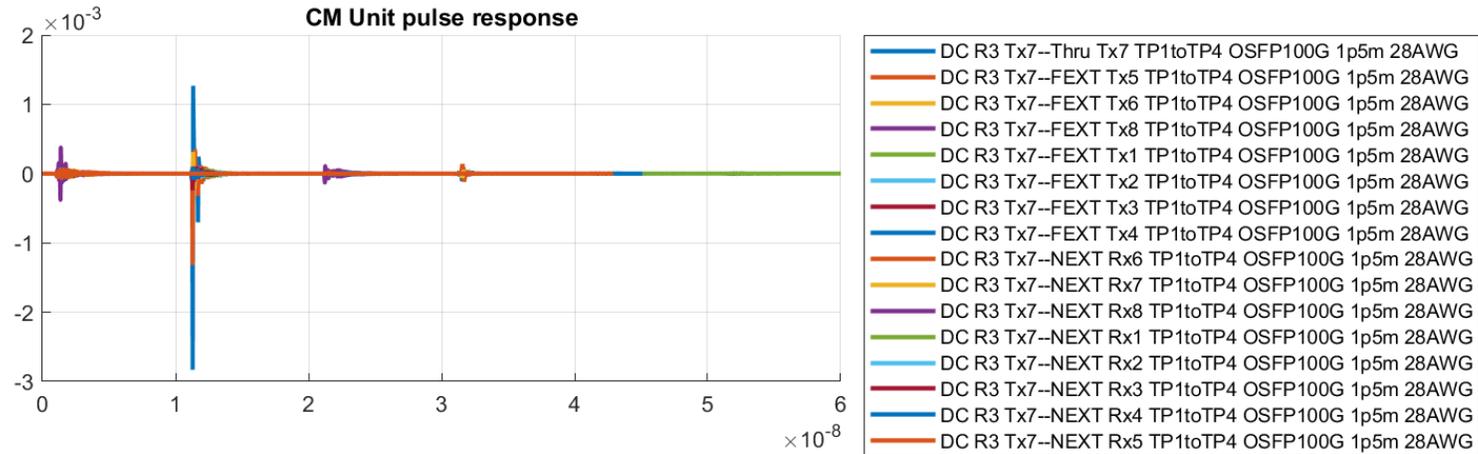


- DC R5 RowA Tx4--THRU Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx4
- DC R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx3
- DC R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx5
- DC R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx6
- DC R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx7
- DC R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx8
- DC R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx1
- DC R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx2
- DC R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx6
- DC R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx7
- DC R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx8
- DC R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx1
- DC R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx2
- DC R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx3
- DC R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx4
- DC R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx5

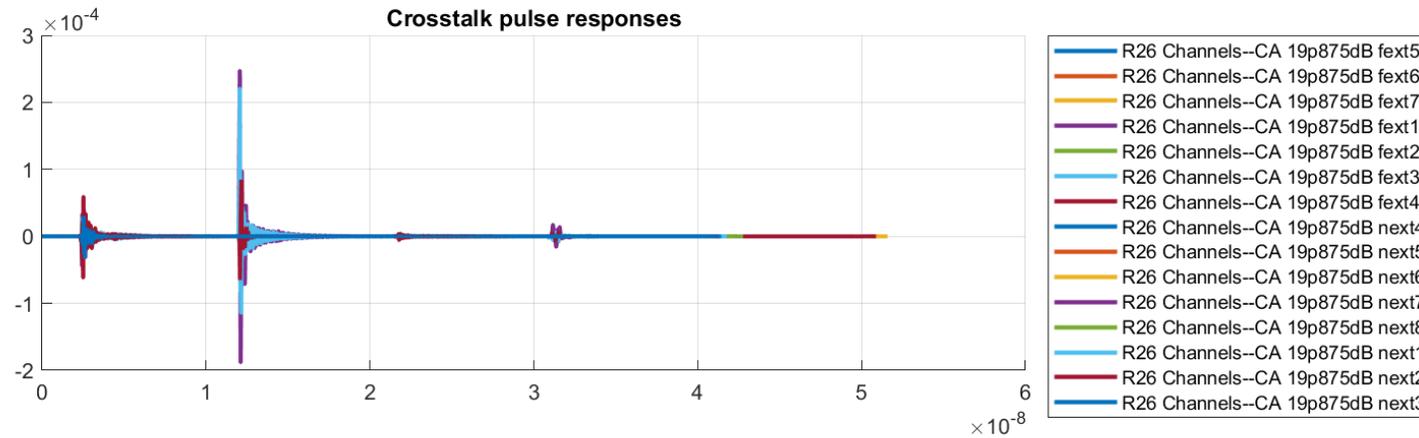
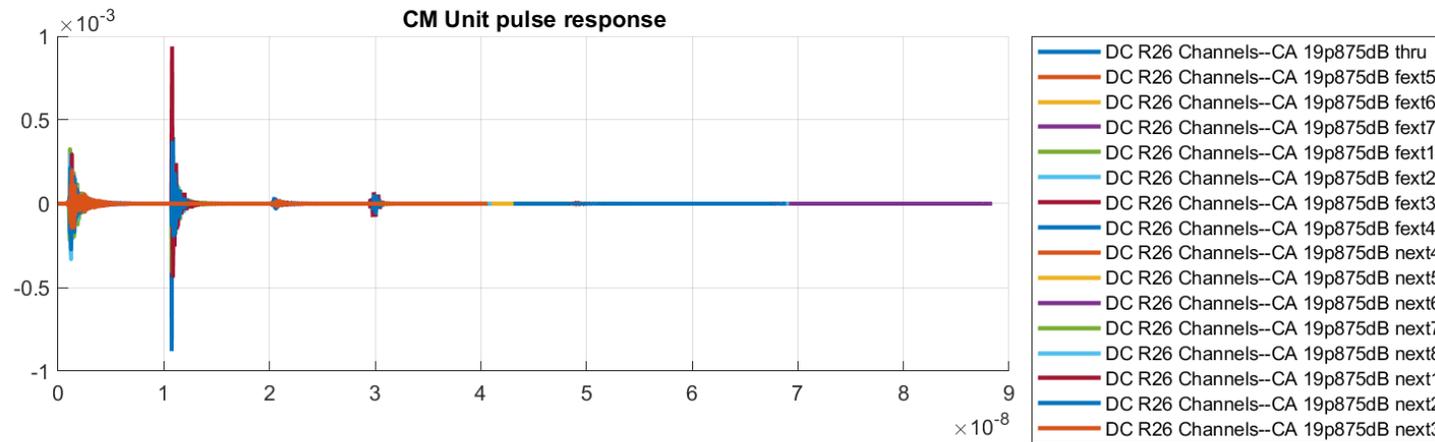


- R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx3
- R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx5
- R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx6
- R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx7
- R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx8
- R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx1
- R5 RowA Tx4--FEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P2Rx2
- R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx6
- R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx7
- R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx8
- R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx1
- R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx2
- R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx3
- R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx4
- R5 RowA Tx4--NEXT Molex TP1-TP4 2mQSFP-DD RowA 07-2019 P1Tx4-P1Rx5

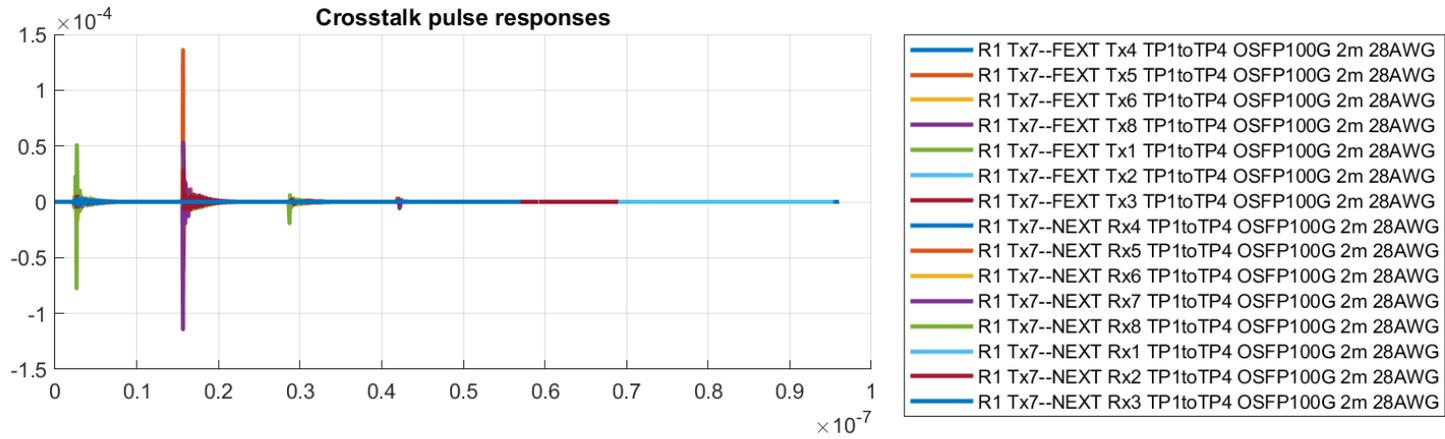
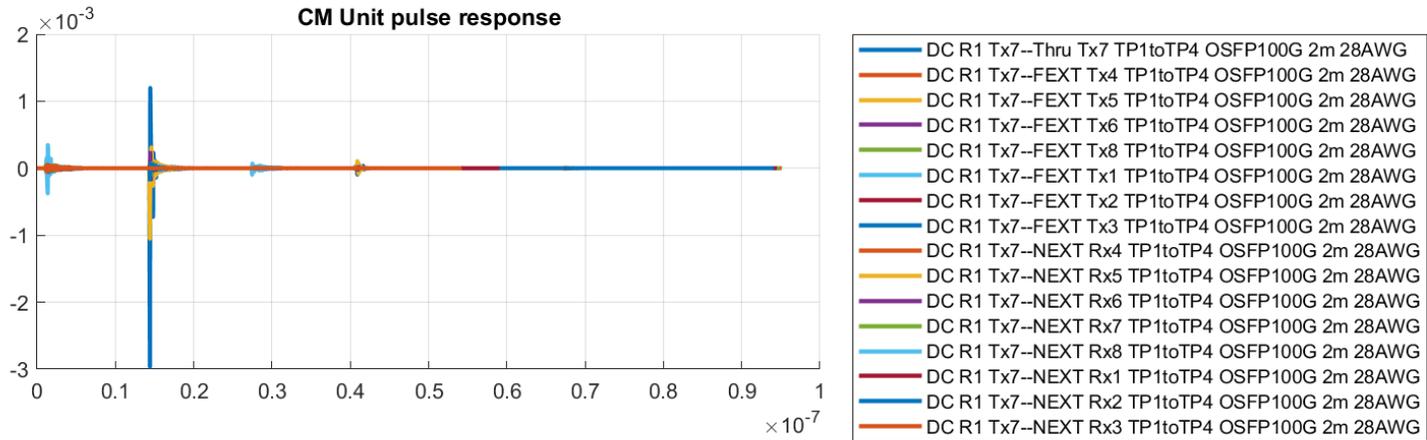
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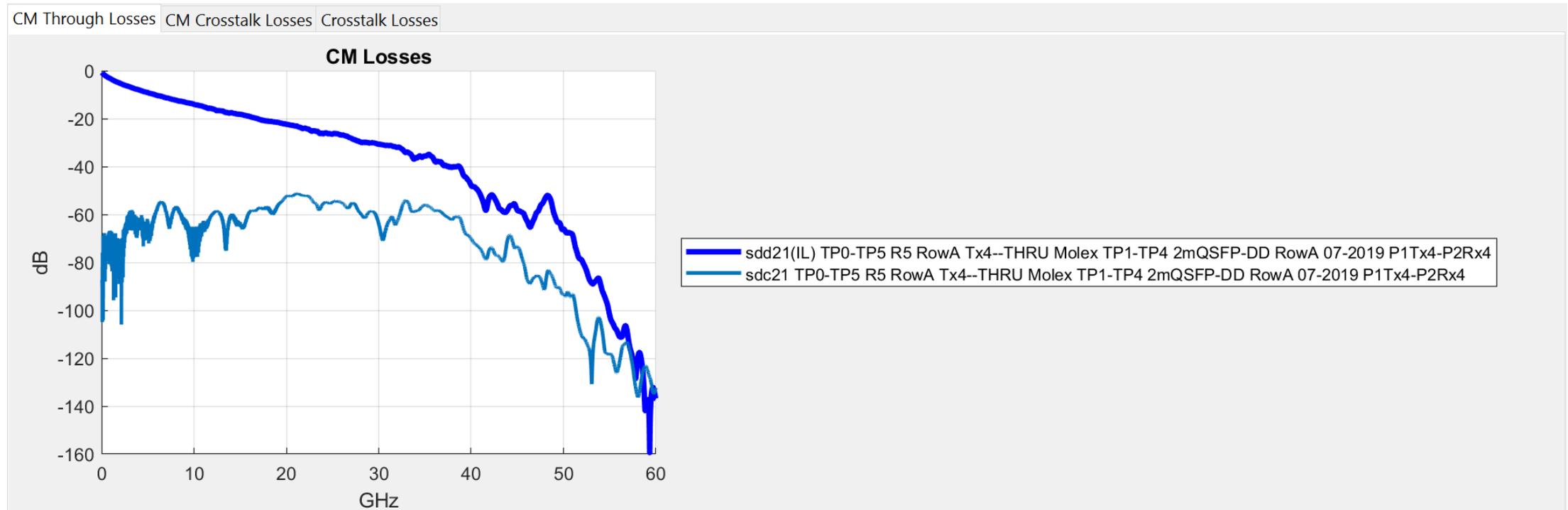
# CA\_19p875dB\_thru



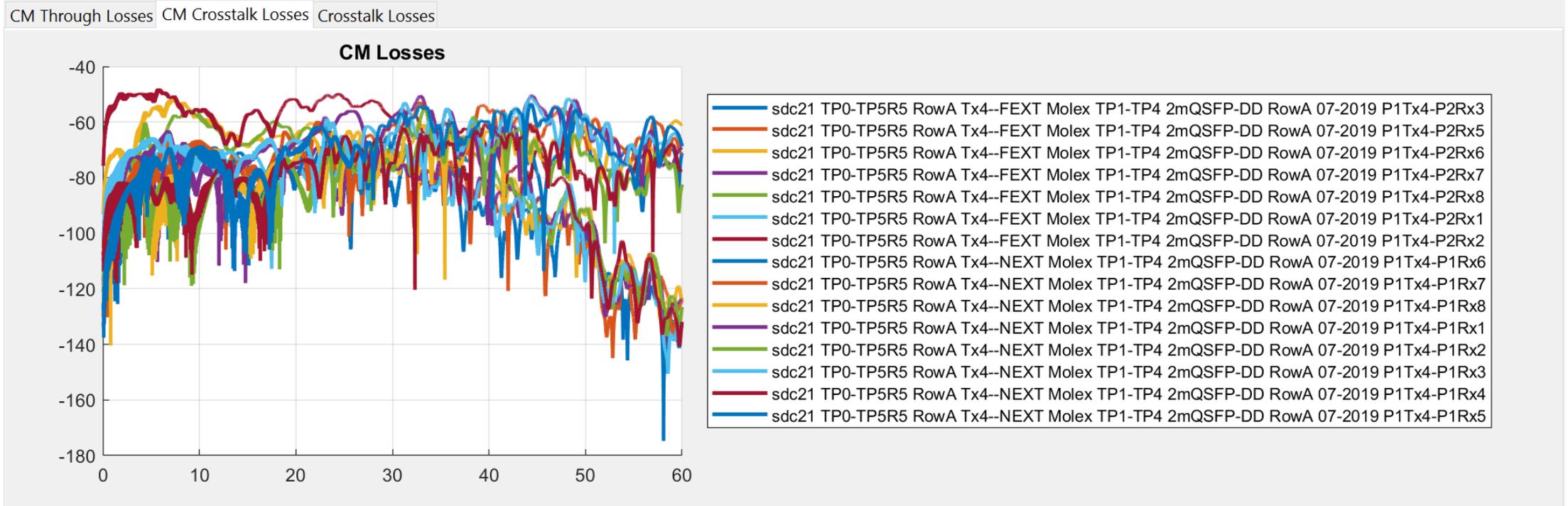
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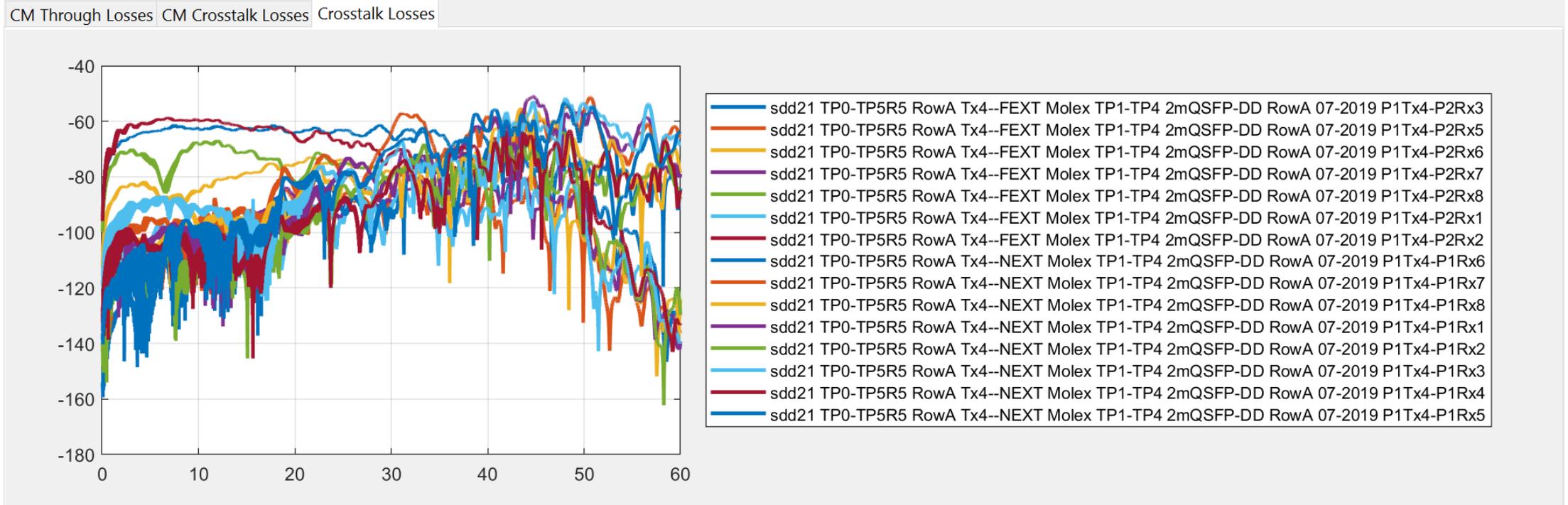
# IL and Through CM response



# CM responses



# Crosstalk responses



# Evaluation of CM sources with waveform simulation

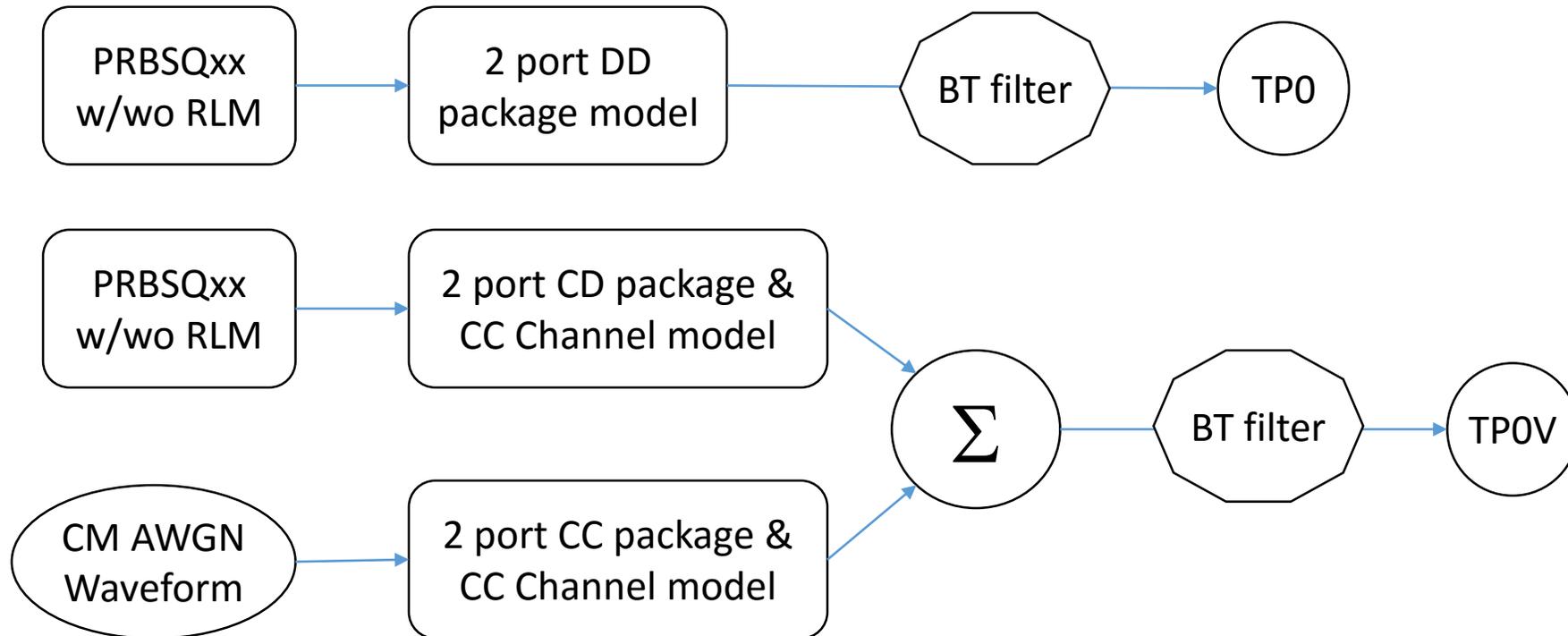
- ❑ Similar to ghiasi\_3ck\_03a\_0720 and mellitz\_3ck\_adhoc\_01\_061720
- ❑ Emulate CM noise
  - Use toleranced package for each p/n leg for correlated CM
  - Use AWGN CM mode noise source at die pad drive for uncorrelated CM
  - Utilize waveform simulation with Matlab (not COM)
- ❑ What to observe
  - Waveforms
  - Noise probability density functions (PDF)
  - Noise Power Spectral densities (PSD)

# Unbalanced and skewed package models

```
param.Zp=30;  
param.Zp1=1.8;  
param.Cd=120e-15*pvdt;  
param.Cp=87e-15*pvdt;  
param.Zc=87.5*pvdt;  
param.Zc1=92.5*pvdt;  
param.Lb=120e-12;  
param.Cb=30e-15;  
param.pkg_tau=0.00614*pvdt*skew;  
skew_ps= 0.00614*(1-pvdt*skew)*1e-9/1e-12*param.Zp;  
param.pkg_gamma0_a1_a2=[0 0.0009909 0.0002772];  
param.Z0=50;  
fprintf('%0.2g ps skew \n',skew_ps)
```



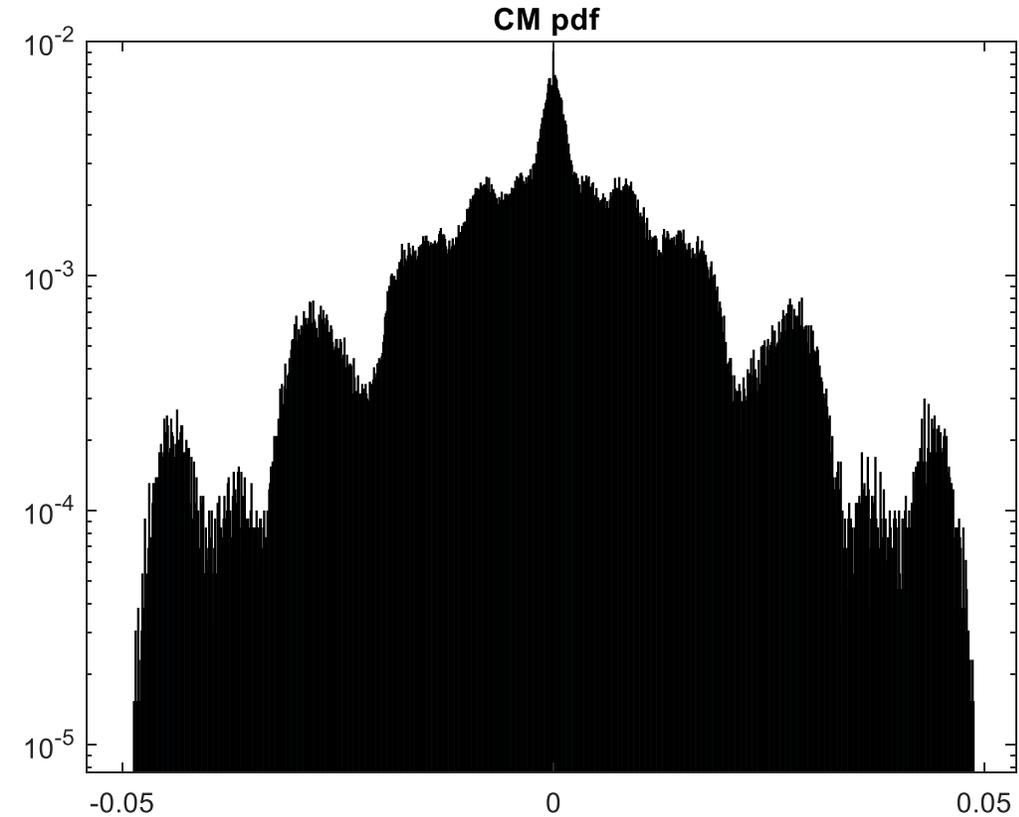
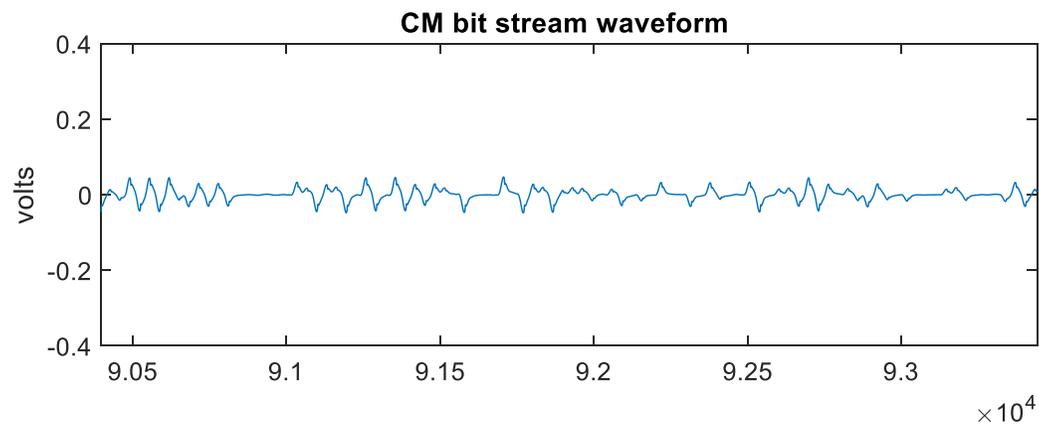
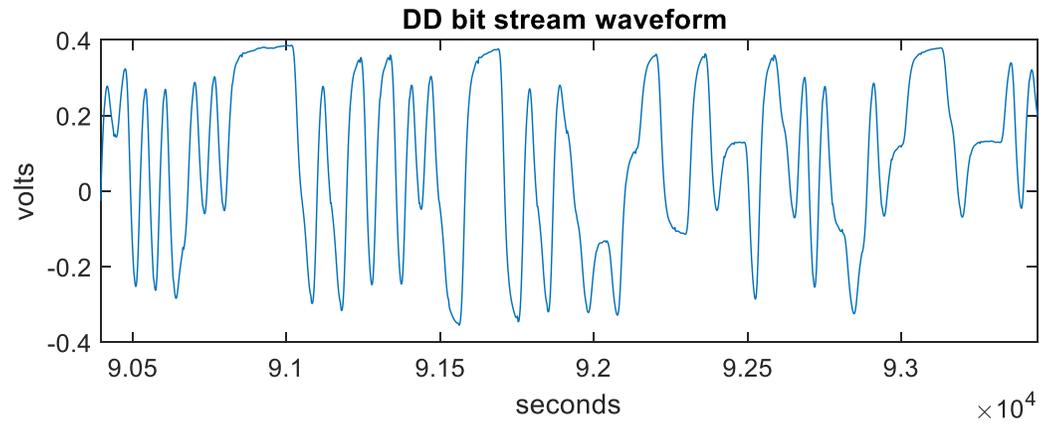
# Simulation of common mode at package voltages at TPO and TPOv



# Simulation Conditions

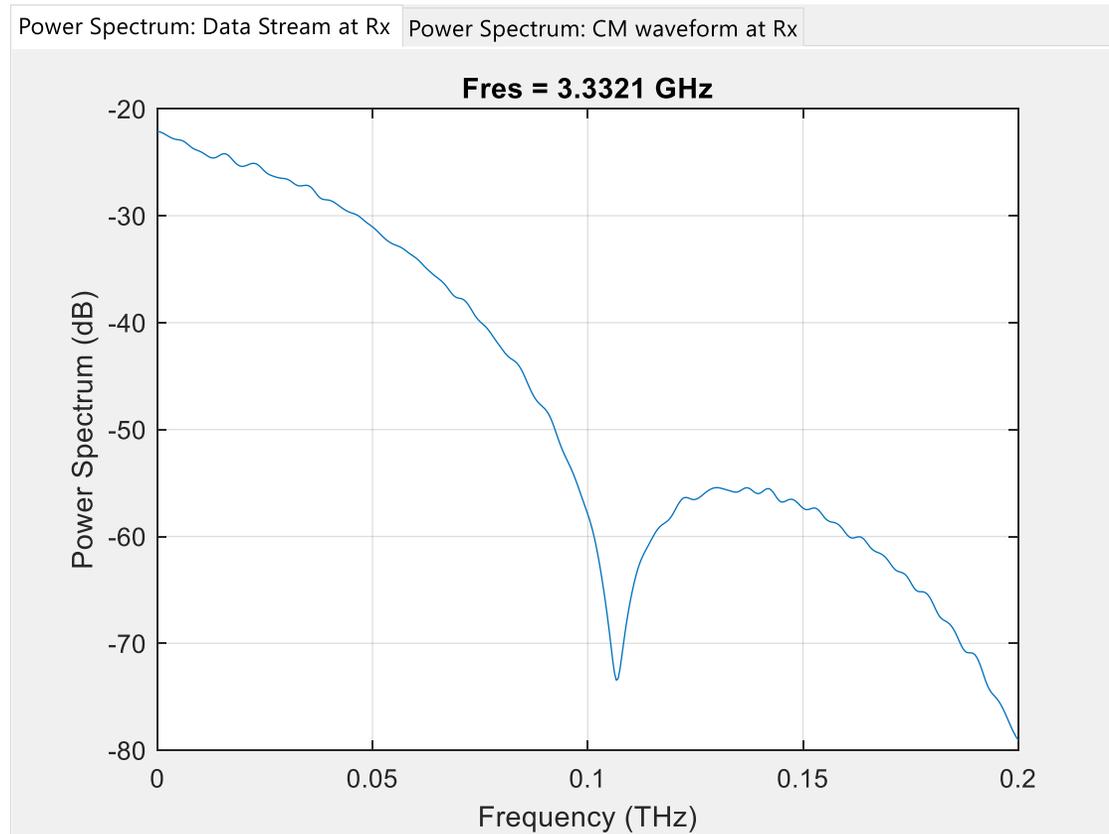
- No Txffe
- $T_r = 7.5 \text{ ps}$
- $A_v = 400 \text{ mV}$
- No noise or jitter
- BT filter

# Introduce 3.7 ps p-n skew at TP0

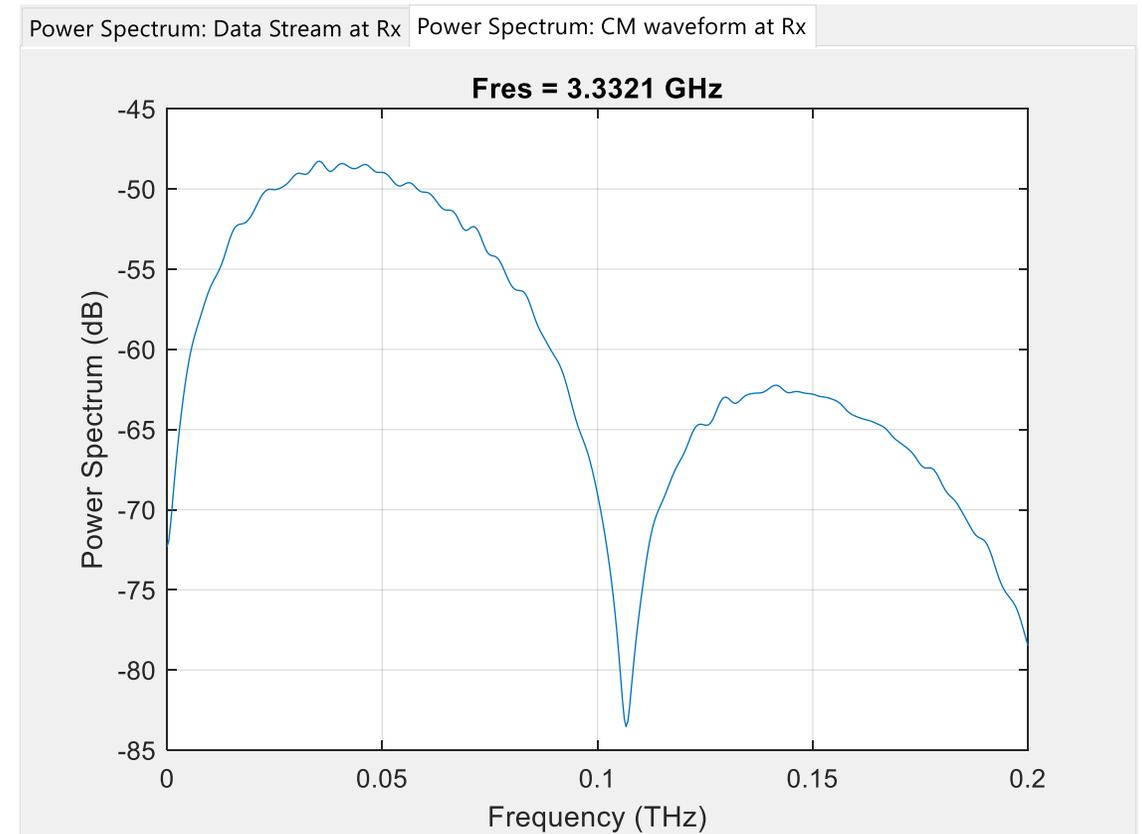


Noise PDF does not look Gaussian

# PSD from 3.7 ps skew at TPO

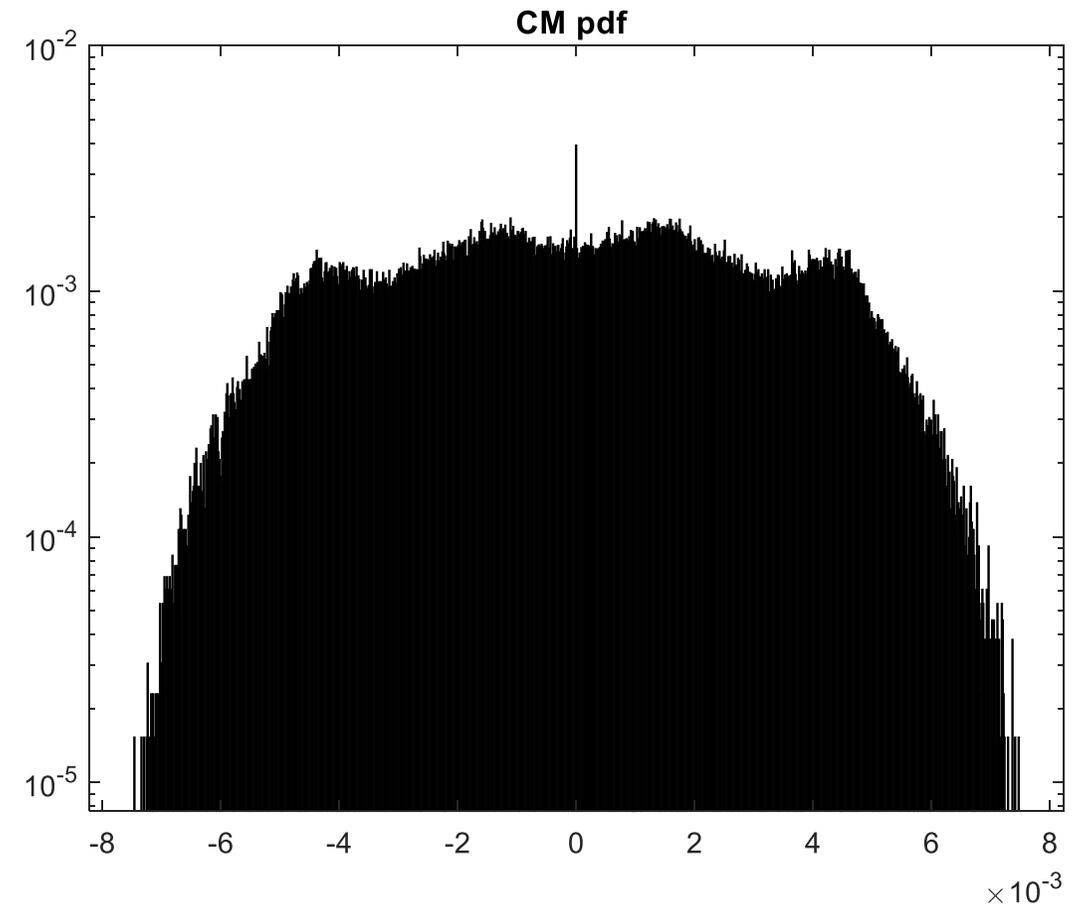
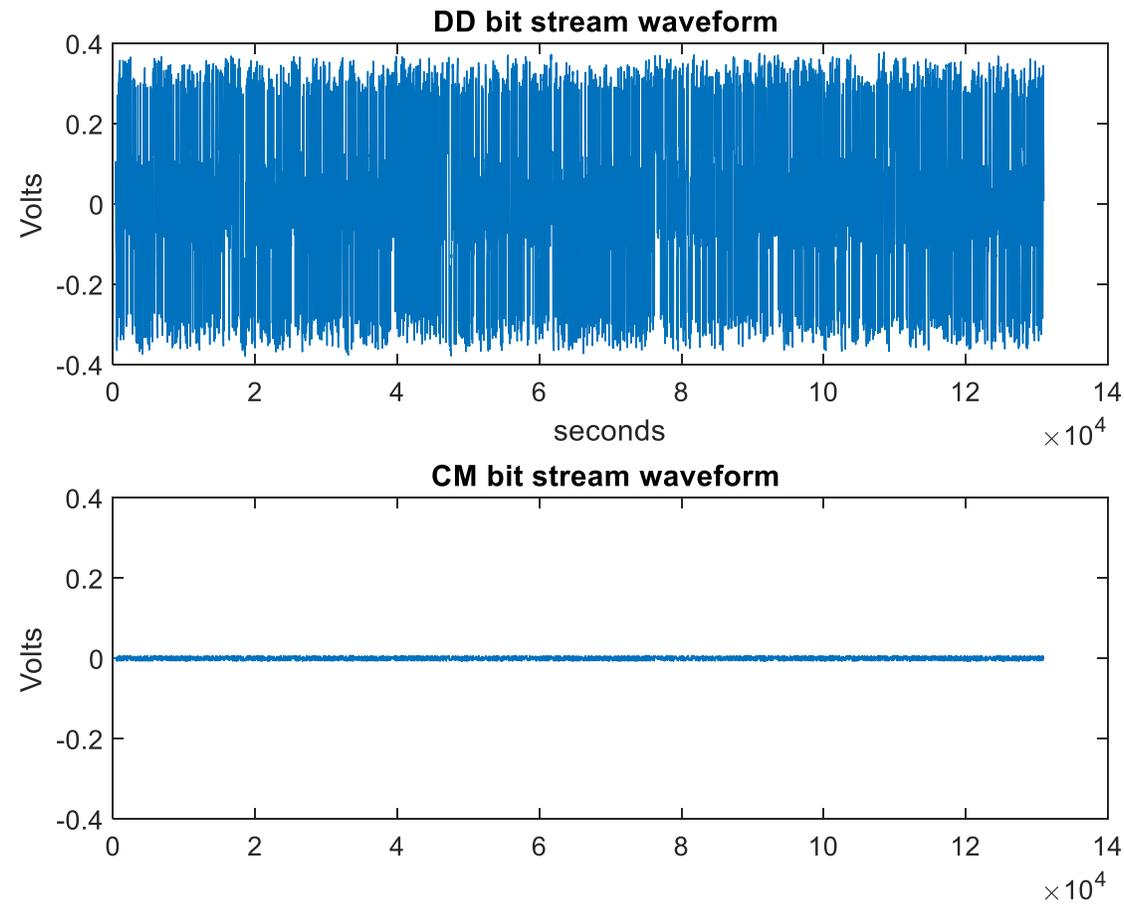


Data Stream PSD



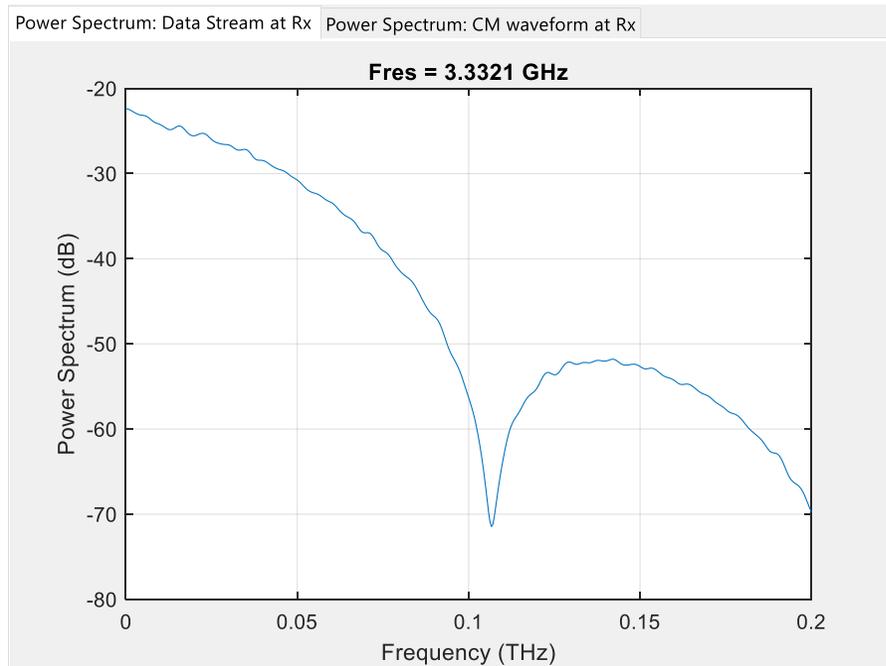
CM waveform PSD

# Introduce 10 % p-n package components variation at TPO

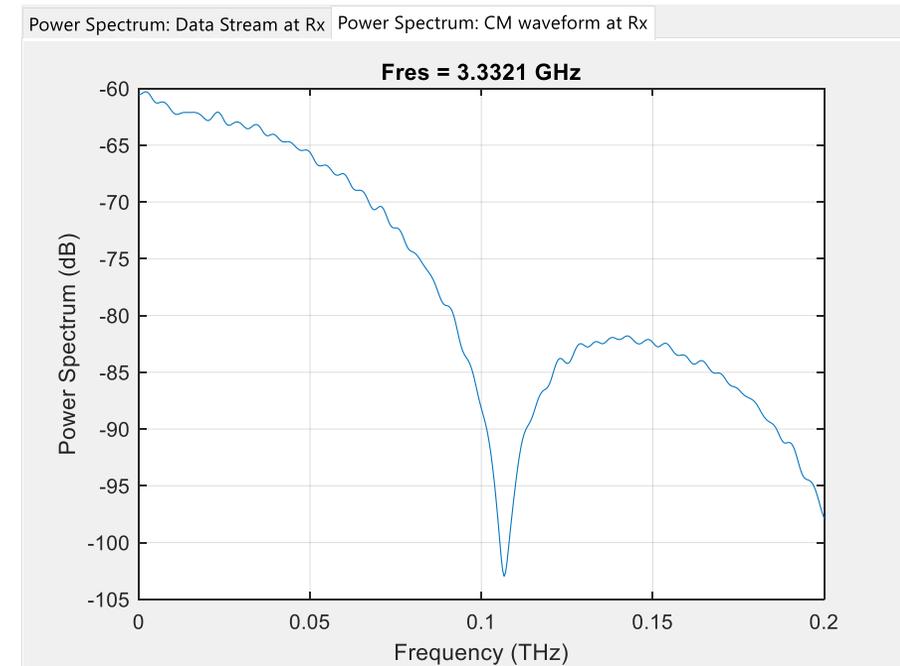


Not as much as might be expected

# PSD from 10 % p-n package components variation at TP0

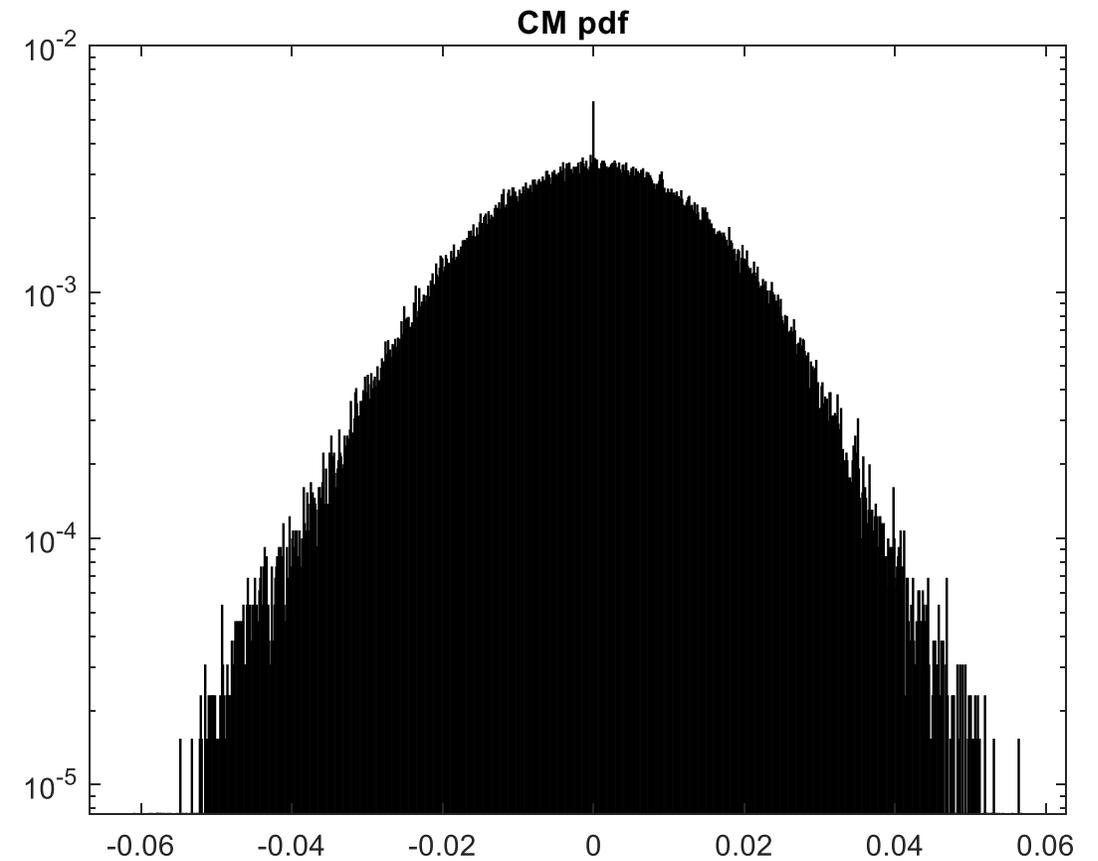
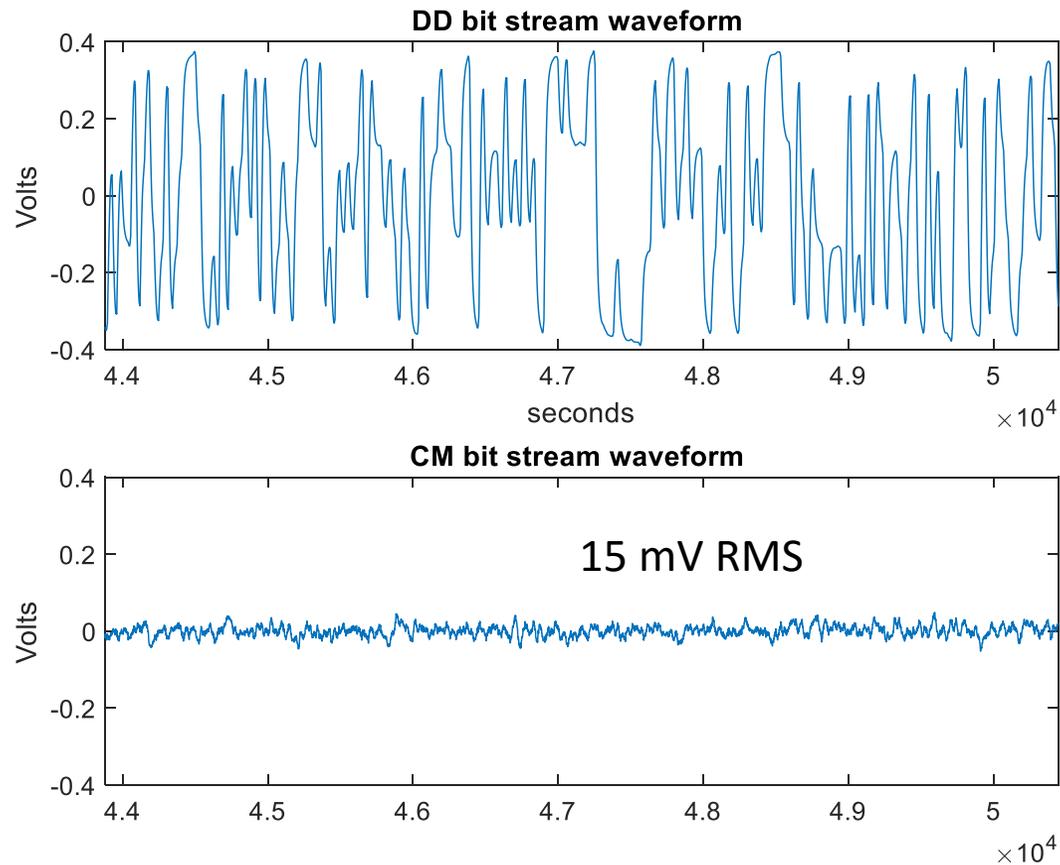


CM waveform PSD

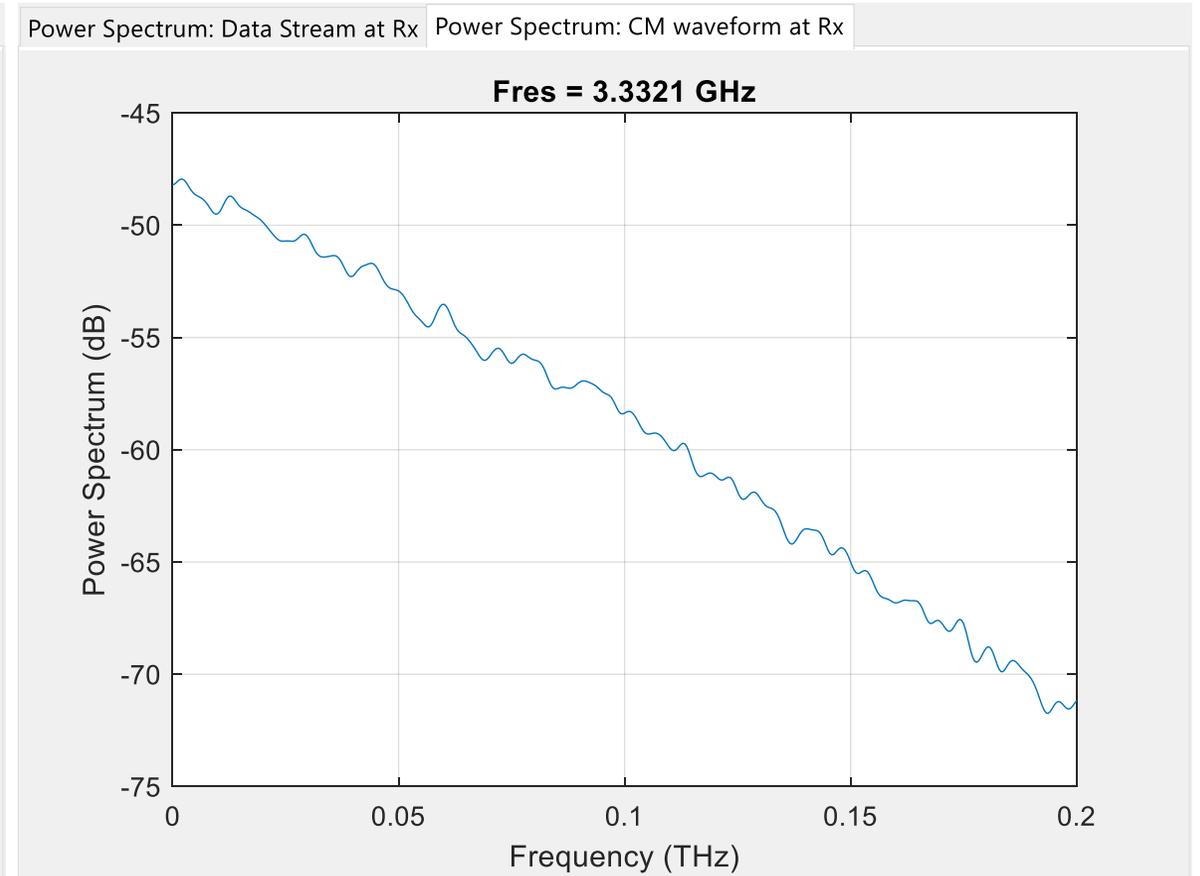
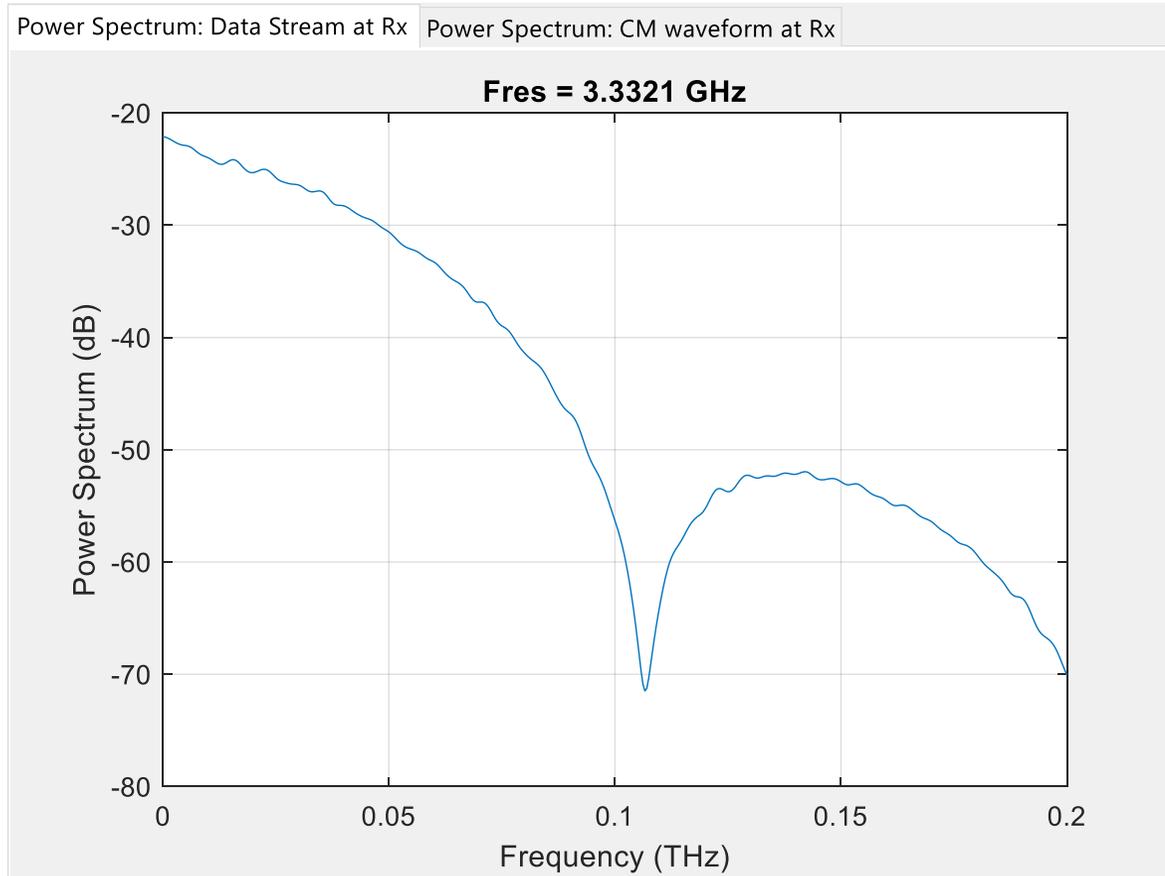


Data Stream PSD

# 15 AC CM RMS at TPO (Broad Band AWGN Source)



# PSD from 15 AC CM RMS at TP0

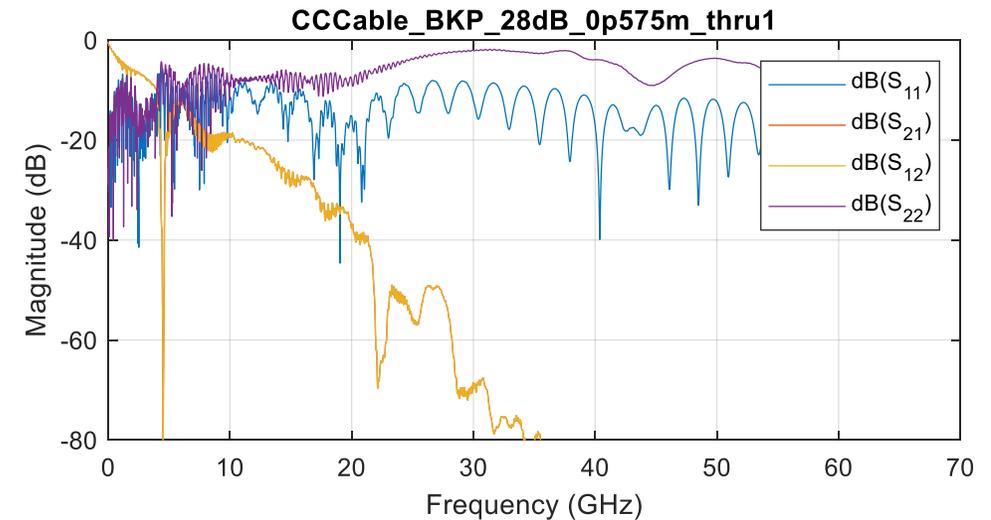
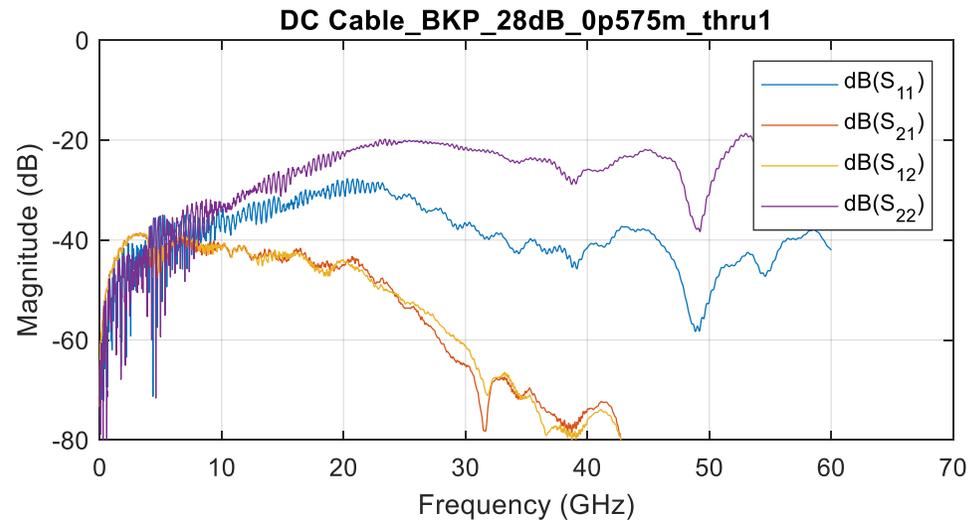
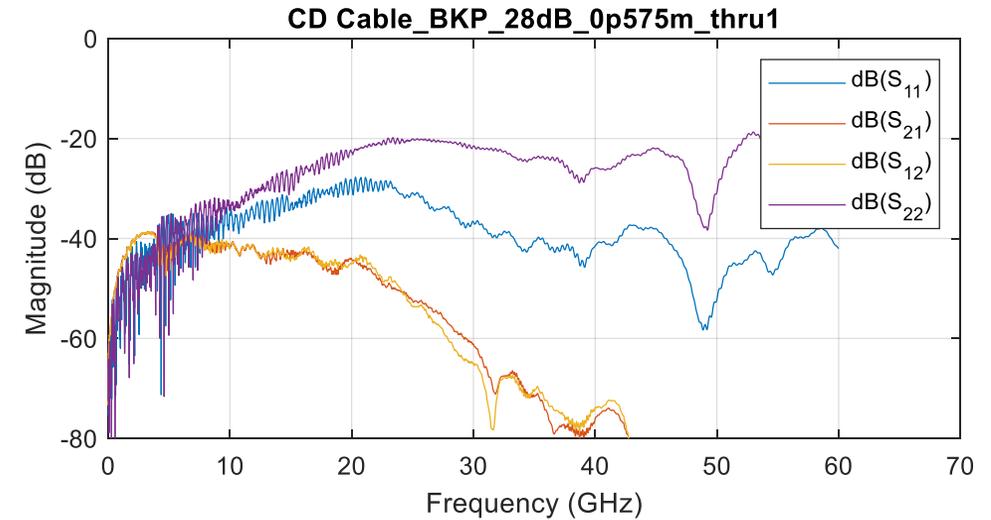
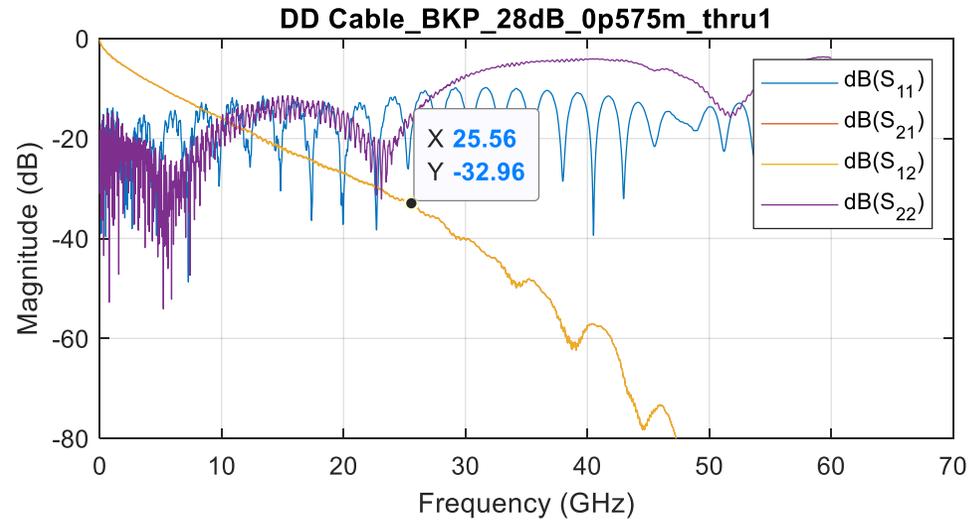


# Now let's look at a channel simulation

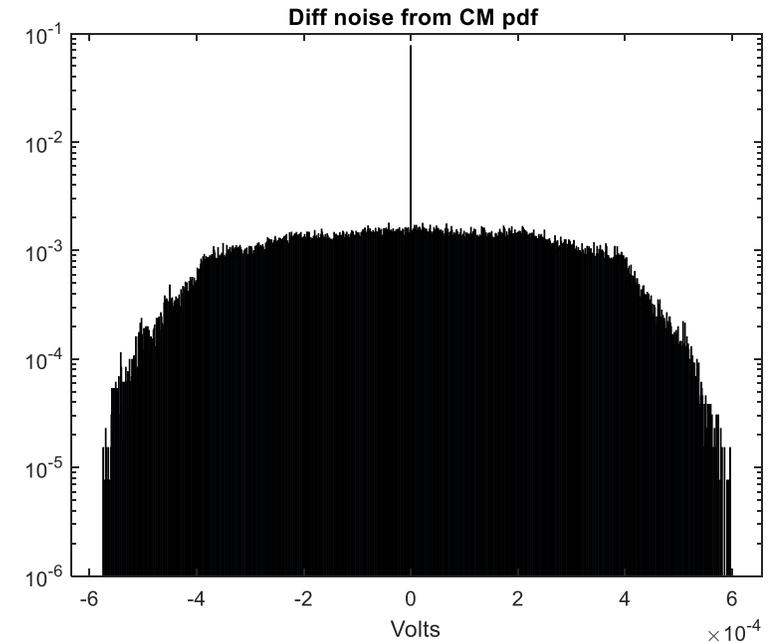
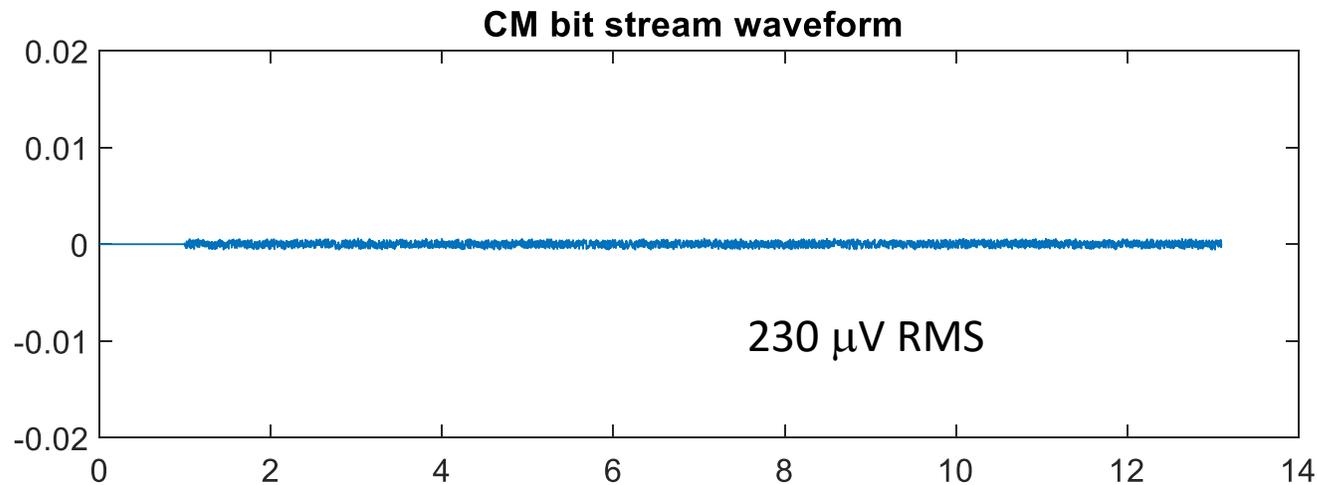
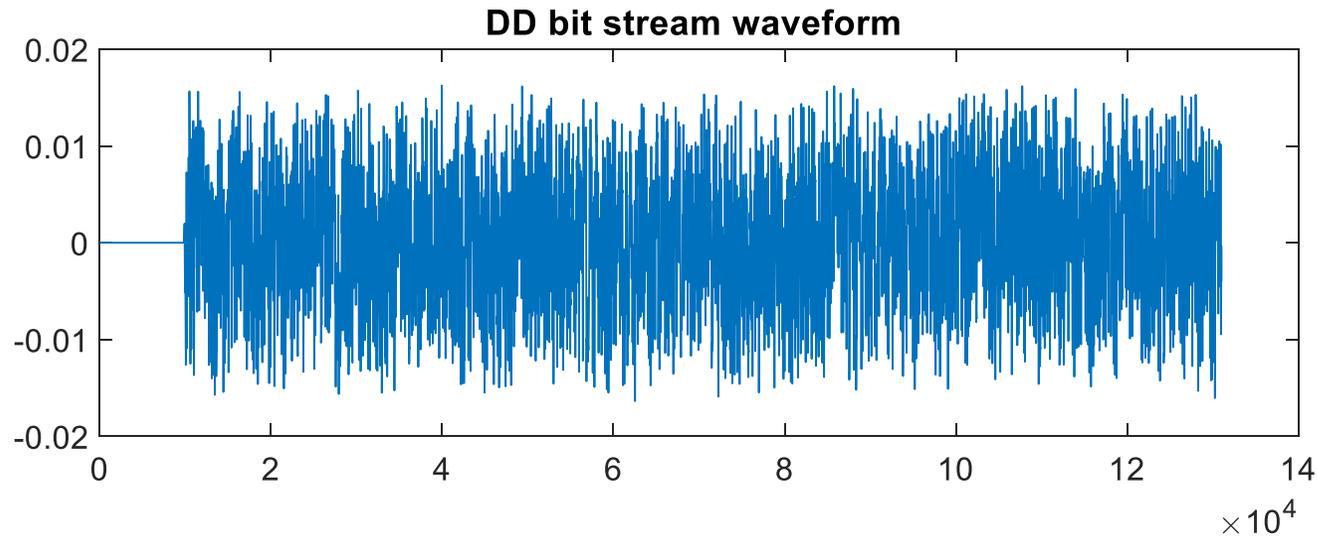
- Looking at Rx w/o Rx package
- No Jitter
- No Noise
- No DFE



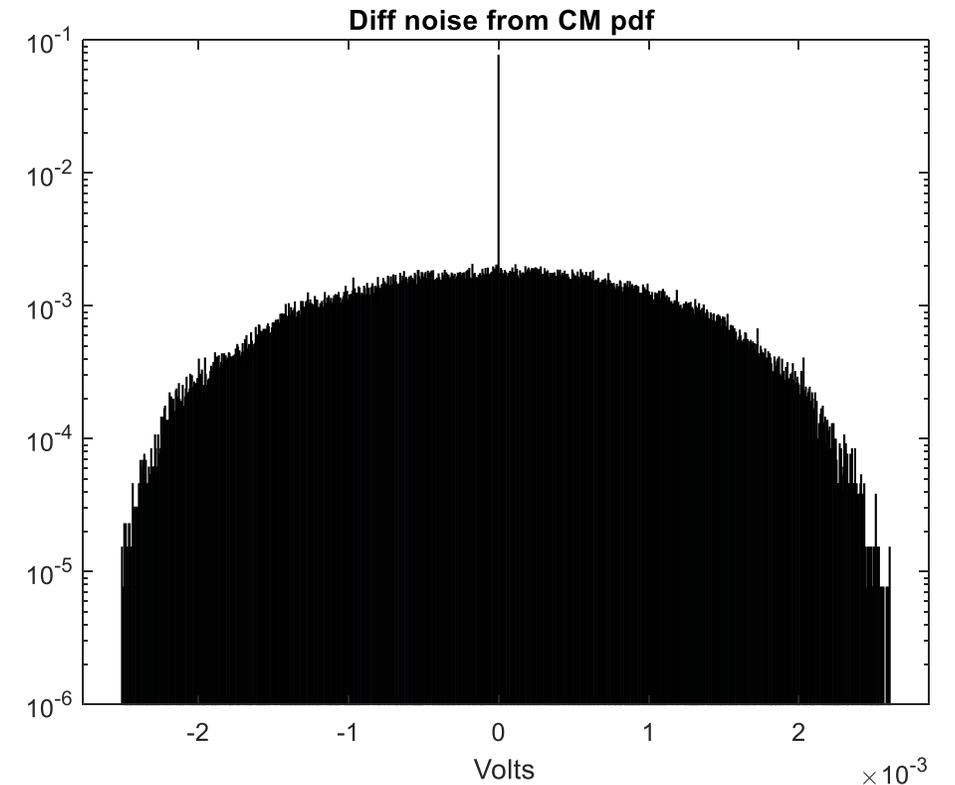
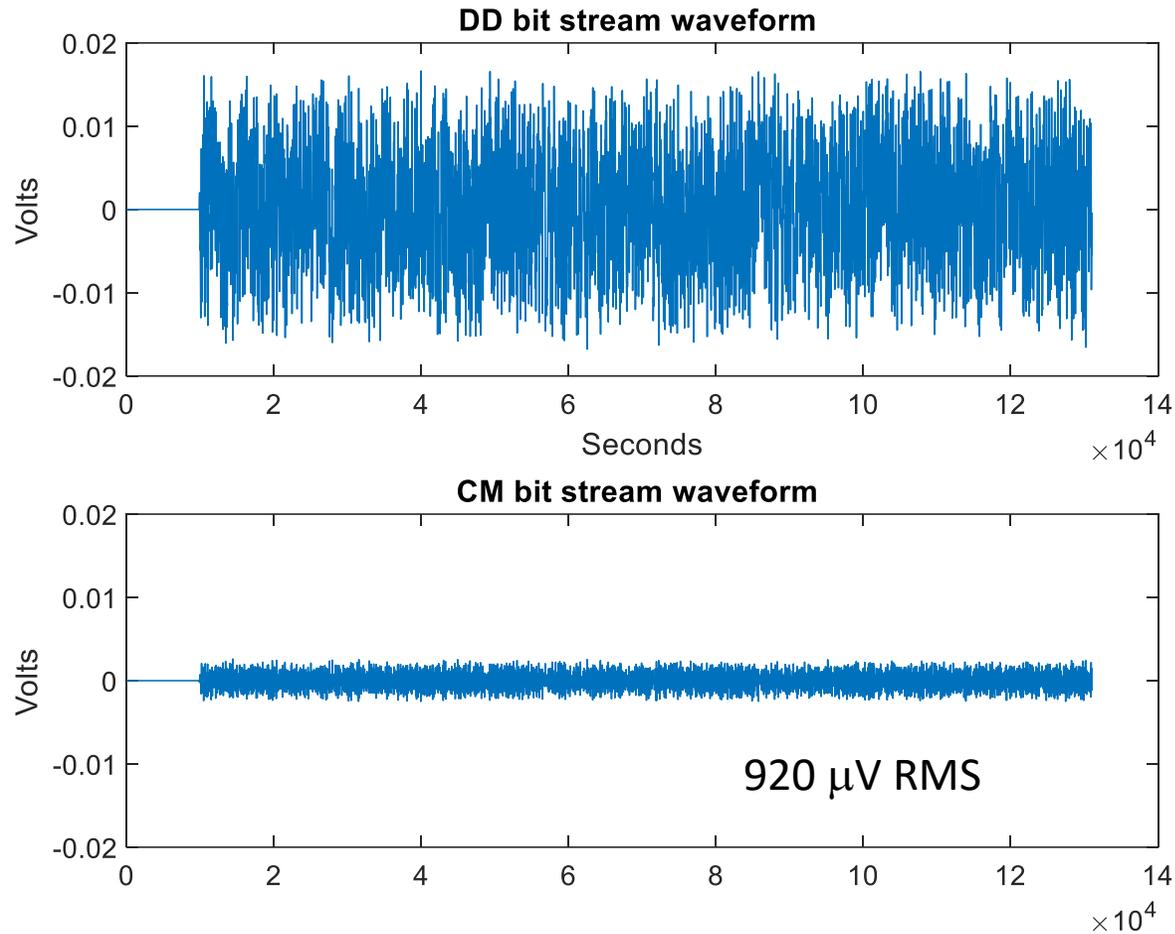
# Use a 28 dB channel Plus a Tx package



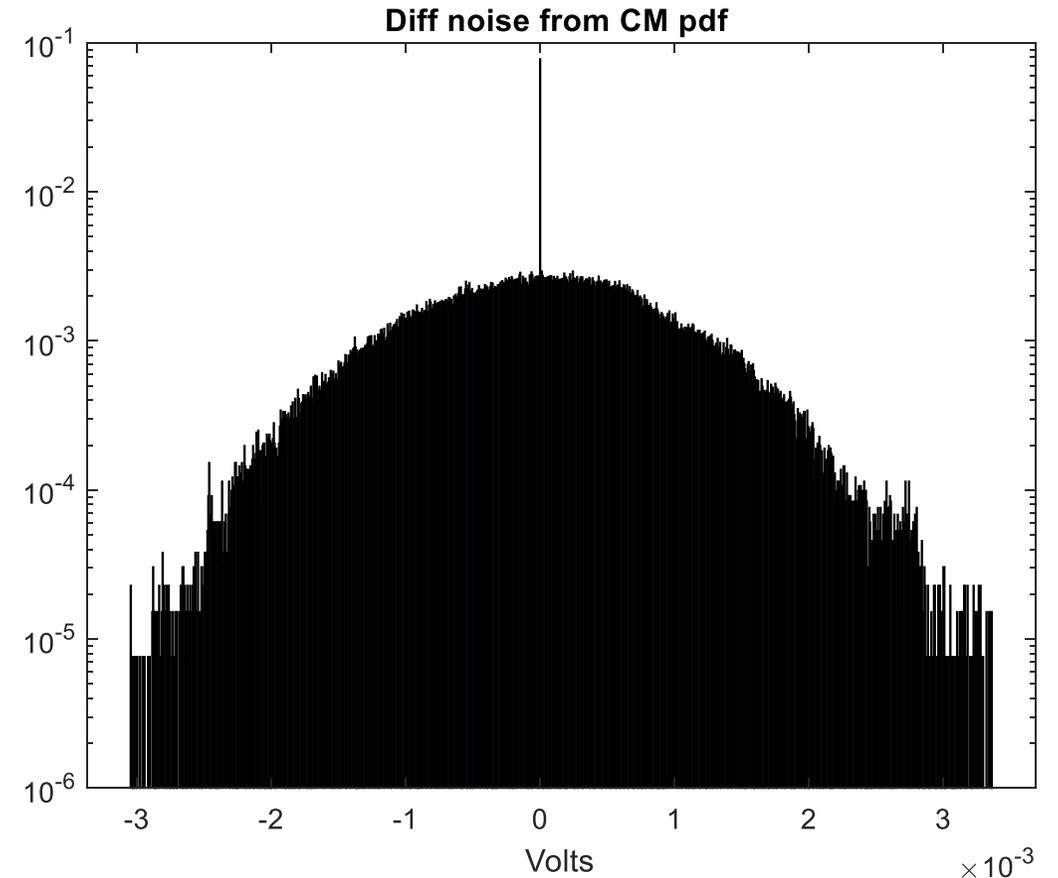
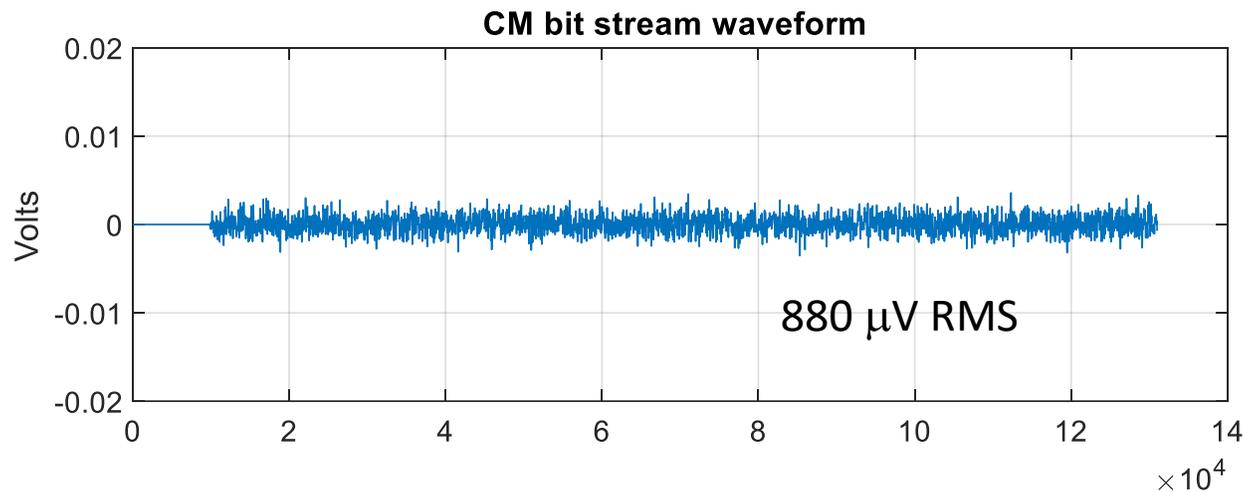
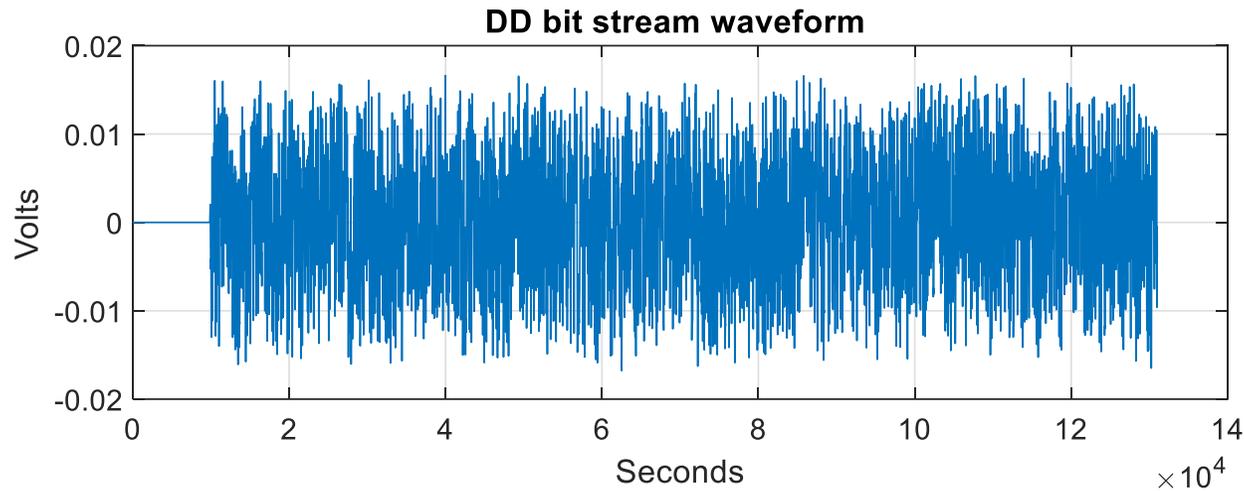
# 10 % p-n package components variation at Rx



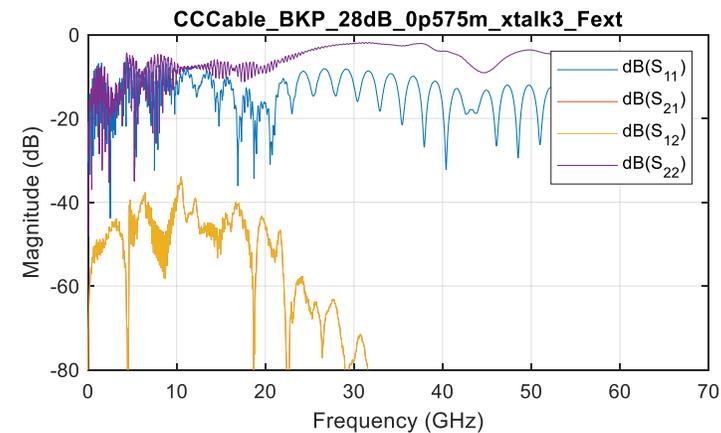
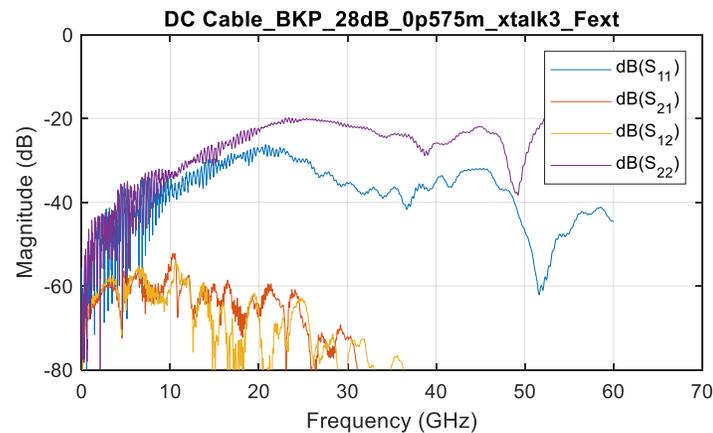
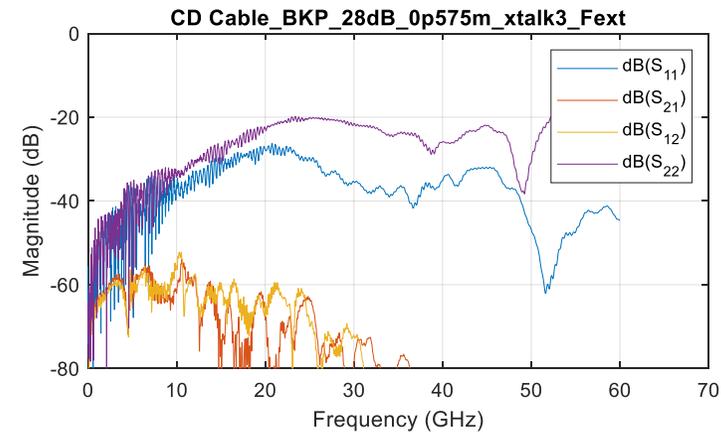
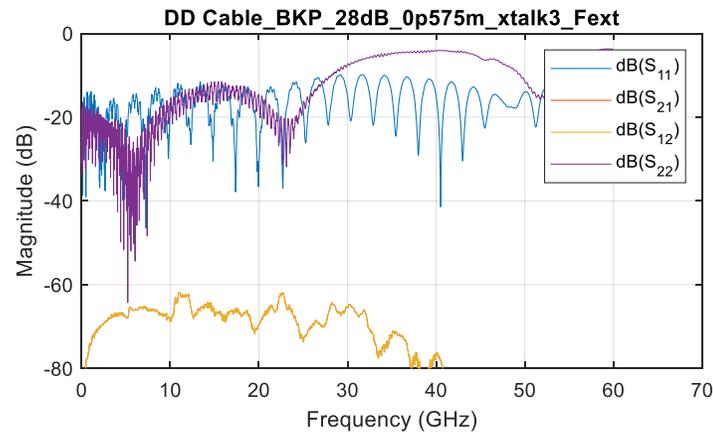
# 3.7 ps p-n skew



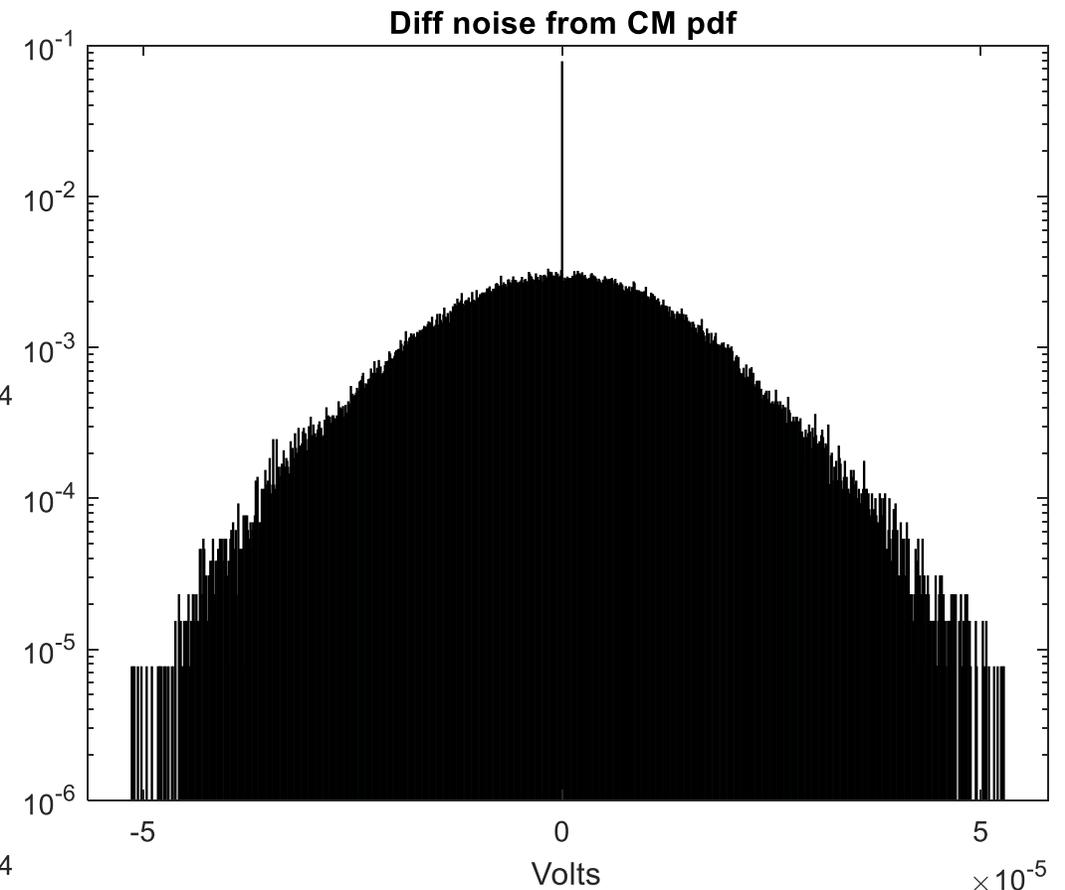
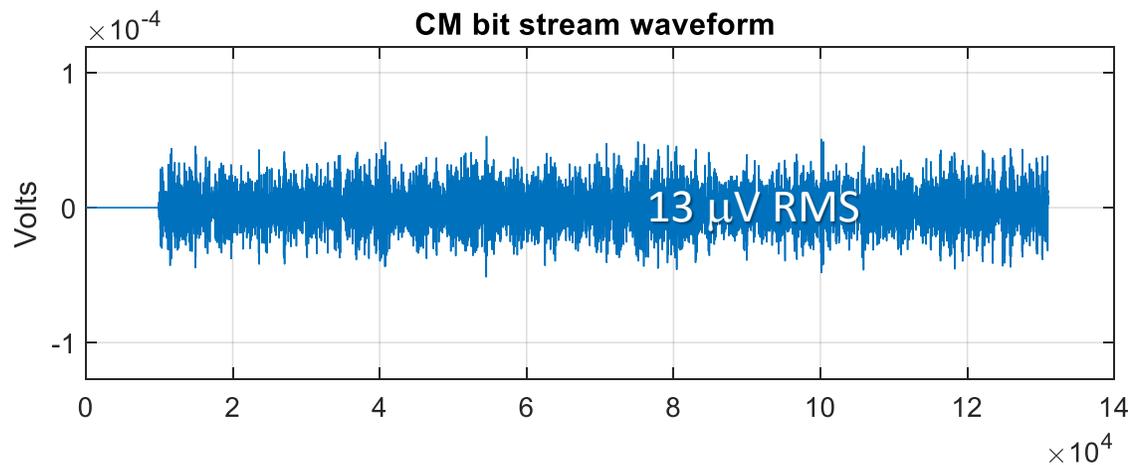
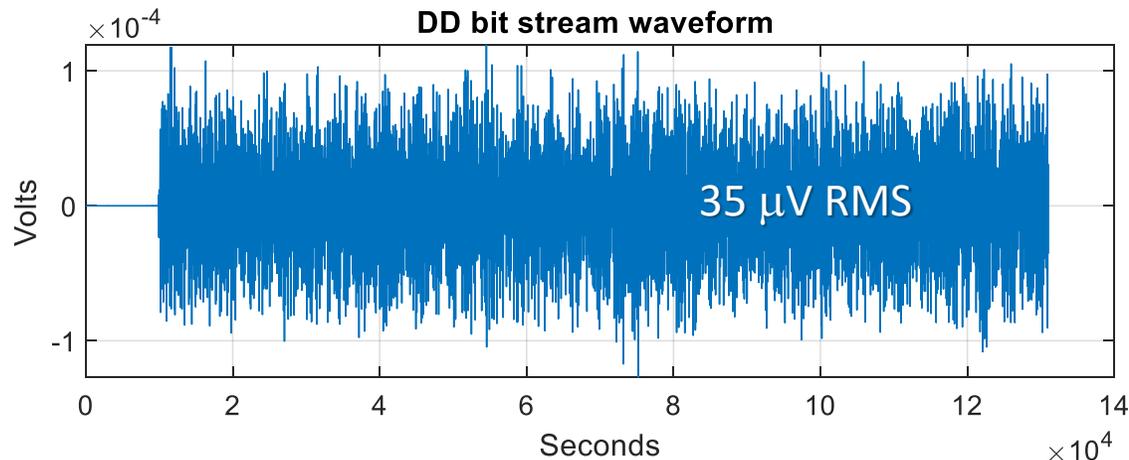
# Noise at Rx with 15 AC CM RMS at TP0 (Broad Band AWGN Source)



# Now let's look at 1 crosstalk file for the 28 dB channel



Crosstalk noise compared CM cause by 3.7 ps package skew, 10 % p-n package components , and 15 mV BBN (TPO)



# Actions Required

Measurement proposals

# Discussion: This all boils down to what is observable

- ❑ ran\_3ck\_04\_1020 suggests
  - Refine AC common mode measurements to separate correlated and uncorrelated components
- ❑ What could be considered
  - How small a CM voltage is reasonable?
  - Time sampled CM signal acquisition
    - What are the Attributes which are different from simulation
  - PDF of signal
    - DD or Modes
    - RMS
    - Instrument noise removal
  - Bandwidth filters
  - Pattern lock trigger or untriggered
  - New → Specify 95% confidence factor for the noise measurements.
    - This may mitigate instrument differences