

PSE MPS baseline v100

Change 33.2.9.1.2 as follows:

33.2.9.1.2 PSE DC MPS component requirements

A PSE shall consider the DC MPS component to be present if I_{Port} is greater than or equal to $I_{Hold\ max}$ for a minimum of T_{MPS} . A PSE shall consider the DC MPS component to be absent if I_{Port} is less than or equal to $I_{Hold\ min}$. A PSE may consider the DC MPS component to be either present or absent if I_{Port} is in the range of I_{Hold} .

Power shall be removed from the PI when DC MPS has been absent for a duration greater than T_{MPDO} .

The specification for T_{MPS} in Table 33–11 applies only to the DC MPS component. In addition to the requirements on T_{MPS} and T_{MPDO} in Table 33–11, the PSE shall use values for T_{MPS} and T_{MPDO} that meet Equation 33–7a.

$$T_{MPDO} - T_{MPS} \geq 250\text{ms} \quad (33-7a)$$

~~The PSE shall not remove power from the port when I_{Port} is greater than or equal to $I_{Hold\ max}$ continuously for at least T_{MPS} every $T_{MPS} + T_{MPDO}$, as defined in Table 33–11. This allows a PD to minimize its power consumption.~~

The PSE shall not remove power from the port when less than T_{MPDO} has passed since MPS was last present. This allows a PD to minimize its power consumption.

Add the new requirement to the PICS tables.