

Comments Received

IEEE P802.3cs D3.1 SuperPON Task Force 1st Sponsor recirculation ballot comments

CI **FM** SC **FM** P1 L 29 # **R1-5**
 Dawe, Piers J G NVIDIA
 Comment Type **E** Comment Status **X**
 physical layers
 SuggestedRemedy
 Physical Layers
 Also in 164.1, first sentence
 Proposed Response Response Status **O**

CI **56** SC **56.1.3** P38 L 24 # **R1-1**
 Dawe, Piers J G NVIDIA
 Comment Type **E** Comment Status **X**
 The standard order is by increasing speed (MAC rate) then increasing reach, then others.
 See e.g. comments to 802.3dc D3.0. The reader should not be expected to think in
 historical order.
 SuggestedRemedy
 Instead of inserting the new rows at the end of Table 56-1 (below 50GBASE-PQX-U3), I
 believe that 10/2.5GBASE-SP1-D and 10/2.5GBASE-SP1-U go between 10/1GBASE-PRX-
 U4 and 10GBASE-PR-D1, and 10GBASE-SP1-D and 10GBASE-SP1-U go between
 10GBASE-PR-U4 and 10PASS-TS-O (!)
 Proposed Response Response Status **O**

CI **56** SC **56.1.3** P38 L 32 # **R1-2**
 Dawe, Piers J G NVIDIA
 Comment Type **E** Comment Status **X**
 At over three pages long, Table 56-1 is far too long and cumbersome. While maintenance
 might take an interest in this, as this project is extending the table...
 SuggestedRemedy
 Split the table into three: point-to-point optical; copper and CCDN; PON.
 Proposed Response Response Status **O**

CI **56** SC **56.1.3** P38 L 47 # **R1-4**
 Dawe, Piers J G NVIDIA
 Comment Type **E** Comment Status **X**
 The standard order is by increasing speed (MAC rate) then increasing reach, then others. It
 helps the reader if the way things are done is similar for columns as for rows.
 SuggestedRemedy
 I believe the new column would best be between 10G-EPON P2MP MPMC and
 Nx25GBASE-PQ PMD
 Proposed Response Response Status **O**

CI **56** SC **56.1.3** P38 L 47 # **R1-3**
 Dawe, Piers J G NVIDIA
 Comment Type **E** Comment Status **X**
 The standard order is by increasing speed (MAC rate) then increasing reach, then others.
 See e.g. comments to 802.3dc D3.0. The reader should not be expected to think in terms
 of historical order.
 SuggestedRemedy
 Instead of inserting the new rows at the end of Table 56-3 (below 50GBASE-PQG-D3), I
 believe that 10/2.5GBASE-SP1-D and 10/2.5GBASE-SP1-U go between 10/1GBASE-PRX-
 U4 and 10GBASE-PR-D1, and 10GBASE-SP1-D and 10GBASE-SP1-U go between
 10GBASE-PR-U4 and 25/10GBASE-PQG-U2.
 Proposed Response Response Status **O**

CI **164** SC **164.2.9.10** P53 L 50 # **R1-6**
 Dawe, Piers J G NVIDIA
 Comment Type **E** Comment Status **X**
 I read "the Trx_settling time holds" and thought of setup-and-hold time. I expected to see
 some signal held for this time.
 SuggestedRemedy
 If this said "the Trx_settling time applies", that distracting thought would be avoided.
 Proposed Response Response Status **O**