

Chief Editor's report

Tom Issenhuth, Huawei, P802.3ct Chief Editor

IEEE P802.3ct Task Force, Salt Lake City, May 2019

Editorial team

Tom Issenhuth, Huawei

- Chief Editor and Editor for Clause 156

Pete Anslow, Ciena

- Editor for Clauses 00, 1, 30, 45, 78, 80, 82, 116, 119, Annex A

Steve Trowbridge, Nokia

- Editor for Clauses 152, 153, Annex 83C

Peter Stassar, Huawei

- Editor for Clause 154

John DeAndrea, Finisar

- Editor for Clause 155

Introduction

The IEEE P802.3ct project has adopted baselines for:

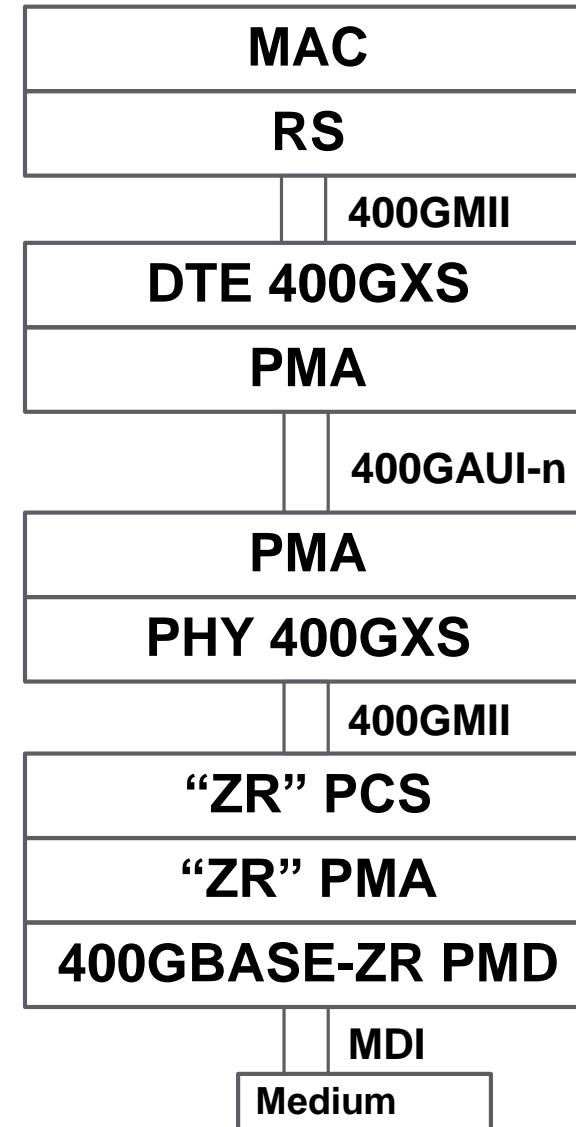
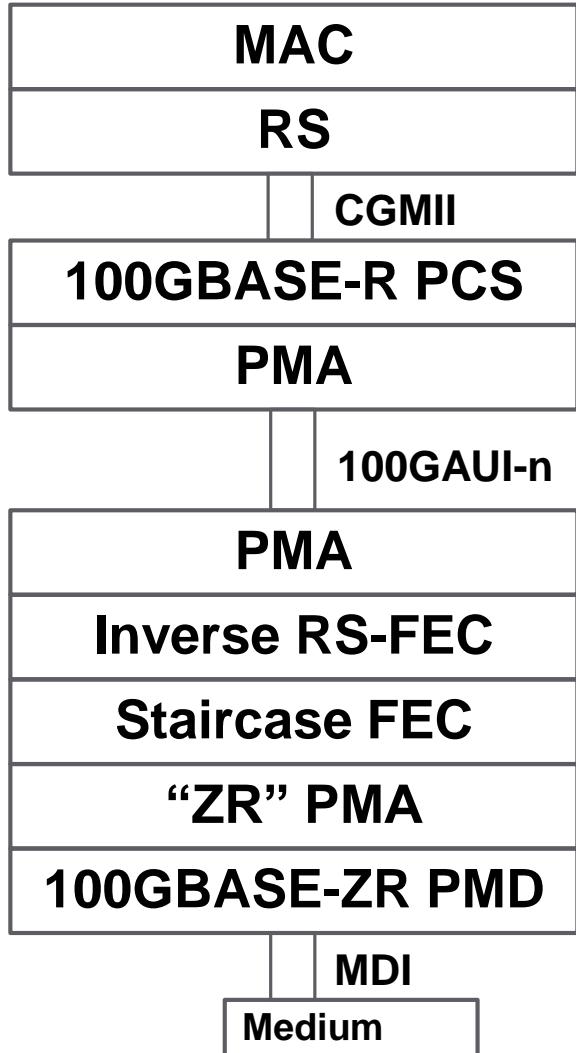
- 100GBASE-ZR FEC and frame format on slides 9 to16 of trowbridge_3cn_01a_0119
- Inverse RS-FEC sublayer on slide 7 of nicholl_3ct_01a_0319
- 400GBASE-ZR PCS/PMA in lyubomirsky_3cn_01b_0119

And has adopted the following modulation formats:

- DP-DQPSK modulation format for 100GBASE-ZR
- DP-16QAM modulation format for 400GBASE-ZR

This presentation sets out the expected structure of the P802.3ct amendment.

Expected stacks



New clauses

Clause	Content	Baseline
152	Inverse RS-FEC sublayer	Baseline adopted
153	Staircase FEC and PMA for 100GBASE-ZR	Baseline adopted
154	PMD clause for 100GBASE-ZR	Need baseline
155	PCS (including FEC) and PMA for 400GBASE-ZR	Baseline adopted
156	PMD clause for 400GBASE-ZR	Need baseline

Amended clauses

Clause	Change	
1	Add new references, definitions, abbreviations	
30	Add new management objects / attributes	
45	Add new registers / bits	
78	Add new EEE fast wake PHYs	
80	Add new 100G PHY type	
82	Move G.709 location	
116	Add new 400G PHY type	
119	Move G.709 location	
Annex A	Add any new bibliography entries	
Annex 83C	Add new figures	

Thanks!