

IEEE 802.3CY – BEYOND 10G ELECTRICAL AUTOMOTIVE ETHERNET PHY TF

MDI PARAMETERS

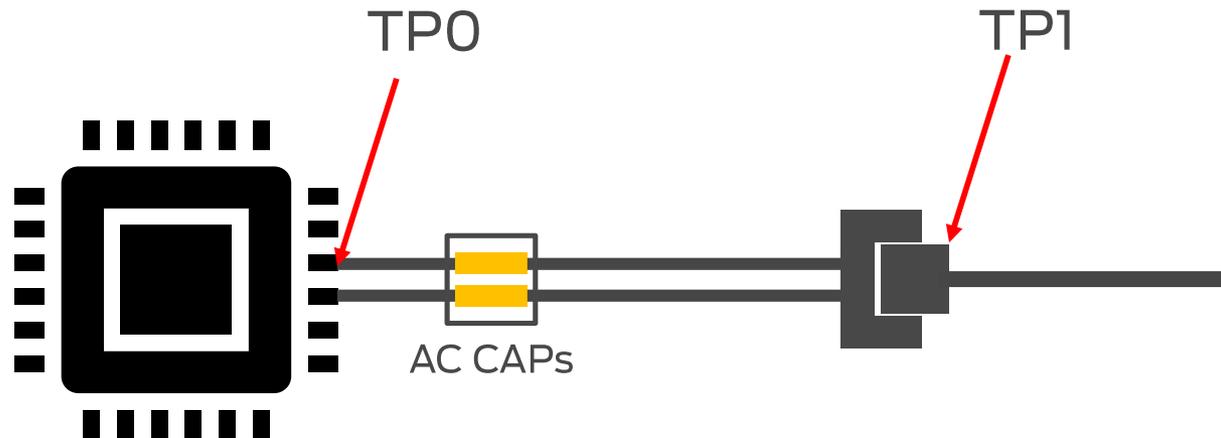
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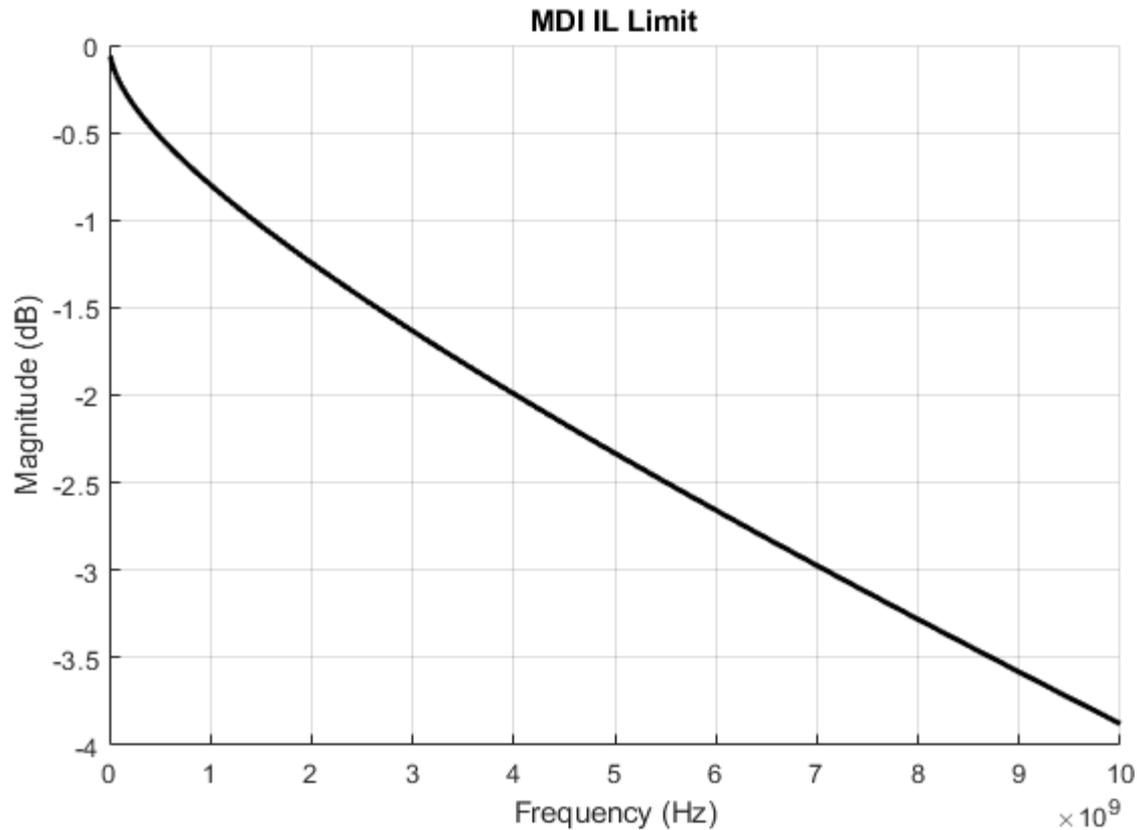
MDI Test Points



- These are the proposed TPs for the MDI
- Based on the analysis from [Kadry_3cy_01a_03_01_21.pdf](#), The recommended PCB insertion loss is 3dB @ 7031.25MHz from TP0 to TP1.
- TP0 is at the PMD output, IC pin and PCB solder pad
- TP1 is the output of the MDI connector. Measurements at this point will most likely require a test fixture

MDI IL Limit

This is a preliminary plot for the recommended MDI IL limit



$$IL = 0.2 * \sqrt{f(\text{GHz}) + 0.066 * f(\text{GHz})}$$

Discussion Points

- Should we specify TPs for the AC coupling and PoDL?
- With power requirements is PoDL still a feasible option to include for this specification?
- Need to specify MDI electrical parameters
- Do we specify test fixtures and test fixture performance?

Thank you!



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