

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 30 SC 30.5.1.1.2 P37 L44 # R1-27

Ran, Adee Cisco Systems, Inc.

Comment Type T Comment Status A labels

Following the response to comment I-43:

The changes to the entries for 200GBASE PHYs are not within the scope of this project, which is "for 400 Gb/s and 800 Gb/s Operation".

The changes to the entries for existing 400GBASE PHYs (400GBASE-DR4, 400GBASE-SR4, 400GBASE-SR4.2, 400GBASE-SR8, 400GBASE-SR16, and 400GBASE-VR4) should be reconsidered as they may affect existing implementations.

SuggestedRemedy

Delete the changes related to 200GBASE PHYs.

Consider deleting the changes to existing 400GBASE PHYs and making appropriate changes to the descriptions of new 400GBASE PHYs to distinguish them from existing ones instead..

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3df D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However the commenter raises a valid concern regarding potential side effects of changing legacy text that is not within the scope of the 802.3df project. Therefore the entries for the 200G and 400G PHYs (other than 400GBASE-DR4-2) should be reverted to be consistent with the base standard.

Delete the amendments related to the 200GBASE MAUs.

Delete the amendments related to the 400GBASE-DR4, 400GBASE-SR4, 400GBASE-SR4.2, 400GBASE-SR8, 400GBASE-SR16, and 400GBASE-VR4 MAUs.

Implement with editorial license.

Cl 31B SC 31B.4.6 P255 L50 # R1-12

Marris, Arthur Cadence Design Systems, Inc.

Comment Type T Comment Status A (bucket)

Need to add PICS item TIM17 for 800 Gbps

SuggestedRemedy

Add new PICS item at end of 31B.4.6

TIM17 Measurement point for station at 800 Gb/s 31B.3.7 Delay at MDI ≤ 1810 pause\_quanta Millp: M Yes

Response Response Status C

ACCEPT.

Cl 90A SC 90A.3 P251 L44 # R1-4

Marris, Arthur Cadence Design Systems, Inc.

Comment Type E Comment Status A (bucket)

There is crossed out text "Annex\_" that should not be there

SuggestedRemedy

Change "See Annex\_90A.3" to "See 90A.3" on line 44.

Response Response Status C

ACCEPT.

Cl 116 SC 116 P L # R1-30

Ran, Adee Cisco Systems, Inc.

Comment Type T Comment Status A (bucket)

The PHY type 400GBASE-DR4-2 introduced by this amendment is not listed in clause 116.

The following seem to require updates:

- 116.1.2 item h
- 116.1.3: Table 116-2
- 116.1.4: Table 116-5

SuggestedRemedy

Add Clause 116 into the amendment and add 400GBASE-DR4-2 in the locations listed in the comment, and elsewhere if required.

Response Response Status C

ACCEPT IN PRINCIPLE.

Clause 116 was in Draft 3.0 with the suggested amendments. However, Clause 116 was inadvertently deleted from the FrameMaker book for D3.1. Reinstate Clause 116 as it was in D3.0.

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 124 SC 124.1.1 P105 L9 # R1-31

Dawe, Piers J G NVIDIA  
 Comment Type E Comment Status A FLR

This sentence needs more work. At present, it says that if something is not good enough to achieve an end, something else has to be better than what's needed to achieve that unachievable end.  
 However, clarifying this may be out of scope.  
 pdf page 100, printed page 105

*SuggestedRemedy*

If the error statistics are not sufficiently random to meet the specified frame loss ratio for 64-octet frames with minimum interpacket gap \*when the BER is at the limit\*, then the BER shall be less than the value required to meet that frame loss ratio.

Response Response Status C

ACCEPT IN PRINCIPLE.

The referenced paragraph is difficult to parse as written.

Change the text as proposed on slide 5 of the following presentation.  
[https://www.ieee802.org/3/df/public/23\\_11/brown\\_3df\\_03\\_2311.pdf](https://www.ieee802.org/3/df/public/23_11/brown_3df_03_2311.pdf)

Implement with editorial license.

Cl 124 SC 124.5.4 P110 L11 # R1-32

Dawe, Piers J G NVIDIA  
 Comment Type T Comment Status R signal detect

The signal detect max could be defined better, considering that the same modules are used for 400GBASE-DR4-2 and 800GBASE-DR8-2 lanes and 100GBASE-FR1.  
 SD thresholds would be lower than 0.2 dB below spec-worst sensitivity, so it's OK to base the SD max on -7.1 while the average power min is -6.9 dBm.

*SuggestedRemedy*

For 400GBASE-DR4-2 and 800GBASE-DR8-2, change the SIGNAL\_DETECT Optical power at TP3 criterion from "average receive power, each lane (min) in Table 124-7" to >=-7.1 dBm.

Response Response Status C

REJECT.

The signal detect level points to the value specified for "average receive power, each lane (min)", so it scales with the "average receive power, each lane (min)" in Table 124-7.

There is no consensus to make the proposed change.

Cl 124 SC 124.5.4 P110 L12 # R1-33

Dawe, Piers J G NVIDIA  
 Comment Type T Comment Status R signal detect

At present an OMA-based signal detect is required to say OK for a signal at -6.9 dBm regardless of its extinction ratio, so a signal with  $-6.9-4.2+3 = -8.1$  dB OMA must be shown as OK when the intended minimum OMA at the receiver is  $-0.1-4 = -4.1$  dBm. (4.2 dB is the extinction ratio penalty for 3.5 dB). ("compliant 400GBASE-R or 800GBASE-R signal" is about signalling rate, scrambling and so on.)  
 The proposed remedy is based on -7.1 dB average power (see another comment).  
 Notice that "The PMD receiver is not required to verify whether a compliant 400GBASE-DR4 signal is being received", so the receiver may reject a signal that fails any of the three criteria without checking the other two.

*SuggestedRemedy*

For 400GBASE-DR4-2 and 800GBASE-DR8-2, SIGNAL\_DETECT should be OK when:  
 Optical power at TP3 >=-7.1 dBm; and  
 OMA at TP3 >= -4.3 dBm; and  
 compliant 400GBASE-R or 800GBASE-R signal input.

Response Response Status C

REJECT.

This comment does not apply to the substantive changes between IEEE P802.3df D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

The comment proposes an additional parameter (OMA) for signal detect that is not traditionally specified for similar PMDs.

There is no consensus to make the proposed change.

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 169 SC 169.1.2 P177 L41 # R1-1

Brown, Matthew Alphawave  
 Comment Type E Comment Status A (bucket2)

Figure 169-1 is relevant to any 800GBASE PHY, not just 800GBASE-R PHY types.

*SuggestedRemedy*

Under the medium block change "800GBASE-R" to "800GBASE".

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3df D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, there was consensus to make the proposed change.

Implement the suggested remedy.

Cl 169 SC 169.4 P184 L13 # R1-2

Brown, Matthew Alphawave  
 Comment Type E Comment Status A (bucket)

The units bit times and pause\_quanta are defined twice in this subclause. First in the opening paragraph and again in the table footnotes.

*SuggestedRemedy*

Change: "Table 169-4 contains the values of maximum delay (sum of transmit and receive delays at one end of the link) for each instance of a sublayer in bit times (as specified in 1.4.215) and pause\_quanta (as specified in 31B.2) for 800 Gigabit Ethernet."

To: Change: "Table 169-4 contains the values of maximum delay (sum of transmit and receive delays at one end of the link) for each instance of a sublayer."

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement the suggested remedy with editorial license.

Cl 169 SC 169.4 P184 L14 # R1-3

Brown, Matthew Alphawave  
 Comment Type E Comment Status A (bucket2)

For a description of bit times the paragraph points to the definition in 1.4.215 while the description of pause\_quanta points to a reference in 31B.2, even though there is a definition for pause\_quantum in 1.4.459 which refers to 31B.2.

*SuggestedRemedy*

Change the reference for pause\_quanta description from 31B.2 to 1.4.459.

Response Response Status C

ACCEPT.

Cl 169 SC 169.5 P185 L34 # R1-44

Dawe, Piers J G NVIDIA  
 Comment Type T Comment Status R skew point

D2.0 comment 96: 0.2 ns Skew Variation. This dates back to SFI-5 when it was 1.5 UI of "relative wander at up to 11.1 Gbps" (per lane, so 0.14 ns). It got rounded up to 0.2 ns or just over 2 UI "dynamic skew" (giannakopoulos\_01\_1108) which was unfortunate. At 53.125 GBd this is 11 UI and "dynamic skew buffer per input lane Size is 2x the max dynamic skew", so over 21 UI, very roughly four times the length of the 4-tap or 6-tap AUI equaliser.

*SuggestedRemedy*

Define SP0 as the first exposed AUI interface (nearest the PCS or PHY 800GXS). Recommend a max Skew Variation 0.1 ns or about 5 UI at 53.125 GBd there. Modify 173.5.3 accordingly.

Response Response Status C

REJECT.

This comment does not apply to the substantive changes between IEEE P802.3df D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

The current specification is consistent with multiple generations of Ethernet where this point was not explicitly specified. A PMA is required to tolerate the skew variation at SP1.

There is not a clear benefit to reducing the skew variation requirement at the proposed SP0. Also, this would impose a tighter specification on the PCS/PMA above.

There is no consensus to make the proposed changes.

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 169 SC 169.5 P187 L1 # R1-43

Dawe, Piers J G NVIDIA  
 Comment Type E Comment Status R (bucket2)

Empty lines

SuggestedRemedy

Removing the blank space at lines 1 and 25-26 should let the 169.6 FEC Degrade section fit on this page.

Response Response Status C

REJECT.

It is not necessary to retain an entire subclause on a single page.

When the draft is prepared for publication, the publication editors update the formatting of the entire draft as required.

There is no consensus to make the proposed changes.

Cl 169 SC 169.5 P187 L33 # R1-42

Dawe, Piers J G NVIDIA  
 Comment Type T Comment Status A (bucket)

I suspect that the "N/A" here was copied from Table 116-9 and dates from a time when there were 26.5625 GBd (50G) AUIs but not 53.125 GBd AUIs. Now that there are, the missing numbers should be filled in.

SuggestedRemedy

Change the three N/A to approx 11, 202, 213.  
 This should be done in Table 116-9 also, and a 53.125 GBd column should be added to Table 80-9 (both out of scope).

Response Response Status C

ACCEPT IN PRINCIPLE.

In Table 169-6 in the UI column, change the three "N/A" to 11, 202, and 213 with each value preceded by the approximation symbol like other rows in this column.

Cl 171 SC 171.1 P196 L35 # R1-45

Dawe, Piers J G NVIDIA  
 Comment Type ER Comment Status R (bucket2)

Layout

SuggestedRemedy

Set Figure 171-1 to float and save a page.

Response Response Status C

REJECT.

It is not necessary to save space in an electronic document.

When the draft is prepared for publication, the publication editors update the formatting of the entire draft as required.

There is no consensus to make the proposed change.

Cl 171 SC 171.3.3 P195 L36 # R1-14

Slavick, Jeff Broadcom Inc  
 Comment Type T Comment Status A (bucket)

The PHY 800GXS is the same as the 800GMII that is defined in Clause 170, so the wording is a bit odd. Follow the wording used in 172.1.5.1

SuggestedRemedy

Change "The service interface below the PHY 800GXS is defined as the 800GMII in Clause 170, with some exceptions and additional signals as follows:"  
 to  
 "The service interface below the PHY 800GXS is the 800GMII defined in Clause 170, with the following exceptions and additional signals:"

Response Response Status C

ACCEPT.

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 171 SC 171.6 P201 L21 # R1-34

Dawe, Piers J G NVIDIA  
 Comment Type T Comment Status A (bucket2)

The FEC degrade feature doesn't propagate FEC degrade conditions. It signals or reports them, and sometimes in the opposite direction, so the first "propagate" doesn't work. Is "all" telling us something (what?) or is it a rhetorical flourish? If the feature is present, it reports a lack of FEC degrade (nothing untoward detected) too.

SuggestedRemedy

Change the first sentence from "The FEC degrade feature provides the ability to detect degrade conditions at the RS-FEC decoder using FEC degrade detection and to propagate all detected FEC degrade conditions using FEC degrade signaling." to "The FEC degrade feature provides the ability to detect degrade conditions at the RS-FEC decoder using FEC degrade detection and to report FEC degrade conditions using FEC degrade signaling." If "all" is intentional, change it to "report all three possible types of FEC degrade condition". Same in 172.1.4.

Response Response Status C

ACCEPT IN PRINCIPLE.

Replace the first paragraph in 171.6 with the following:

"The FEC degrade feature provides the ability to detect and indicate a degrade condition at the RS-FEC decoder using FEC degrade detection, and to propagate the FEC degrade indication using FEC degrade signaling. The propagation of FEC degrade indications across PCS and XS is described in 116.6."

Make similar changes to the first paragraph in 172.1.4 and 169.6.

Implement with editorial license.

Cl 171 SC 171.8 P202 L44 # R1-28

Dudek, Michael Marvell  
 Comment Type E Comment Status A (bucket)

Sentence without a verb

SuggestedRemedy

Change "described" to "are described"

Response Response Status C

ACCEPT.

Cl 171 SC 171.8 P202 L44 # R1-41

Dawe, Piers J G NVIDIA  
 Comment Type E Comment Status A (bucket)

Missing verb

SuggestedRemedy

... are described

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolve using the response to comment R1-28.

Cl 172 SC 172.1.3 P211 L18 # R1-35

Dawe, Piers J G NVIDIA  
 Comment Type T Comment Status A (bucket2)

MDIO is optional. So is any management, usually, although "it is recommended that an equivalent access is provided" (172.3).

SuggestedRemedy

Change "and informing" to "and, optionally, informing"

Response Response Status C

ACCEPT IN PRINCIPLE.

This comment does not apply to the substantive changes between IEEE P802.3df D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

However, there was consensus to address this comment.

This list relates to the 800GMII. The statement in item j is not relevant to the 800GMII and thus should be deleted from this list.

Delete item j) from the list.

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 172 SC 172.1.5.1 P211 L47 # R1-21

Ran, Adeo Cisco Systems, Inc.  
 Comment Type E Comment Status A (bucket)

"The PCS service interface is the 800GMII in Clause 170" (twice, line 47 and line 50)

Similar references to xGMII clauses in the base document use the word "defined". For example see 149.3.1.

*SuggestedRemedy*

Change to "The PCS service interface is the 800GMII defined in Clause 170", twice.

Response Response Status C

ACCEPT IN PRINCIPLE.

Adding the word "defined" is a good change. However, It is not necessary to say "defined in Clause 170" twice in consecutive sentences.

Change the two sentences to the following :

"When the client sublayer is the Reconciliation Sublayer, the PCS service interface is the 800GMII defined in Clause 170.

When the client sublayer is the PHY 800GXS, the PCS service interface is the 800GMII with additional signals TXRD, TXLD, RXRD, RXLD and PCS\_status."

Cl 172 SC 172.1.5.1 P212 L1 # R1-22

Ran, Adeo Cisco Systems, Inc.  
 Comment Type E Comment Status A (bucket)

"The TXRD and TXLD status signals indicate..."

These are not referred to as "status signals" elsewhere. The subsequent two paragraphs describe RXRD and RXLD without the word "status".

The last paragraph has "The PCS\_status signal indicates..." but in this case "status" is part of the signal name - this adds confusion.

*SuggestedRemedy*

Change to "The TXRD and TXLD signals indicate..."

Response Response Status C

ACCEPT.

Cl 172 SC 172.2.4.6 P216 L38 # R1-23

Ran, Adeo Cisco Systems, Inc.  
 Comment Type T Comment Status A (bucket2)

"tx\_am\_sf<2:0> = {FEC\_degraded\_SER + rx\_local\_degraded,0,0}"

The "+" sign apparently means logical-or here, but it is used in two other places in this subclause and in Figure 172-3 with the meaning of numerical addition. It can also be interpreted as addition modulo 2 (XOR) as used in other contexts.

The text should be made unambiguous.

Also applies to 171.6.1, although there are no additional + signs there.

*SuggestedRemedy*

Add "and + denotes logical OR" after "where FEC\_degraded\_SER and rx\_local\_degraded are defined in 172.2.6.2.2".

Add a similar statement in 171.6.1, including references to the variable definitions in 172.2.6.2.2.

Response Response Status C

ACCEPT IN PRINCIPLE.

In 172.2.4.6...

Change "where FEC\_degraded\_SER and rx\_local\_degraded are defined in 172.2.6.2.2" To "where FEC\_degraded\_SER and rx\_local\_degraded are defined in 172.2.6.2.2 and + denotes logical OR"

In 171.6.1, add the following statement:

"where FEC\_degraded\_SER and rx\_local\_degraded are defined in 172.2.6.2.2 and + denotes logical OR" after tx\_am\_sf<2:0> = {FEC\_degraded\_SER + rx\_local\_degraded,0,0}

Implement with editorial license.

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 172 SC 172.2.4.6 P216 L49 # R1-36  
 Dawe, Piers J G NVIDIA  
 Comment Type E Comment Status A (bucket2)  
 Font size  
 SuggestedRemedy  
 Fix  
 Response Response Status C  
 ACCEPT IN PRINCIPLE.  
 The font size of the cross-references is larger than text in the rest of the sentence.  
 Fix the font formatting in the note with editorial license.

Cl 172 SC 172.2.5.2 P221 L12 # R1-24  
 Ran, Adee Cisco Systems, Inc.  
 Comment Type T Comment Status R (bucket)  
 "Within a flow, the data from the 16 PCS lanes is de-interleaved to reconstruct the original two streams of FEC codewords"  
 The similar statement in 119.2.5.2 is "the two FEC codewords are de-interleaved to reconstruct the original stream of two FEC codewords". And indeed this is a single stream of (pairs of) codewords, not two (independent) streams, that should be reconstructed.  
 The wording of 119.2.5.2 may be improved by changing "the original stream of two FEC codewords" to "the original stream of FEC codewords", or alternatively "of FEC codeword pairs" if the CRG prefers.  
 SuggestedRemedy  
 Change "Within a flow, the data from the 16 PCS lanes is de-interleaved to reconstruct the original two streams of FEC codewords" to "Within a flow, the data from the 16 PCS lanes is de-interleaved to reconstruct the original stream of FEC codewords".  
 Response Response Status C  
 REJECT.  
 The text is accurate as written. The data is broken into two streams, one for each FEC decoder.  
 There is no consensus to make the proposed changes.

Cl 172A SC 172A P282 L30 # R1-26  
 Ran, Adee Cisco Systems, Inc.  
 Comment Type E Comment Status A (bucket)  
 "the variable tx\_scrambled\_am is produced as shown in Table 172A-1 for flow 0 and Table 172A-4 for flow 1"  
 and then  
 "The expanded codewords are shown in Table 172A-2 and Table 172A-3 for flow 0, and in Table 172A-5 and Table 172A-6 for flow 1"  
 This annex would be easier to read and follow if the order of the tables was such that tables 172A-1 and 172A-4 appear first, right after the text that describes them, followed by the text that describes the remaining tables, and the remaining tables. All tables would be renumbered accordingly.  
 SuggestedRemedy  
 Re-order the tables and the text per the comment.  
 Response Response Status C  
 ACCEPT IN PRINCIPLE.  
 The suggested remedy makes the table numbering consistent with the description.  
 Implement the suggested remedy with editorial license.

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 172A SC 172A P282 L51 # R1-39

Dawe, Piers J G NVIDIA  
 Comment Type TR Comment Status R (bucket2)

Experience with Annex 172A shows us how valuable it is. But more complexity follows: twice "Mux and 10-bit symbol distribution" as in 119.2.4.8 Figure 119-11 (with an order reversal that doesn't seem to be mentioned in the text), then 32:8 bit mux as in 173.5.2.1 where the two flows get interleaved, which is a new thing and worth an example.

SuggestedRemedy

Show some of the 16+16-lane output of the PCS for these cxA and cxB. It may be enough to show e.g. the beginnings of lanes 1 and 31, enough to include some differences between four codewords.

Also show some of the 8-lane output of an 32:8 bit mux from that (which could go in a NOTE in 173). Again, showing a couple of lanes would be enough to resolve most or all misinterpretations or ambiguities. Add a cross-reference from here.

If only a few hundred bits are needed, it could go in text. But if a more complete example is preferred, tables could be added and plain-text equivalents uploaded.

Response Response Status U

REJECT.

The example patterns are provided to help the implementer confirm correct interpretation of the encoding functionality which is complex.

Figure 119-11 provides sufficient guidance to correctly implement "Mux and 10-bit symbol distribution". Therefore adding the suggested additional patterns is not necessary.

There is no consensus to make the proposed changes.

Cl 172A SC 172A P287 L11 # R1-37

Dawe, Piers J G NVIDIA  
 Comment Type E Comment Status A (bucket)

A more specific reference would make this annex easier to use

SuggestedRemedy

Change cross-reference from Clause 172 to 172.2.4

Response Response Status C

ACCEPT.

Cl 172A SC 172A P287 L24 # R1-18

Slavick, Jeff Broadcom Inc  
 Comment Type T Comment Status R (bucket2)

Just before "the" 257-bit block was scrambled is not quite correct since it doesn't truly specify which of the 32 257-bit blocks in each flow the seeds applies to, but it is the first one

SuggestedRemedy

Change: "just before the 257-bit block was scrambled"  
 To: "prior to scrambling the first 257-bit block"

Response Response Status C

REJECT.

[Editor's note: page was changed from 282 to 287]

The point at which the initial seed is applied is implicit. This wording is consistent with the wording Annex 119A. It is not necessary to make the proposed change.

There is no consensus to make the proposed change.

Cl 172A SC 172A P287 L30 # R1-19

Slavick, Jeff Broadcom Inc  
 Comment Type T Comment Status R (bucket2)

The scrambling and mapping processes have produced a state of the tx\_scrambled\_am variable which are shown in the tables.

SuggestedRemedy

Change: "the variable tx\_scrambled\_am is produced as shown in "  
 To: "the state of the variable tx\_scrambled\_am is shown in"

Response Response Status C

REJECT.

[Editor's note: page was changed from 282 to 287]

This wording is consistent with the wording Annex 119A. The wording is sufficiently clear to be understood. It is not necessary to make the proposed change.

There is no consensus to make the proposed change.

## IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 172A SC 172A P287 L50 # R1-38

Dawe, Piers J G

NVIDIA

Comment Type E Comment Status A tables

These valuable tables are easier to use in plain text format. D3.0 comment 107 "Please prepare a plain-text file with the large tables for convenient reading into a program, and post it on the project web site for review with future drafts". Files have been made available.

*SuggestedRemedy*

Upload the text files, eventually to <https://standards.ieee.org/downloads/802.3/>, and include a NOTE here bringing them to the reader's attention.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolve using response to comment R1-13.

Cl 172A SC 172A P288 L4 # R1-20

Marris, Arthur

Cadence Design Systems, Inc.

Comment Type T Comment Status A tables

There are errors in the "tx\_scrambled\_am i:j Flow <f>" table values.

My understanding is that the values in the tables incorrectly used the following coding:

For all k=0 to 11

For all j=0 to 7

if even(k)

am\_mapped<160k+20j+ 9:160k+20j > = am\_{2j }<10k+9:10k>

am\_mapped<160k+20j+19:160k+20j+10> = am\_{2j+1}<10k+9:10k>

else

am\_mapped<160k+20j+ 9:160k+20j > = am\_{2j+1}<10k+9:10k>

am\_mapped<160k+20j+19:160k+20j+10> = am\_{2j }<10k+9:10k>

when it should have used the following coding:

For all k=0 to 11

For all j=0 to 7

if even(k)

am\_mapped<160k+20j+ 9:160k+20j > = am\_{2j }<10k+9:10k>

am\_mapped<160k+20j+19:160k+20j+10> = am\_{2j+1}<10k+9:10k>

else

am\_mapped<160k+20j+19:160k+20j+10> = am\_{2j+1}<10k+9:10k>

am\_mapped<160k+20j+ 9:160k+20j > = am\_{2j }<10k+9:10k>

*SuggestedRemedy*

Please correct the example coding tables in Annex 172A

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolve using response to comment R1-13.

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 172A SC 172A P288 L10 # R1-29

Nicholl, Shawn Advanced Micro Devices (AMD)

Comment Type T Comment Status A tables

There are errors in "Table 172A-1 - Example tx\_scrambled with alignment marker group for 800GBASE-R PCS flow 0" table values, specifically rows 2-8. The errored values differ from the expected values based on 119.2.4.4.2.

Similar errors exist in "Table 172A-4 - Example tx\_scrambled with alignment marker group for 800GBASE-R PCS flow 1" table values.

*SuggestedRemedy*

A presentation is expected that provides the correct values.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolve using response to comment R1-13.

Cl 172A SC 172A P288 L10 # R1-40

Dawe, Piers J G NVIDIA

Comment Type TR Comment Status A tables

Improved tx\_scrambled\_am tables and text files are available

*SuggestedRemedy*

Use the improved tables and text files

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolve using response to comment R1-13.

Cl 172A SC 172A P288 L10 # R1-13

He, Xiang Huawei Technologies Co., Ltd

Comment Type T Comment Status A tables

There were errors for AM portion in tx\_scrambled\_am<i>j</i> tables for both flows. To be more precise, row 2-8 (<257:2055>) of Table 172A-1 and 172A-4.

*SuggestedRemedy*

Change the AM portion in rows 2-8 of Table 172A-1 and Table 172A-4 to the correct values as shown in the contribution discussed during the .3dj & .3df joint ad hoc on Nov. 2.

Response Response Status C

ACCEPT IN PRINCIPLE.

Resolve using updated values in slide 5 of contribution:

[https://www.ieee802.org/3/dj/public/adhoc/optics/1023\\_OPTX/he\\_3dj\\_optx\\_01\\_231102.pdf](https://www.ieee802.org/3/dj/public/adhoc/optics/1023_OPTX/he_3dj_optx_01_231102.pdf)

Also, add a reference to the following text files using the same approach used in 172.2.4.6:

[https://www.ieee802.org/3/df/public/23\\_11/he\\_3df\\_02\\_2311.txt](https://www.ieee802.org/3/df/public/23_11/he_3df_02_2311.txt)

[https://www.ieee802.org/3/df/public/23\\_11/he\\_3df\\_03\\_2311.txt](https://www.ieee802.org/3/df/public/23_11/he_3df_03_2311.txt)

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 173 SC 173.2 P237 L8 # R1-25

Ran, Adeo Cisco Systems, Inc.  
 Comment Type T Comment Status A (bucket)

"The 8:32 and 8:8 PMAs may optionally provide signal status information to the PMA client by disabling (squelching) one or more of the PAM4 symbol streams sent to the PMA client (PMA:IS\_UNITDATA\_0:7.indication), see 173.5.8.2 and 173.5.8.3

This sentence is technically inaccurate - it is the output lane (AUI transmitter) that is squelched, not the PAM4 symbol streams; a squelched transmitter does not correspond to any PAM4 symbol stream. Indeed, the text in 173.5.8.2 and 173.5.8.3 uses different wording.

It is also is not directly related to the subject of this subclause, PMA service interface. Since signal detect is defined in other subclauses, this level of detail is not necessary here.

Similarly for the 4th paragraph in 173.3.

SuggestedRemedy

In 173.2, change the quoted sentence to "The 8:32 and 8:8 PMAs may optionally provide signal status information to the PMA client as described in 173.5.8.2 and 173.5.8.3" and make it a separate paragraph.

In the 4th paragraph of 173.3, change "the 8:8 PMA may optionally provide signal status information by disabling (squelching) one or more of the PAM4 symbol streams sent to the sublayer below via PMA:IS\_UNITDATA\_0:7.request (see 173.5.8.3)" to "the 8:8 PMA may optionally provide signal status information to the sublayer below as described in 173.5.8.3".

Response Response Status C  
 ACCEPT IN PRINCIPLE.

In Clause 173.2...  
 Change "The 8:32 and 8:8 PMAs may optionally provide signal status information to the PMA client by disabling (squelching) one or more of the PAM4 symbol streams sent to the PMA client (PMA:IS\_UNITDATA\_0:7.indication), see 173.5.8.2 and 173.5.8.3."  
 To "The 8:32 and 8:8 PMAs may optionally provide signal status information to the PMA client as described in 173.5.8.2 and 173.5.8.3"  
 And make this a new paragraph.

In Clause 173.3...  
 Change "For the 8:8 PMA, if the sublayer below the PMA is another PMA, the 8:8 PMA may optionally provide signal status information by disabling (squelching) one or more of the PAM4 symbol streams sent to the sublayer below via PMA:IS\_UNITDATA\_0:7.request (see 173.5.8.3)."

To "The 8:8 PMA may optionally provide signal status information to the sublayer below as described in 173.5.8.3"

Implement with editorial license.

Cl 173 SC 173.4.1 P239 L1 # R1-46

Dawe, Piers J G NVIDIA  
 Comment Type ER Comment Status R (bucket2)

Possibly, removing the blank line 1 and reducing the figure at lines 9-10...

SuggestedRemedy

would let it fit on the previous page with its subclause text.

Response Response Status C  
 REJECT.

This comment does not apply to the substantive changes between IEEE P802.3df D3.0 and D3.1 or the unsatisfied negative comments from previous drafts. Hence it is not within the scope of the recirculation ballot.

There is no consensus to make the proposed changes.

Cl 173 SC 173.5.2.1 P242 L15 # R1-15

Slavick, Jeff Broadcom Inc  
 Comment Type T Comment Status A (bucket)

In 173.4.1 we state that the Tx bit multiplexing function is restricted and Rx is unrestricted for the 32:8 PMA. In 173.5.2.1 we state the PMA provides bit-multiplexing for Tx and Rx and then repeat the transmit bit-multiplex is done over these lanes and then magically convert from general bit-multiplexing phrase to "restricted bit multiplexing".

SuggestedRemedy

In the second paragraph. Change "The restricted bit-level multiplexing function is identical" To: "This is a restricted bit-level multiplexing function that is identical"  
 In the third paragraph. Change "The unrestricted bit-level multiplexing function is identical" To: "This is an unrestricted bit-level multiplexing function that is identical"

Response Response Status C  
 ACCEPT IN PRINCIPLE.

Implement suggested remedy with editorial license.

[Editor's note: page was changed from 237 to 242]

IEEE P802.3df D3.1 1st Sponsor recirculation ballot comments

Cl 173 SC 173.5.2.2 P242 L37 # R1-16

Slavick, Jeff Broadcom Inc

Comment Type T Comment Status A (bucket)

In 173.4.2 we state that the Tx bit multiplexing function is unrestricted and Rx is restricted for the 8:32 PMA. In 173.5.2.2 we state the PMA provides bit-multiplexing for Tx and Rx and then repeat the transmit bit-multiplex is done over these lanes and then magically convert from general bit-multiplexing phrase to "unrestricted bit multiplexing".

*SuggestedRemedy*

In the second paragraph. Change "The unrestricted bit-level multiplexing function is identical"

To: "This is an unrestricted bit-level multiplexing function that is identical"

In the third paragraph. Change "The restricted bit-level multiplexing function is identical"

To: "This is a restricted bit-level multiplexing function that is identical"

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement suggested remedy with editorial license.

[Editor's note: page was changed from 237 to 242]

Cl 173 SC 173.5.2.3 P243 L15 # R1-17

Slavick, Jeff Broadcom Inc

Comment Type T Comment Status A (bucket)

In 173.4.3 we state that the Tx and Rx bit multiplexing function is restricted for the 8:8 PMA. In 173.5.2.3 we state the PMA provides bit-multiplexing for Tx and Rx and then state transmit bit-multiplex is done over these lanes and then magically convert from general bit-multiplexing phrase to "restricted bit multiplexing".

*SuggestedRemedy*

In the third paragraph. Change "The restricted bit-level multiplexing function is identical"

To: "This is a restricted bit-level multiplexing function that is identical"

Response Response Status C

ACCEPT IN PRINCIPLE.

Implement suggested remedy with editorial license.

[Editor's note: page was changed from 238 to 243]

Cl 173 SC 173.5.4 P244 L37 # R1-5

Brown, Matthew Alphawave

Comment Type E Comment Status R (bucket2)

Reference to "169.4 and its references" is unnecessarily verbose.

*SuggestedRemedy*

Change "169.4 and its references" to "169.4".

Response Response Status Z

REJECT.

This comment was WITHDRAWN by the commenter.