

IEEE P802.3df Architecture and Logic April Ad Hoc Meetings Report

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Architecture and Logic Ad Hoc Chair

Report

- Charter: The Architecture and Logic Ad Hoc is chartered to address the following:
 - 1) Develop terminology and definitions for terms related to architecture, including but not limited to FEC Architecture, FEC Scheme, Endto-end FEC, Segmented FEC, Concatenated FEC. Draft definitions to be presented to Task Force at May 2022 session.
 - 2) Act as forum to discuss architectural requirements and consider proposals related to PCS, FEC, and PMA logic sublayers.
- 2 ad hoc calls since July 2018 meeting
 - 11 April 2022, 25 April 2022
 - 50+ attendees
 - 6 contributions presented
- Meeting minutes and presentation materials:
<https://www.ieee802.org/3/df/public/adhoc/logic/index.html>
- Next meeting: June 23rd

Presentations

- 11 April 2022
 - To Reach a Holistic FEC Architectures of 800G/1.6TbE - Xinyuan Wang, Xiang He
 - 200G/Lane AUI FEC Proposal – Mark Gustlin
 - Consideration of Implementations and Architecture – John D’Ambrosia
- 25 April 2022
 - Stateless 64B/66B coding - Eugene Opsanick
 - FEC architecture and performance investigation for 800GbE and 1.6TbE – Yuchun Lu, Liang Li, Yan Zhuang, Huawei Technologies
 - End to segmented FEC – Brian Welch, Mark Gustlin

Straw Polls

- No straw polls for these sessions

THANKS!