Cl **30** *SC* **30..5.1.1.2** *P* **18** *L* **10** # 15

Simms, William NVIDIA

Comment Type **E** *Comment Status* **X**

aMAUType

SuggestedRemedy

Change from aMAUType to: MAU Type

Proposed Response *Response Status* **O**

Cl **45** *SC* **45.2.1.145a** *P* **24** *L* **7** # 2

Dawe, Piers Nvidia

Comment Type **E** *Comment Status* **X** *Late*

Some interfaces have mandatory precoding ability without this bit. This is likely to cause confusion unless explained.

SuggestedRemedy

Similarly to 45.2.1.117.7a, insert: This register applies to 100GBASE-BRx.

Proposed Response *Response Status* **O**

Cl **45** *SC* **45.2.1.145a** *P* **24** *L* **23** # 16

Simms, William NVIDIA

Comment Type **E** *Comment Status* **X**

order of the bits should be 1.607.1 and then 1.607.0.

SuggestedRemedy

Suggest to flip 45.2.1.145a.1 and 45.2.1.145a.2 to make the order 1.607.1 followed by 1.607.0 to be consistent with other parts of the spec

Proposed Response *Response Status* **O**

Cl **56** *SC* **56.1.3** *P* **26** *L* **1** # 3

Dawe, Piers Nvidia

Comment Type **E** *Comment Status* **X** *Late*

802.3cp has been absorbed into 802.3-2022

SuggestedRemedy

Delete "(as modified by IEEE Std 802.3cp-2022)" in 3 places

Proposed Response *Response Status* **O**

Cl **56** *SC* **56.1.3** *P* **26** *L* **11** # 4

Dawe, Piers Nvidia

Comment Type **E** *Comment Status* **X** *Late*

Two ... fiber

SuggestedRemedy

Two ... fibers (3 times) as in the base standard

Proposed Response *Response Status* **O**

Cl **56** *SC* **56.1.3** *P* **30** *L* **29** # 5

Dawe, Piers Nvidia

Comment Type **E** *Comment Status* **X** *Late*

If this table includes 161 RS-FEC-Int

SuggestedRemedy

Perhaps it should include 152 Inverse RS-FEC also

Proposed Response *Response Status* **O**

Cl **80** *SC* **80.1.5** *P* **34** *L* **20** # 6

Dawe, Piers Nvidia

Comment Type **E** *Comment Status* **X** *Late*

Some of what is shown in blue in the diff version seems to be the same as in D2.2.

SuggestedRemedy

Proposed Response *Response Status* **O**

Cl **91** SC **91.7.4.1** P **42** L **10** # 7

Dawe, Piers — Nvidia

Comment Type **E** *Comment Status* **X** *Late*

TF10 has KR4 not KP4

SuggestedRemedy

Change KP4 back to KR4, once

Proposed Response *Response Status* **O**

Cl **135** SC **135.5.7.2** P **44** L **24** # 17

Simms, William — NVIDIA

Comment Type **ER** *Comment Status* **X**

typo: 1/(1+D) mode 4

SuggestedRemedy

substitute 1/(1+D) mod 4

Proposed Response *Response Status* **O**

Cl **135** SC **135.6** P **5287** L # 8

Dawe, Piers — Nvidia

Comment Type **E** *Comment Status* **X** *Late*

In Table 135-3, MDIO/PMA status variable mapping

SuggestedRemedy

Insert new rows:
MDIO status variable PMA/PMD register name Register/bit number PMA status variable
PMA Rx precoding ability PMA precoding ability 1.607.1 Rx_precoding_ability
PMA Tx precoding ability PMA precoding ability 1.607.0 Tx_precoding_ability

Proposed Response *Response Status* **O**

Cl **161** SC **161** P **52** L **1** # 9

Dawe, Piers — Nvidia

Comment Type **E** *Comment Status* **X** *Late*

Should say that Clause 161 was added by 802.3ck

SuggestedRemedy

Proposed Response *Response Status* **O**

Cl **161** SC **161** P **52** L **10** # 10

Dawe, Piers — Nvidia

Comment Type **E** *Comment Status* **X** *Late*

stauts

SuggestedRemedy

status

Proposed Response *Response Status* **O**

Cl **168** SC **168.7.5** P **65** L **8** # 11

Dawe, Piers — Nvidia

Comment Type **T** *Comment Status* **X** *Late*

This says "there's a proposal to add the maximum tap weight for the tap immediately after the largest tap: max 0.07 in CL 168.7.5.". chayeb_3dj_01_2505 slide 8 shows that a very asymmetric signal can pass all the specs and still be troublesome to receive. P802.3dj D2.0 comment 392 proposes "The absolute difference between c(-1) and c(1) shall be less than 0.3". However, ordinary filtering effects (pulses decay slower than they build up) can cause the optimum setting for the tap immediately after the largest tap to be more negative than the one immediately before; this is expected. Having the tap before at -0.2 and the tap after at +0.1 would be more undesirable than the reverse.

SuggestedRemedy

Add two specs:
Tap weight for the tap immediately after the largest tap: max 0.08. (Typically this tap would be -ve)
-0.3 <= (tap after - tap before) <= 0.15

Proposed Response *Response Status* **O**

Cl **168** *SC* **168.10.1** *P* **72** *L* **7** # 12

Dawe, Piers Nvidia

Comment Type **E** *Comment Status* **X** *Late*

Note b about dispersion doesn't relate to the insertion loss row.

SuggestedRemedy

Remove the b after Channel insertion loss a,

Proposed Response *Response Status* **O**

Cl **Content** *SC* **Contents** *P* **13** *L* **3** # 1

Dawe, Piers Nvidia

Comment Type **E** *Comment Status* **X** *Late*

Layout

SuggestedRemedy

Missing white space after each clause number

Proposed Response *Response Status* **O**

Cl **Introdu** *SC* **Introduction** *P* **12** *L* **3** # 18

Lewis, Jon Dell Technologies

Comment Type **ER** *Comment Status* **X**

The first line states that Amendment 11 "adds Clause ." but doesn't include the clause

SuggestedRemedy

Change the end of the first sentence to "adds Clause 168."

Proposed Response *Response Status* **O**

Cl **TOC** *SC* **TOC** *P* **13** *L* **12** # 14

Simms, William NVIDIA

Comment Type **E** *Comment Status* **X**

Possible Typo aMAUType

SuggestedRemedy

Change from aMAUType to: MAU Type

Proposed Response *Response Status* **O**

Cl **TOC** *SC* **TOC** *P* **14** *L* **24** # 13

Simms, William NVIDIA

Comment Type **ER** *Comment Status* **X**

Typo, no space between "networksPhysical"

SuggestedRemedy

Change to "networks Physical" Medium Attachment...

Proposed Response *Response Status* **O**